



# SPI 2016

20th IEEE Workshop on Signal and Power Integrity

8-11 May 2016, Turin, Italy

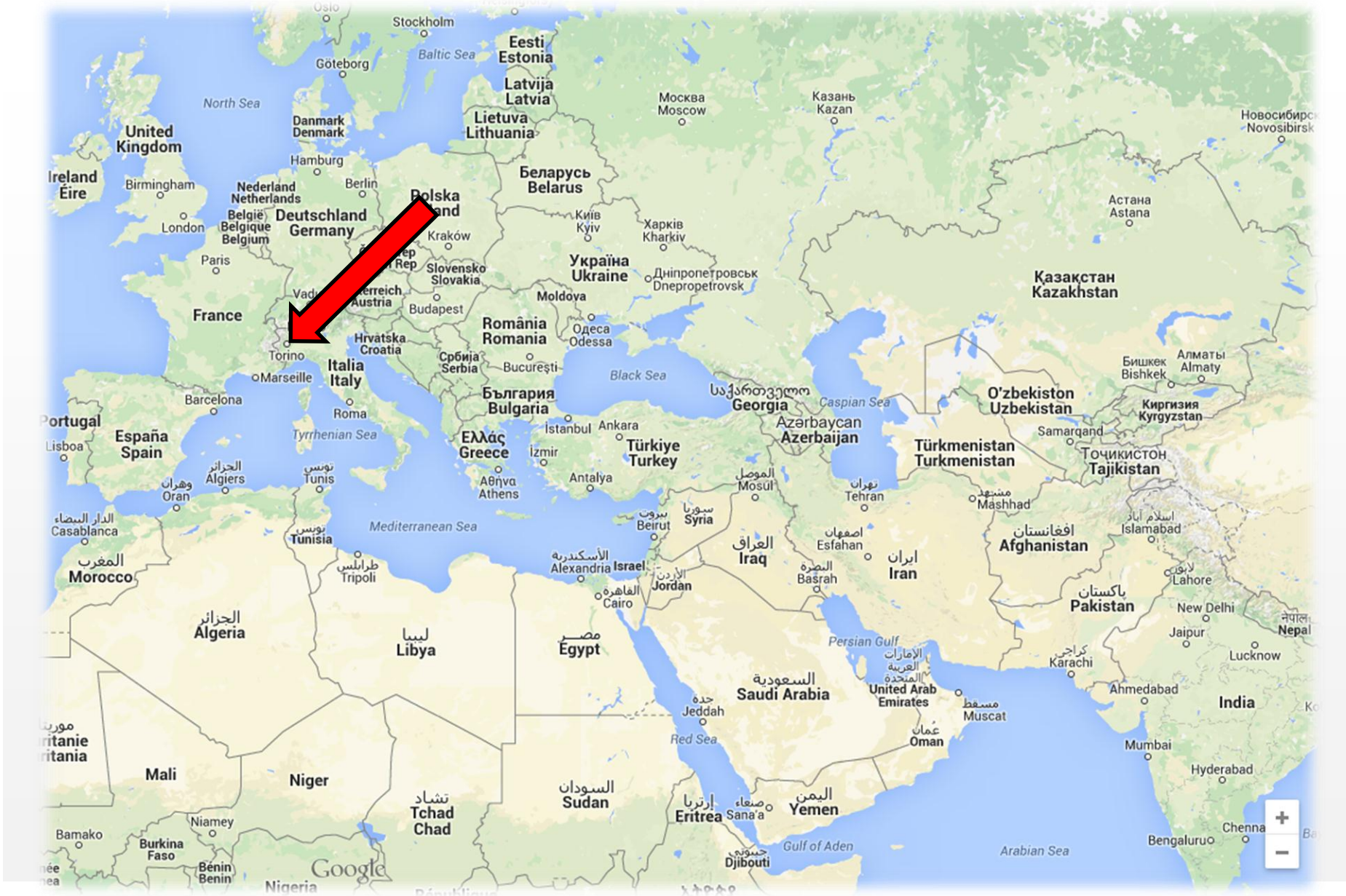
[www.spi2016.org](http://www.spi2016.org)

EPEPS IBIS Summit  
San Jose, California  
October 28, 2015

Stefano Grivet-Talocia  
Politecnico di Torino  
SPI2016 General Chair



# Torino: where?





# Torino: why?





# SPI2016 and Politecnico di Torino



POLITECNICO  
DI TORINO

**The oldest technical university in Italy:  
Founded in 1859 (150+ years of history)**

**Research areas (11 Dept):**  
Industrial Eng.  
Management Eng.  
Mathematics for Eng.  
Civil and Environmental Eng.  
Industrial Design  
Architecture  
ICT

**Faculty: 795 (25.8% women)**

**Students: 33024 (first year: 5552)**

**Programs: 22 BS, 30 MS, 17 PhD**



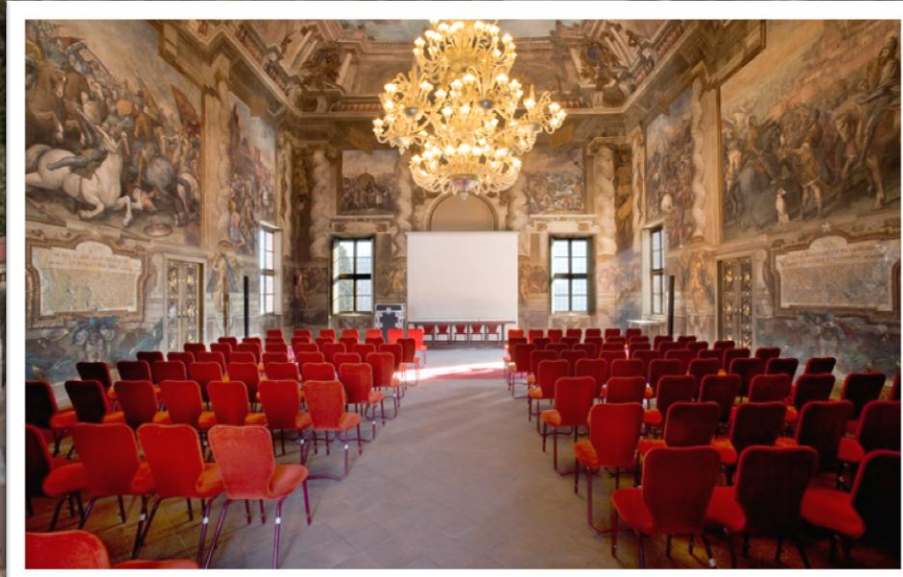
## Engineering campus

My office

An aerial photograph of the Politecnico di Torino engineering campus. The image shows a large, modern brick building complex with multiple courtyards and green spaces. A red arrow points from a red-bordered box containing the text 'My office' to a specific building in the middle ground. The foreground features a large, historic-style building with a dark roof and a series of arched windows. The background shows a dense urban area of Torino, Italy, with hills visible in the distance.



## Architecture campus



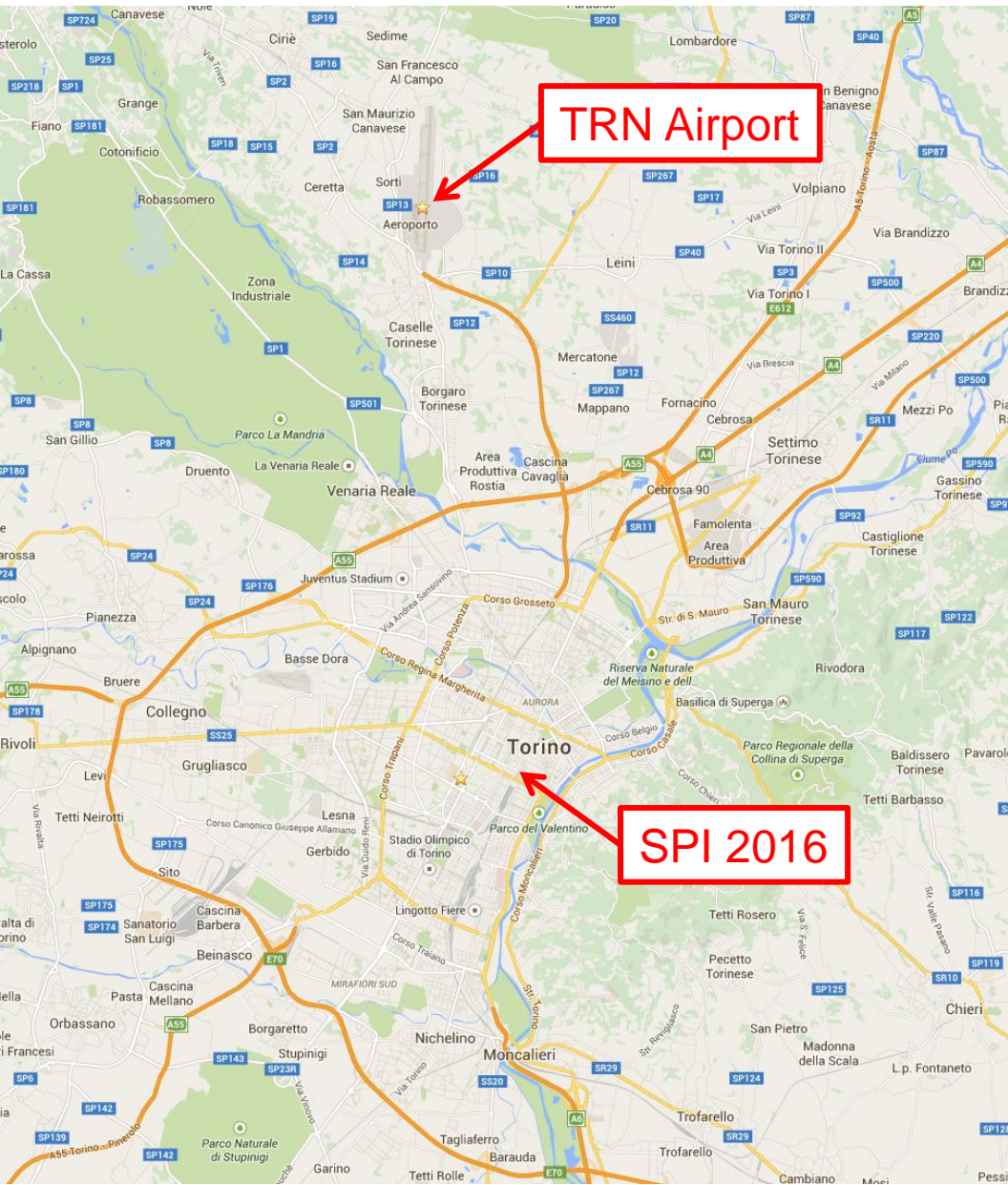


# SPI 2016 location: Starhotel Majestic





# How to reach SPI 2016



By plane:

International airport  
(Torino “Caselle”, TRN)

Trip to downtown:

Bus: 6.50 €  
(40 minutes)

Taxi: 30-40 €  
(20-30 minutes)



# How to reach SPI 2016



By plane:

International airport  
(Milan “Malpensa”, MPX)

Trip to Torino:

Bus: 22 €  
(2 hours)

Train: 30-50 €  
(2 hours)



# How to reach SPI 2016



By train:  
cross the street!

Welcome reception:  
20 min walking

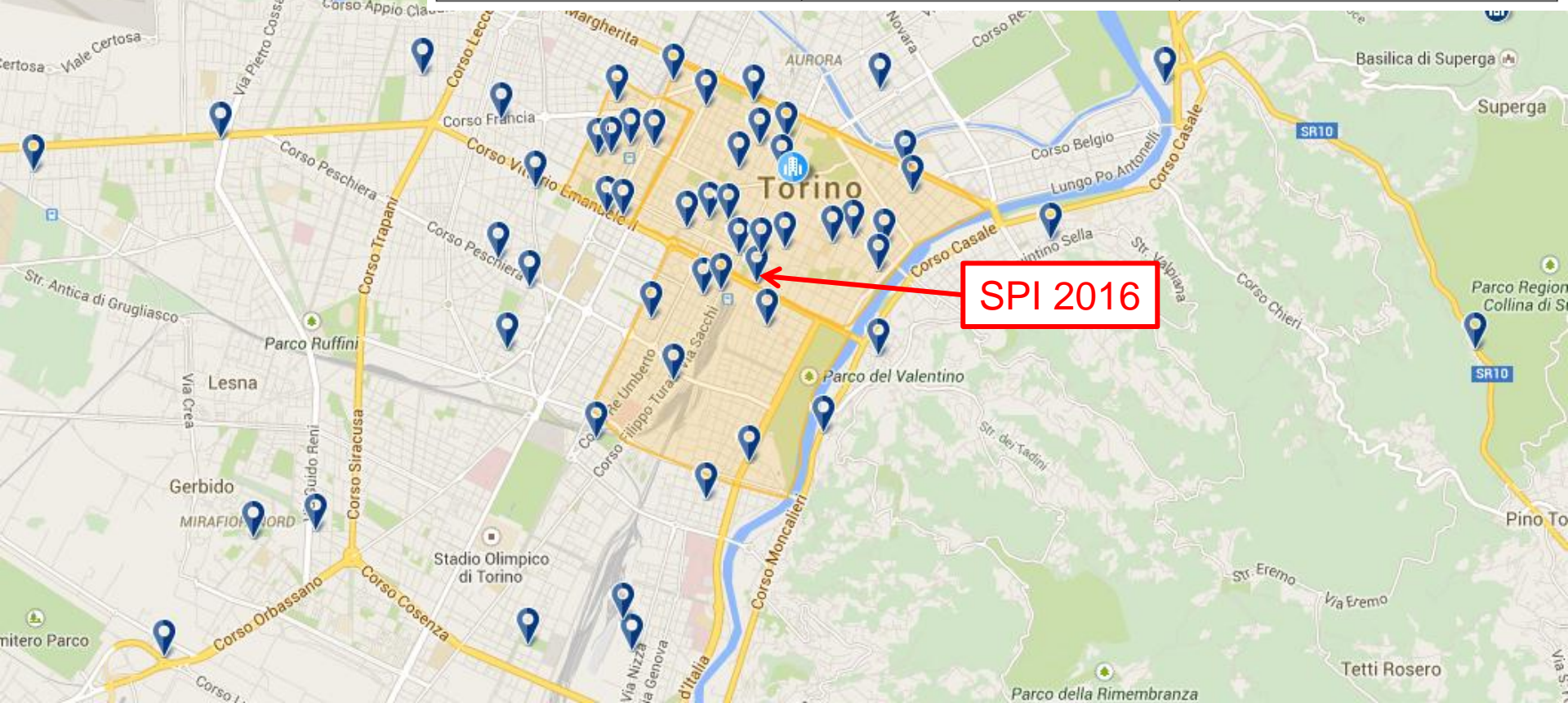
Welcome reception



# Accomodation



Category	Rooms in Torino	Rooms in Torino + M.A.
5 stars hotel	526	526
4 stars hotel	2,540	3,365
3 stars hotel	2,748	4,925
2 stars hotel	628	810
1 stars hotel	639	777
<b>Total</b>	<b>7,081</b>	<b>10,403</b>





## **Sunday May 8, 2016**

Afternoon: Tutorials and short courses

Evening: Welcome reception (hosted in “Valentino Castle”)

## **Monday May 9, 2016**

All day: technical sessions (including keynote) and exhibition

Evening: social event (visit to historical site + cocktail or dinner)

## **Tuesday May 10, 2016**

All day: technical sessions (including keynote) and exhibition

Evening: gala dinner (will be in a “nice” location 😊)

## **Wednesday May 11, 2016**

Morning: technical sessions

Afternoon: 19<sup>th</sup> European IBIS Summit



## Technical Program

oral presentations and posters, peer review, papers to IEEE Xplore

## Best student paper Award



as in past SPI editions, sponsored by: **NVIDIA**.

## Special section of IEEE Transactions on CPMT

authors of best papers (peer review + presentation)

## Industry Forum



special session with (invited and contributed) industry speakers

no academia, no EDA vendors

objectives: present problems, not solutions

stimulate discussion and foster collaboration

managed by Industry Advisory Board

no papers required, only short abstracts

will include a panel discussion and Q&A at the end

willing to help?



## WORKSHOP CHAIR

**Stefano Grivet-Talocia**  
*Politecnico di Torino (ITA)*

## PROGRAM CO-CHAIRS

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*Politecnico di Torino (ITA)*

**Paolo Manfredi**  
*Ghent University, Gent (BEL)*

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*Leibniz University Hannover (GER)*

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*Georgia Institute of Technology (USA)*



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T.M. Winkel, IBM, Boeblingen (GER)  
W.-Y. Yin, Zhejiang Univ. (CN)



## Important Dates

Paper submission (2-4 pages):

**January 31<sup>st</sup>, 2016**

Notification of acceptance:

**February 28<sup>th</sup>, 2016**



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**We wait for you in Torino!**