[Summit Recording (MP4)](https://ibis.org/summits/nov21a/summit_recording.mp4)

[Minutes](https://ibis.org/summits/nov21a/m111221.pdf)

**AGENDA – Virtual Asian IBIS Summit (Japan)**

**Friday, November 12, 2021**

Room: **(Virtual)**

Sponsors: **IBIS Open Forum**

**Japan Electronics and Information Technology Industries Association (JEITA)**

**ANSYS, Inc**

**Apollo Giken Co., Ltd.**

**Keysight Technologies Japan K.K.**

**Ricoh**

**Toshiba Corporation**

**Zuken Inc.**

*(Order and times in JST subject to change)*

*(Most presentations are in pre-recorded audio/ video format with alternative language subtitles)*

8:30 **SIGN IN**

8:35 **MEETING** **WELCOMES**

Randy WOLFF (Micron Technology, USA)  
Chair, IBIS Open Forum

8:40 **2021 Virtual Asian IBIS Summit (TOKYO) MEETING WELCOMES**

Satoshi NAKAMIZO (Keysight Technologies Japan K.K., Japan)  
 Chair, JEITA EDA Model Specialty Committee

8:45 [**IBIS Chair's Report**](https://ibis.org/summits/nov21a/wolff1.pdf)[**(MP4)**](https://ibis.org/summits/nov21a/wolff1.mp4)

Randy WOLFF (Micron Technology, USA)

9:00 [**Expectations for IBIS 7.1**](https://ibis.org/summits/nov21a/wolff2.pdf)[**(MP4)**](https://ibis.org/summits/nov21a/wolff2.mp4)

Randy WOLFF (Micron Technology, USA)

9:20 [**Modeling and Simulation of Single-ended PAM4 Signals in Memory Interfaces**](https://ibis.org/summits/nov21a/rao.pdf)

Fangyi RAO (Keysight Technologies, USA)

9:50 [**DDR Memory System Simulation Method**](https://ibis.org/summits/nov21a/maeda.pdf)[**(MP4)**](https://ibis.org/summits/nov21a/maeda.mp4)

Shinichi MAEDA (KEI Systems, Japan)

10:20 [**Inside an IBIS Provider**](https://ibis.org/summits/nov21a/arai.pdf)[**(MP4)**](https://ibis.org/summits/nov21a/arai.mp4)

Tadashi ARAI (AMD, Japan)

10:45 **BREAK (15 minutes)**

11:00 [**Why are Model Issues Still Alive?**](https://ibis.org/summits/nov21a/kusunoki.pdf)[**(MP4)**](https://ibis.org/summits/nov21a/kusunoki.mp4)

Kazuhiko KUSUNOKI (WADOW, Japan)

11:20 **[3D Package Model for Electromagnetic Field Solver used](https://ibis.org/summits/nov21a/kirinaka.pdf)**

**[in More High-Speed Channel Simulation](https://ibis.org/summits/nov21a/kirinaka.pdf)** [**(MP4)**](https://ibis.org/summits/nov21a/kirinaka.mp4)

Masaki KIRINAKA (Fujitsu Interconnect Technologies Limited, Japan)

11:45 **[A Further Study on the Application of IBIS to CISPR25](https://ibis.org/summits/nov21a/sakata.pdf)**

**[Based EMI Analysis of DCDC Converter](https://ibis.org/summits/nov21a/sakata.pdf)**

**[~Resolving Unexpected Ringing in the Waveform~](https://ibis.org/summits/nov21a/sakata.pdf)** [**(MP4)**](https://ibis.org/summits/nov21a/sakata.mp4)

Kazuyuki SAKATA\*, Koji ICHIKAWA\*\*, Miyoko GOTO\*\*\*, Toshiki KANAMOTO\*\*\*\*

(Renesas Electronics Corporation\*, DENSO CORP.\*\*, Ricoh Corp.\*\*\*,

Hirosaki University\*\*\*\*; Japan)

*[Presented by Kazuyuki SAKATA (Renesas Electronics Corp., Japan)]*

12:10 **DISCUSSION AND** **CONCLUDING ITEMS**

12:30 **END OF IBIS SUMMIT MEETING**

* Thank you for your participation