

AGENDA - Asian IBIS Summit (Shanghai)

Monday, November 13, 2017

Parkyard Hotel
Shanghai, PRC

Room: **Ballroom ABC**

Sponsors: **Huawei Technologies (PRIMARY)**
Cadence Design Systems
IO Methodology
Mentor, a Siemens Business
Synopsys
Teledyne LeCroy
ZTE Corporation

(Order and times subject to change)

8:15 **REFRESHMENTS AND SIGN IN**

8:45 **OFFICIAL OPENING AND WELCOME**

- Yan, Hang (Paul)
(Huawei Technologies, PRC)
- LaBonte, Mike (Chair, IBIS Open Forum)
(SiSoft, USA)

9:00 **IBIS Update**

LaBonte, Mike (SiSoft, USA)

9:25 **IBIS Interconnect Modeling Using IBIS-ISS and Touchstone**

Mirmak, Michael (Intel Corporation, USA)

[Presented by LaBonte, Mike (SiSoft, USA)]

10:00 **Signal Integrity Analysis for 56G-PAM4 Channel of 400G Switch**

Feng, Sophia; Wen, Vincent (Celestica, PRC)

[Presented by Feng, Sophia (Celestica, PRC)]

10:30 **BREAK (REFRESHMENTS AND VENDOR TABLES)**

- Reconvene at 10:50

10:50 **Think PAM4 SerDes**

Zhou, Xiaojun (Huawei Technologies, PRC)

11:20 **Comparison of Time Domain and Statistical IBIS-AMI Analyses**

LaBonte, Mike (SiSoft, USA)

12:00 **FREE BUFFET LUNCH (Hosted by Sponsors)**

- Vendor tables. Reconvene at 13:30

- 13:30 **A Way to Evaluate Post-FEC BER Based on IBIS-AMI Model**
Yu, Yangye, Guo, Tao, Zhu, Shunlin (ZTE Corporation, PRC)
[Presented by Yu, Yangye (ZTE Corporation, PRC)]
- 14:00 **Characterizing and Modeling of a Linear CTE**
Liang, Skipper (Cadence Design Systems, ROC)
- 14:30 **Using DATA Files for IBIS-AMI Models**
Wang, Lance (IO Methodology, USA)
- 15:00 **BREAK (Refreshments and Vendor Tables)**
- Reconvene at 15:20
- 15:20 **IBIS-AMI Modeling Using Scripts and Spice Models**
Huang, Wei-hsing (SPISim, USA)
- 15:50 **Leveraging IBIS Capabilities for Multi-Gigabit Interfaces**
Willis, Ken (Cadence Design Systems, USA)
[Presented by Qin, Zuli (Cadence Design Systems PRC)]
- 16:20 **OPEN DISCUSSION**
- 17:20 **CONCLUDING ITEMS**
- 17:30 **END OF IBIS SUMMIT MEETING**
- Thank you for your participation

