



# JEITA IBIS Promotion Working Group Report

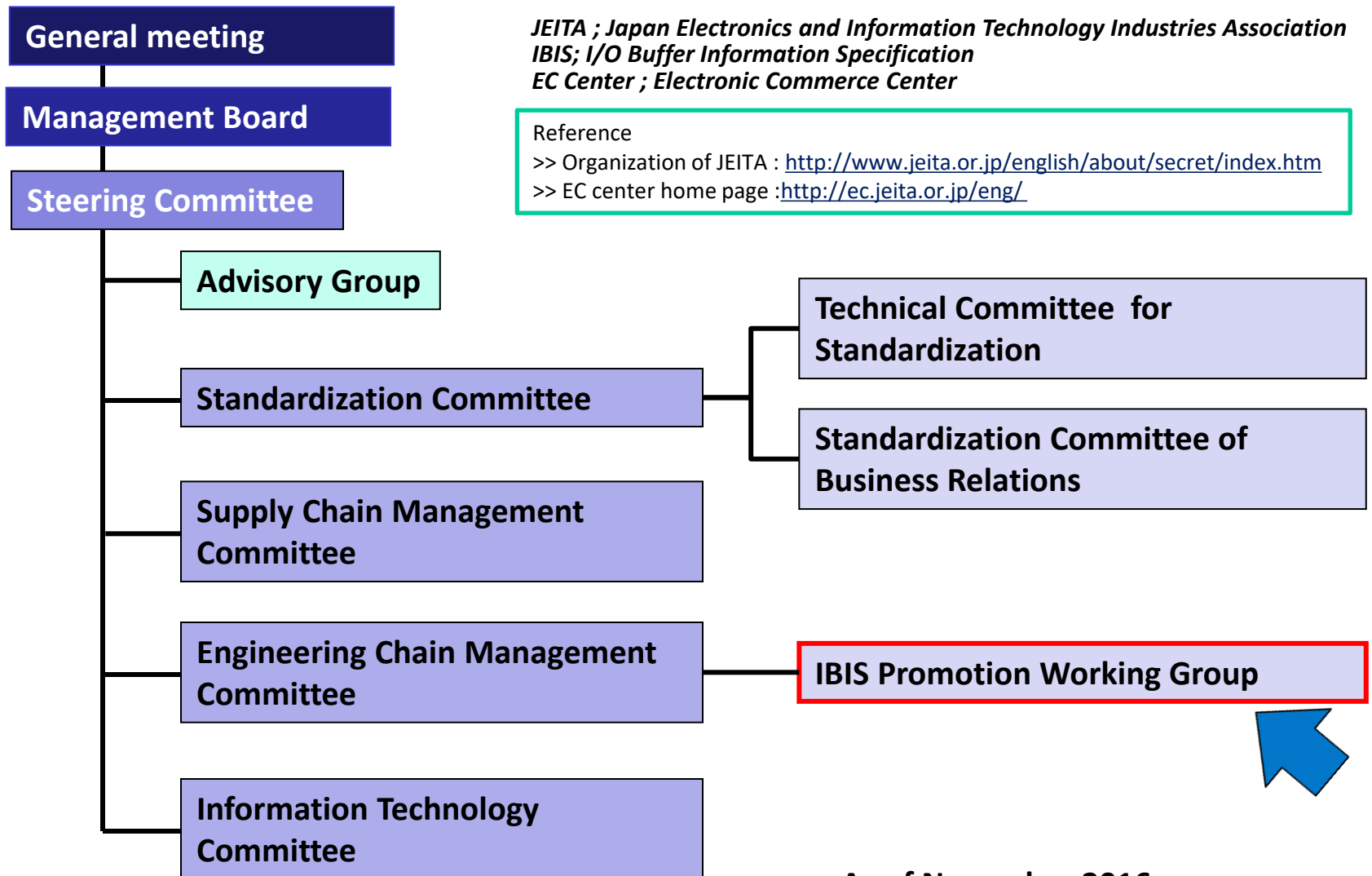
Asian IBIS Summit  
Tokyo, Japan  
November 18, 2016

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**IBIS Promotion Working Group, EC Center / JEITA**



# JEITA/EC Center Committee Organization



*JEITA ; Japan Electronics and Information Technology Industries Association*  
*IBIS; I/O Buffer Information Specification*  
*EC Center ; Electronic Commerce Center*

Reference

>> Organization of JEITA : <http://www.jeita.or.jp/english/about/secret/index.htm>

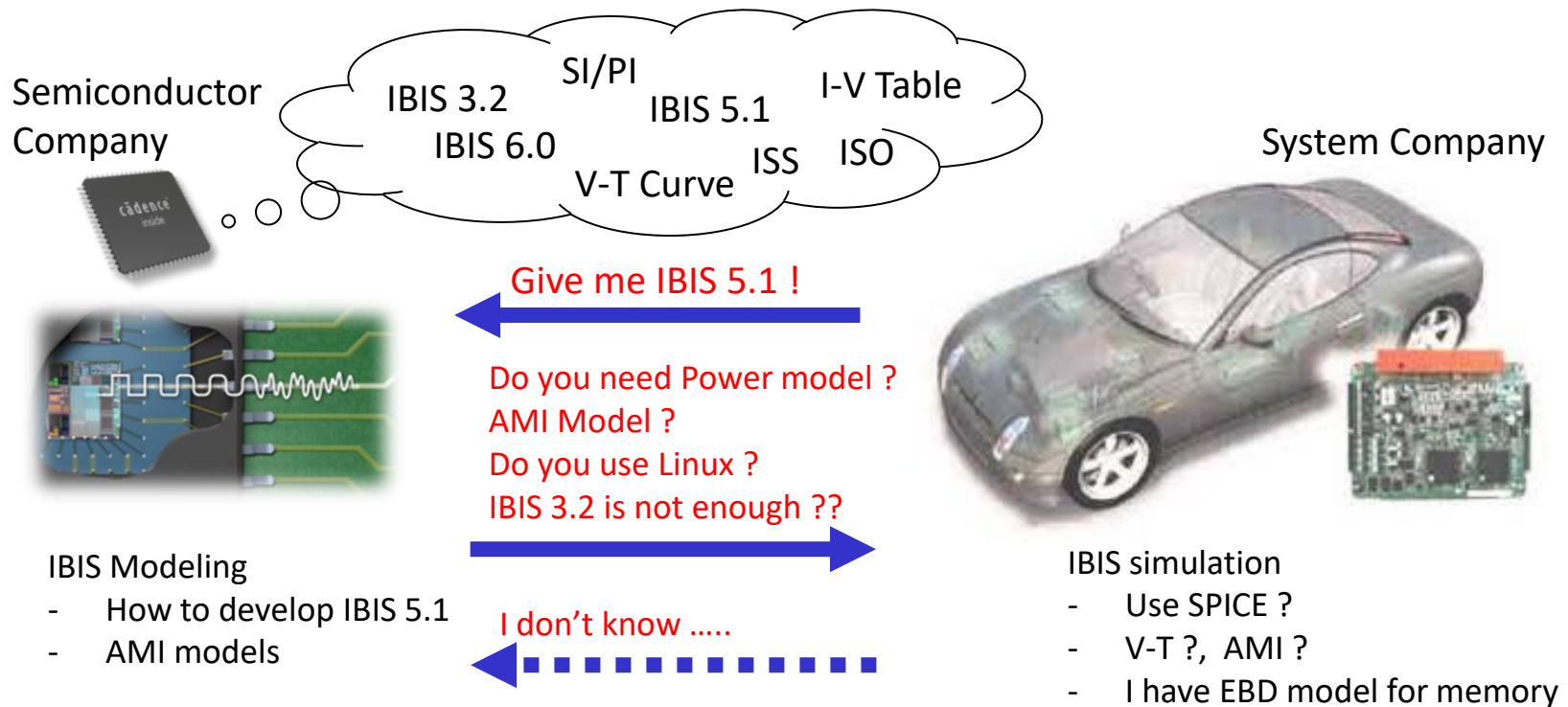
>> EC center home page : <http://ec.jeita.or.jp/eng/>

As of November 2016

# Objective: Let's Use IBIS Simulation

- Promoting IBIS simulation, for High-quality, Low cost and Fast delivery electronics product design

## Example of IBIS issue



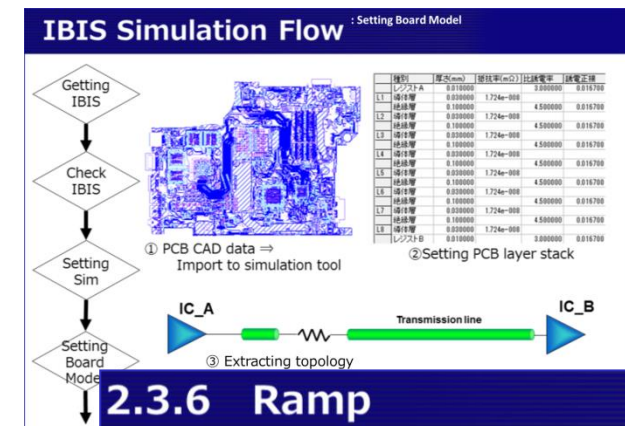
# Activities status: outline

- **Define IBIS simulation procedure guideline**
  - Simulation Guideline for target topologies
- **IBIS promotion of utilization**
  - **IBIS Seminar**
  - Provide any IBIS related information
    - IBIS Keyword Summary
    - Advanced IBIS technology guidance
- Asian IBIS Summit, Japan
  - Support IBIS Open Forum for all logistics
- Maintain IBIS Quality WG results
  - IBIS Quality Framework Home Page
  - JEITA Book: “SI Simulation Model Handbook”

Today's report

# Activity 1: IBIS Basic Seminar

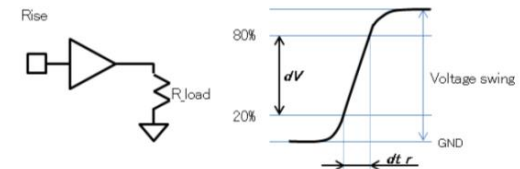
- 2<sup>nd</sup> JEITA IBIS free seminar on June 14, 2016  
"IBIS Simulation Basic Seminar"
  - Recommended IBIS simulation procedure
  - IBIS keyword summary
  - Advanced IBIS technology guidance



## 2.3.6 Ramp

### ■ Ramp $dV/dt$

$$\frac{dV}{dt} = \frac{20\% \text{ to } 80\% \text{ Voltage swing}}{\text{time it takes to swing the above voltage}}$$



## Voice of Attendees

- More information about **IBIS-AMI** and **Power aware IBIS** simulation.
- I would like you to offer **IBIS-AMI** seminars.

# Activity 2: IBIS-AMI Seminar

- 3<sup>rd</sup> JEITA IBIS free seminar on November 18, 2016  
(In the morning of IBIS Summit day)

## "IBIS-AMI First Step"

- Hi-speed signal standards and IBIS
- IBIS-AMI simulation mechanism
- General IBIS-AMI simulation procedure

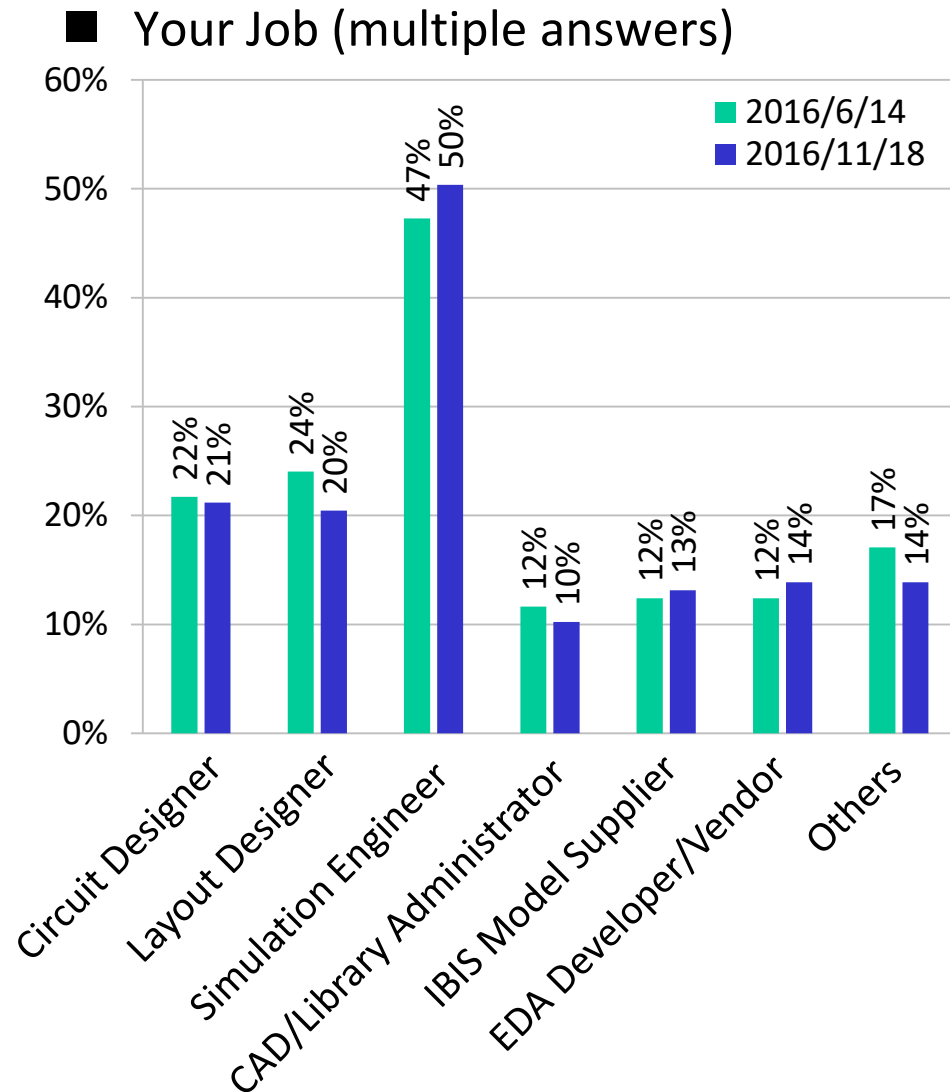
**We summarized the information about IBIS-AMI model's Background, Simulation mechanism, Procedure and Notes for accurate simulation**

# Analysis of seminar attendees (1/3)

■ For understanding the needs of Japanese engineers, we surveyed the 2<sup>nd</sup> and 3<sup>rd</sup> seminar attendees.

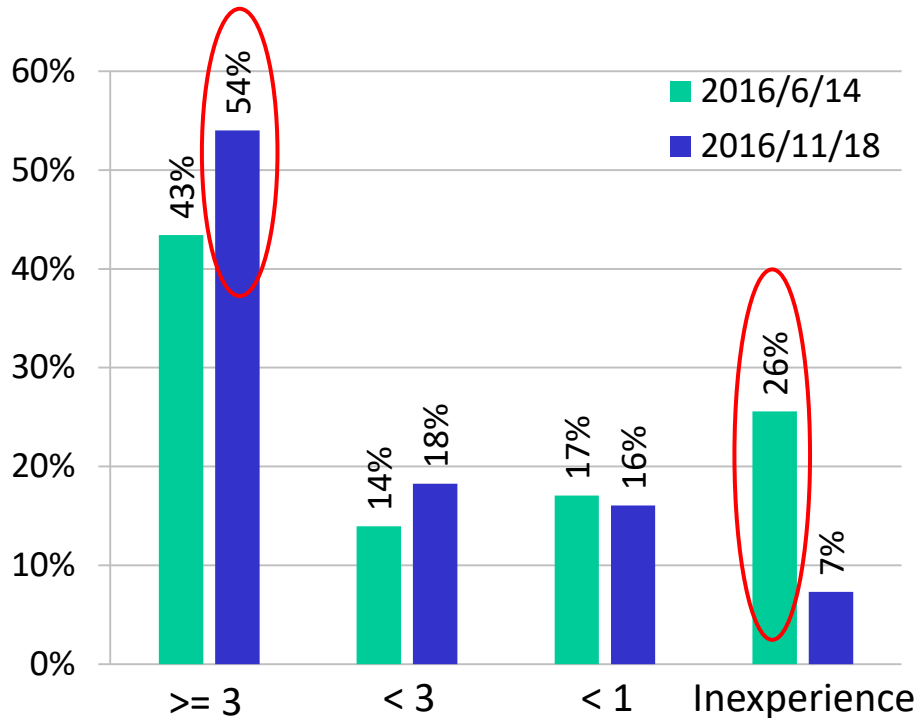
- Number of responses  
2<sup>nd</sup> 2016/6/14 : 129  
3<sup>rd</sup> 2016/11/18 : 137

**We will decide the specific activities based on the survey results.**



# Analysis of seminar attendees (2/3)

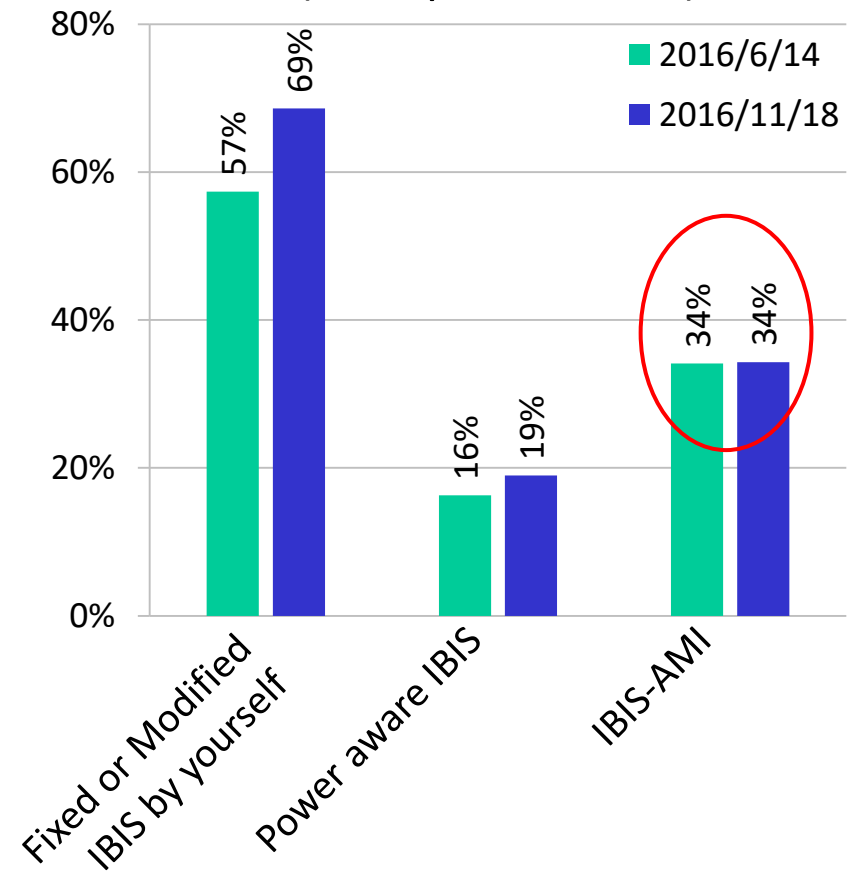
## ■ Years of IBIS simulation experience



**Experienced engineers are interested in IBIS-AMI**

**A lot of IBIS beginners attended the IBIS basic seminar**

## ■ Have you ever used the following IBIS models (multiple answers)

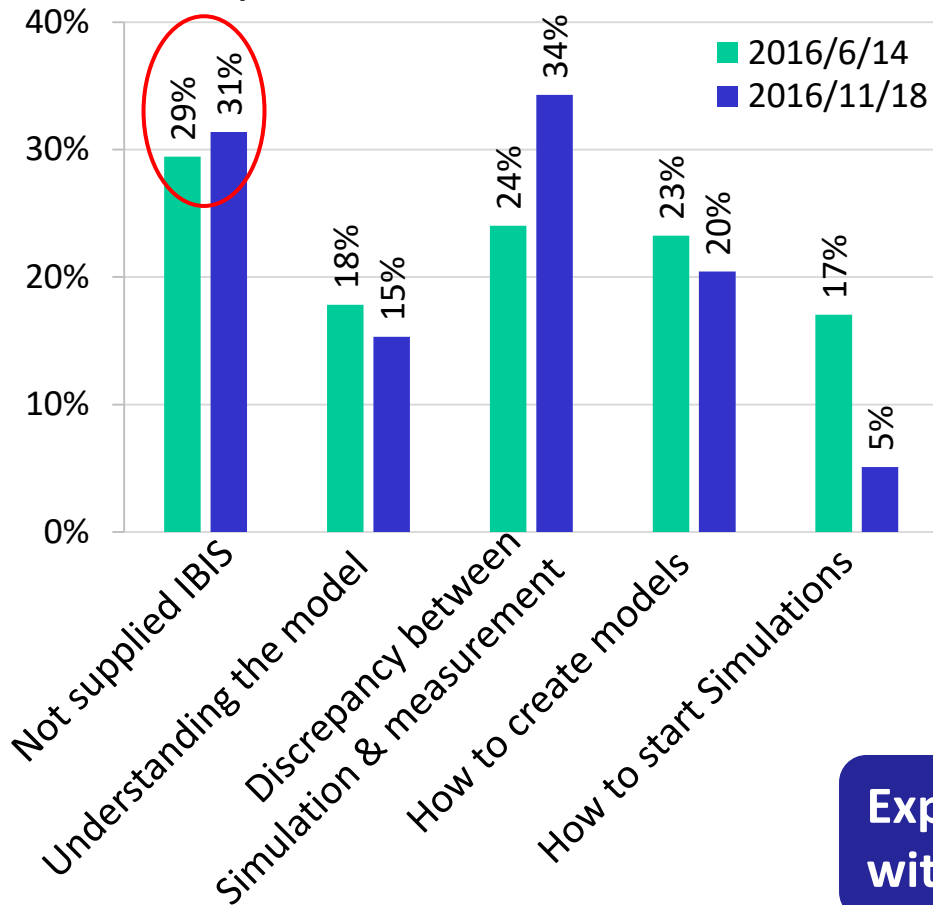


**Already one third people use IBIS-AMI**

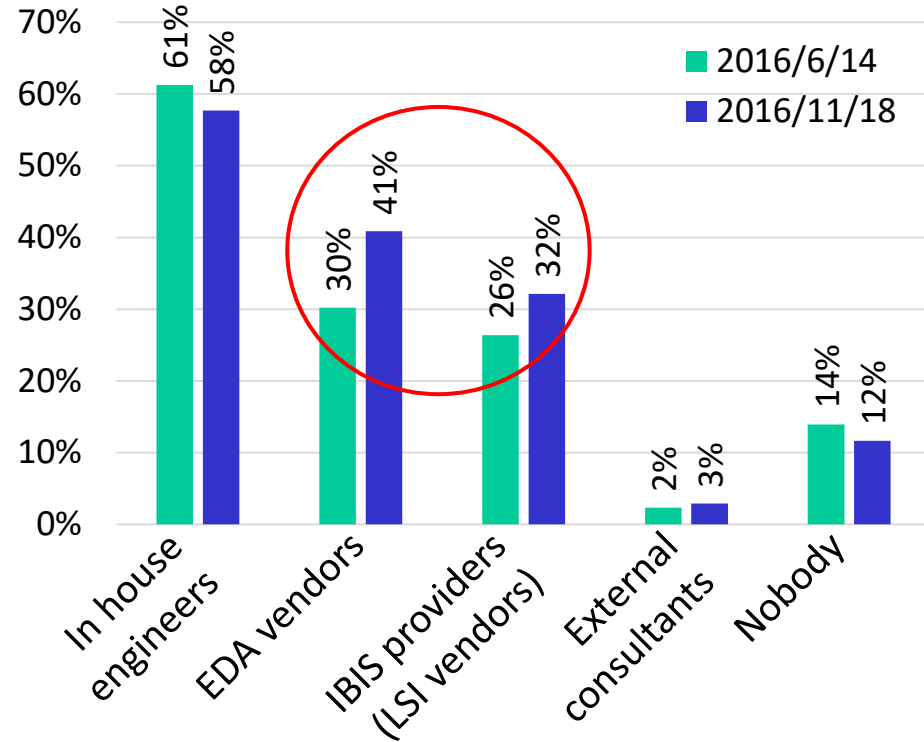


# Analysis of seminar attendees (3/3)

■ What issues concern you with IBIS?  
(multiple answers)



■ Who do you consult with about IBIS simulation? (multiple answers)



Experienced engineers resolve in cooperation with EDA and/or Device vendor

Model availability is still a major problem

**END**