

ASIAN IBIS SUMMIT (CHINA)

亚洲 IBIS 技术研讨会 (中国)



IBIS OPEN FORUM <http://www.eigroup.org/ibis/>



cā d e n c e™

Mentor
Graphics

SYNOPSYS®



ZTE中兴



SIGRITY



CYBERNET



Agilent Technologies

SiSoft®

Agenda



- 9:00 Welcome**
Li, JinJun (Huawei Technologies, China)
Ross, Bob (Chair, IBIS Open Forum, Teraspeed Consulting Group, USA)
- 9:20 Recent IBIS Activities**
Ross, Bob (Teraspeed Consulting Group, USA)
- 9:35 C_comp Extraction Methods for High-Speed I/O Buffers**
Wang, Lance (IO Methodology, USA)
- 10:05 BREAK (Refreshments and Vendor Tables)**
- 10:25 IBIS Quality Review, A Status Review of the IBIS Quality Specification**
Ekholm, Anders* and LaBonte, Mike** (*Ericsson, Sweden and **Cisco Systems, USA)
- 10:55 Huawei's IBIS Model Quality Specification and Technology**
Li, Jun (Huawei Technologies, China)
- 11:25 The Use of Optimization in Signal Integrity Performance Centric High Speed Digital Design Flows**
Bensalem, Brahim**#, Wang, LiHau*#, Gupta, Sanjeev*# and Yuan, XuLiang*## (*Agilent Technologies and **Intel Corporation, #USA and ##China)
- 12:00 FREE BUFFET LUNCH (Hosted by Sponsors) - Vendor Tables**

Agenda



- 13:30** **Behavioral Model Application in HSS Simulation**
Jiang, Nan, Yang, ZhiWei and Zhu, ShunLin (ZTE Corporation, China)
- 14:00** **IBIS-AMI for IBM HSS15 Core Technology**
Herrick, Chris (Ansoft, USA)
- 14:30** **Recent Development of IBIS and Related EDA Technologies**
Hu, JinSong* and Chen, Raymond Y.** (Sigrity, *China and **USA)
- 15:00** **BREAK (Refreshments and Vendor Tables)**
- 15:20** **Sparse Matrix Mapping in Future Touchstone 2.1**
Ross, Bob (Teraspeed Consulting Group, USA)
- 15:50** **Using S-parameters for Accurate Simulation**
Li, BaoLong* and Pitner, Greg** (Ansoft, *China and **USA)
- 16:20** **Subckt Package Model in IBIS**
Dai, WenLiang (Cadence Design Systems, China)
- 16:50** **System Simulation Automation - Model Connection Review**
Zhong, ZhangMin*, Brim, Brad**, and Chen, Raymond Y.** (Sigrity, *China and **USA)
- 17:20** **Concluding Items**
- 17:30** **END OF IBIS SUMMIT MEETING**



Topics

	Presentations
Method	C_comp Extraction Methods for High-Speed I/O Buffers
Quality	IBIS Quality Review, A Status Review of the IBIS Quality Specification
	Huawei's IBIS Model Quality Specification and Technology
System integration	The Use of Optimization in Signal Integrity Performance Centric High Speed Digital Design Flows
AMI	Behavioral Model Application in HSS Simulation
	IBIS-AMI for IBM HSS15 Core Technology
	Recent Development of IBIS and Related EDA Technologies
S-parameters	Sparse Matrix Mapping in Future Touchstone 2.1
	Using S-parameters for Accurate Simulation
Package Model	Subckt Package Model in IBIS
	System Simulation Automation - Model Connection Review

Survey Result (out of 85 responses)

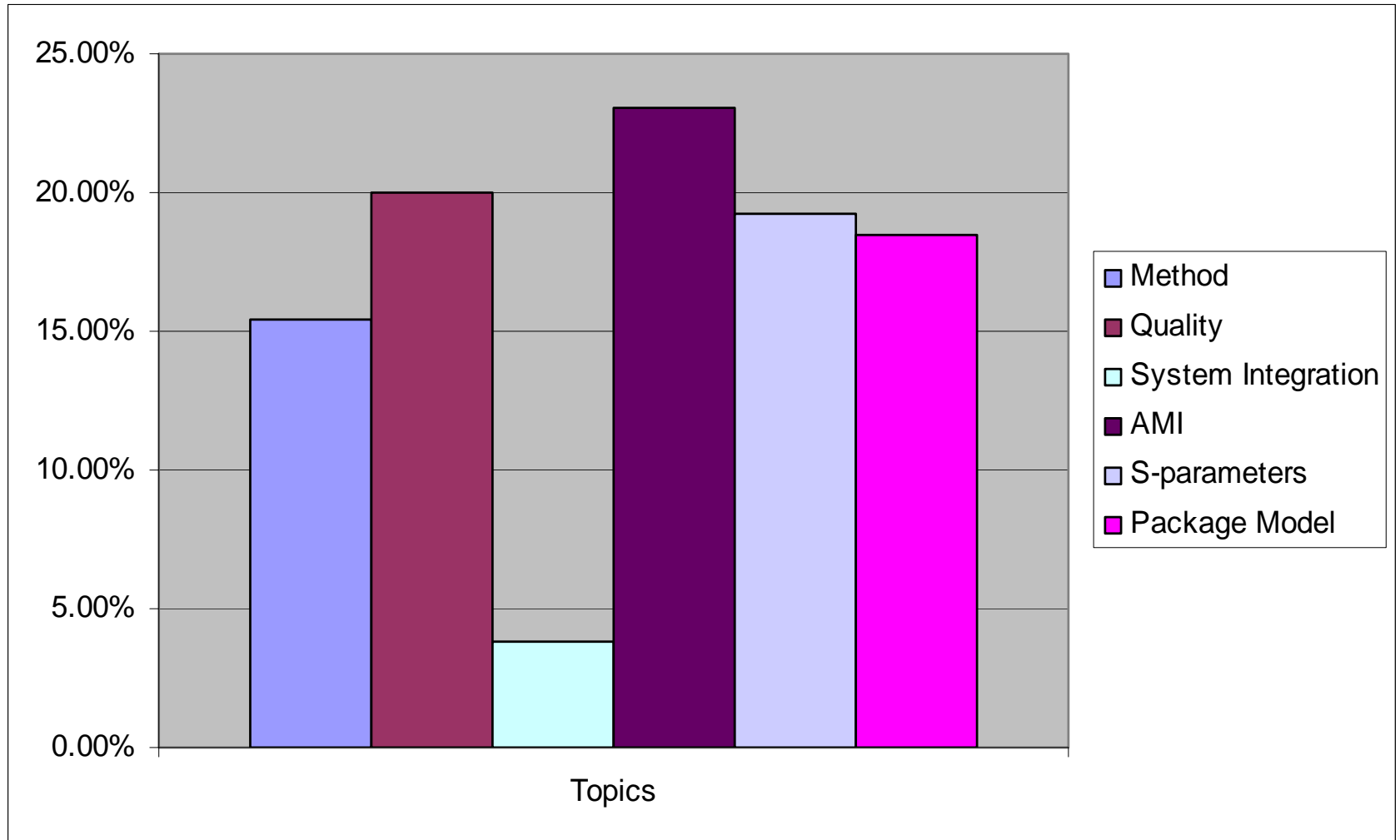
Q1: Did you find this meeting valuable?

- **100% YES**
- **0% NO**



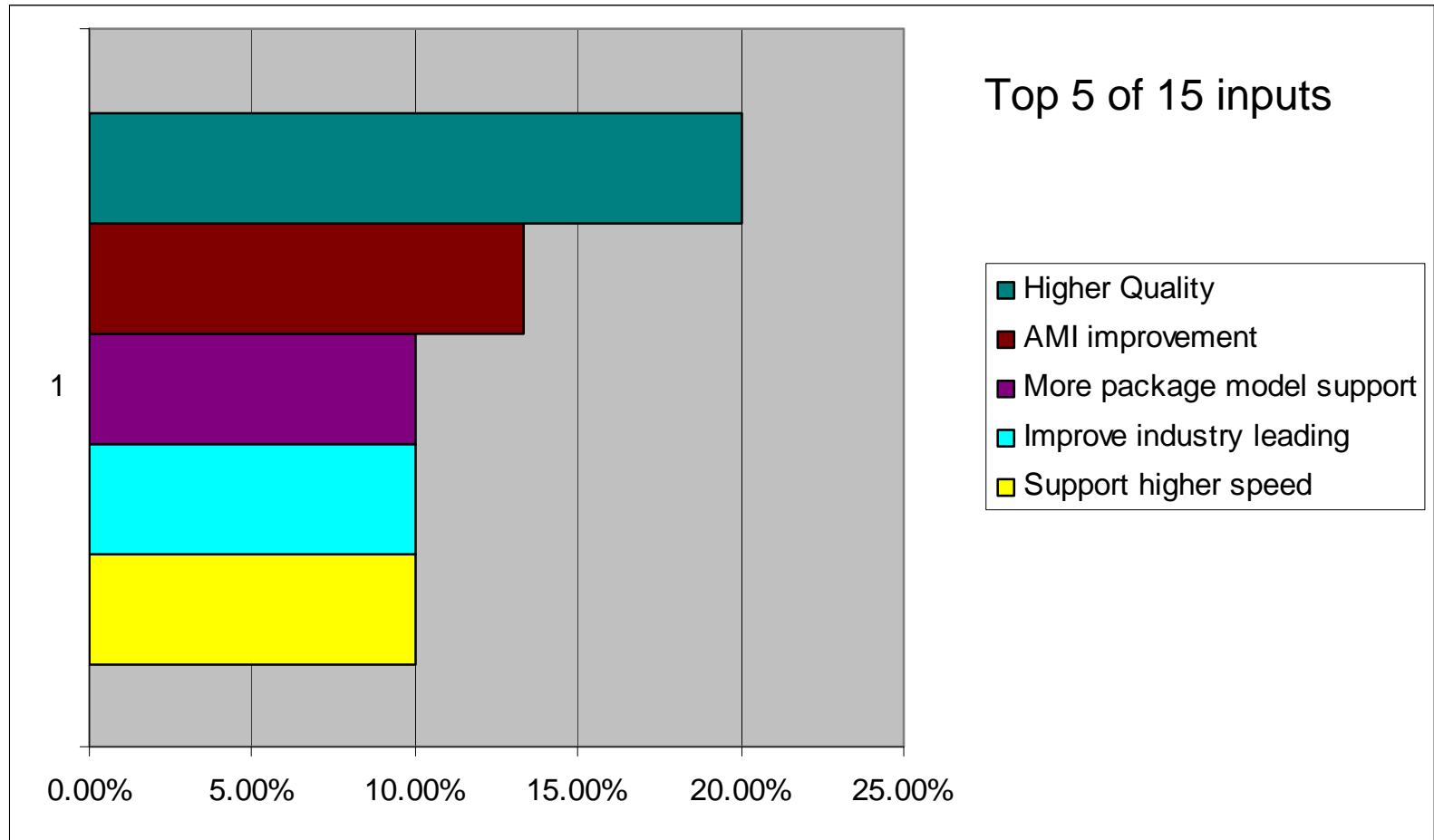
Survey Result (out of 130 responses)

Q2: What was your favorite topic or presentation?



Survey Result (out of 90 responses)

Q3: What would you like IBIS to do in the future?



Survey Results (out of 49 responses)

Q4: Please provide any additional comments or suggestions about the meeting

Top 8 of 18 inputs

