

Japan IBIS Activities Update

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JEITA EDA-WG

IBIS Summit
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JEITA ; Japan Electronics and Information Technology Industries Association

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IBIS Standard

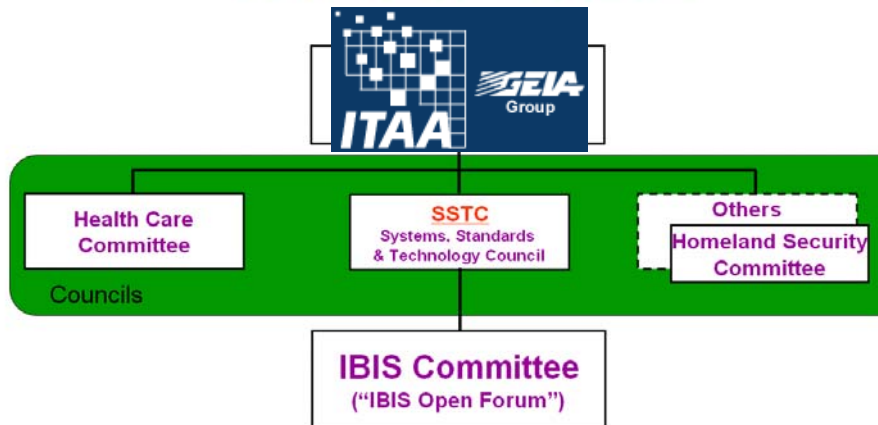
I/O Buffer Information Specification

- Behavioral Modeling of I/O
Including V-t, V-I tables, PKG RLC and other related parameters
- No proprietary information in models
- Platform independent, runs on any tools
- Relatively fast simulation time vs SPICE
- Became Standard Since 1995
- Current Ver. 4.2;
ANSI/EIA-656-B Approved 2007



IBIS Open Forum Organization

IBIS Structure



Notes

- GEIA is an ANSI-accredited standards body

2007 IBIS Summit at DAC

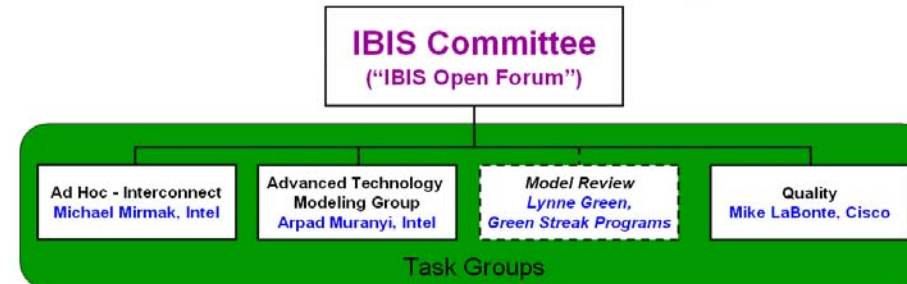
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Official paid member companies

33 members in 2008

**Including worldwide major
IBIS Users, IBIS Creators
& EDA vendors**

IBIS Task Groups



Notes

- The Cookbook Task Group has been suspended until critical mass of needed changes is achieved
- The Futures and Macromodeling Library groups have been combined into the Advanced Technology Modeling Group
- EDA vendors comprise Model Review, independently reviewing received models and providing feedback to model authors; no specification changes are recommended by this group and it does not hold meetings

- Participation in the IBIS Task Groups is open to IBIS members and the general public alike

- Task Groups are created and Chairs appointed by the IBIS Chair
- Some issues are handled by Ad Hoc teams for BIRD development

2007 IBIS Summit at DAC

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Board Member

Michael Mirmak --- Chair

Syed Huq – Vice-Chair

Randy Wolff – Secretary

Lance Wang – Model Librarian

Bob Ross – Postmaster/"Parliamentarian"

Support by JEITA/EC center

What is EC Center (taken from site: <http://ec.jeita.or.jp>)

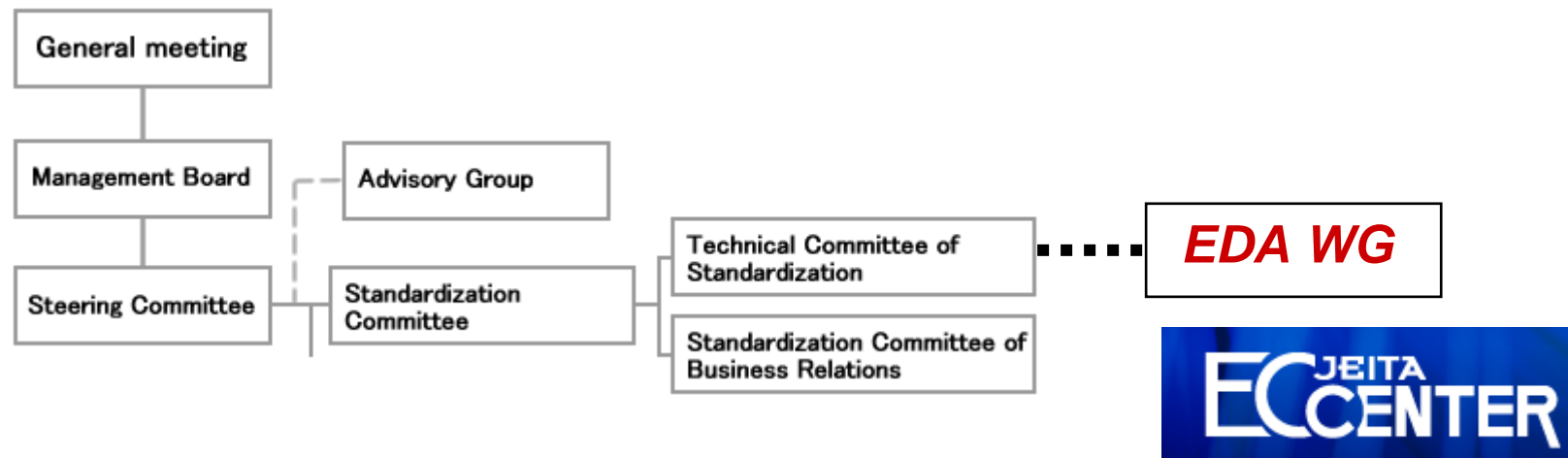
(Objective)

EC Center aims to **contribute to the development of the electronic device and semiconductor/electronic component industry** by maintaining a base for business-to-business electronic commerce and business-to-business concurrent engineering.

(Operations)

EC Center standardizes

- (2) **Upgrade and maintenance of standards** relevant to related information
- (4) Information exchange, **international cooperation**, and investigative research **with related national and international organizations and groups**



Supporting members & Focus

JEITA EC center / EDA WG

- **Leader : Watanabe, Takeshi (NEC Electronics)**
- **Members from Consumer electronics, Tool vendors & Semiconductors**

Focus on;

- **IBIS quality**
- **Interconnect modeling**
- **Promotion**



Promotion

JAPAN IBIS Summit

- 2006 : JEITA meeting with IBIS officers

More than **30** attendees

- 2007 : Successful 1st. Japan Summit

More than **50** attendees of
model users & creators

- 2008 : 2nd. Summit, Today,

More than **60** registered. Updating records!

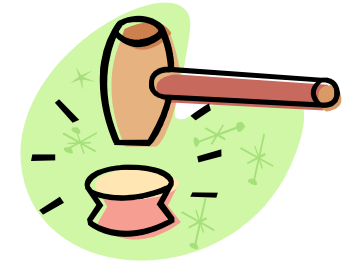
IBIS Guidebook planned 2007

- *Suspended* due to copyright, publish problem
- Part of the work displayed at this site



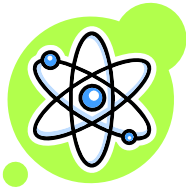
IBIS quality

- Major advancement in this year!
- Defining new tangible quality measures
- Presented in 2 conferences, got audience's favor
- Expecting growing support from users & providers



Interconnect modeling

- Study in progress!
- Seeking standard way of distribution system





Your contribution can
CHANGE
SI & PI communities
Thanks for supporting IBIS