

# More experiences with IBIS-AMI models

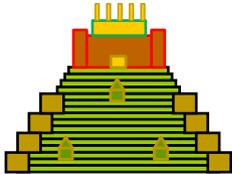
**Eckhard Lenski**  
**European IBIS Summit**  
**Sorrento, Italy**  
**May 16, 2012**

**for a**  
**world**  
**in motion™**

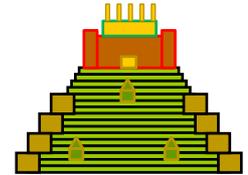
Public

# More experiences with IBIS-AMI models

summary



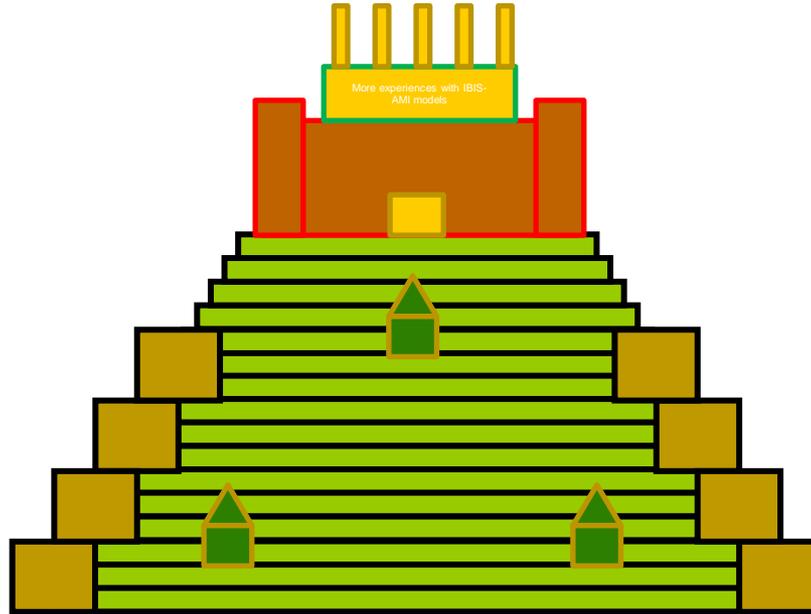
Why IBIS-AMI



IBIS-AMI  
models



Public



Basics IBIS-AMI



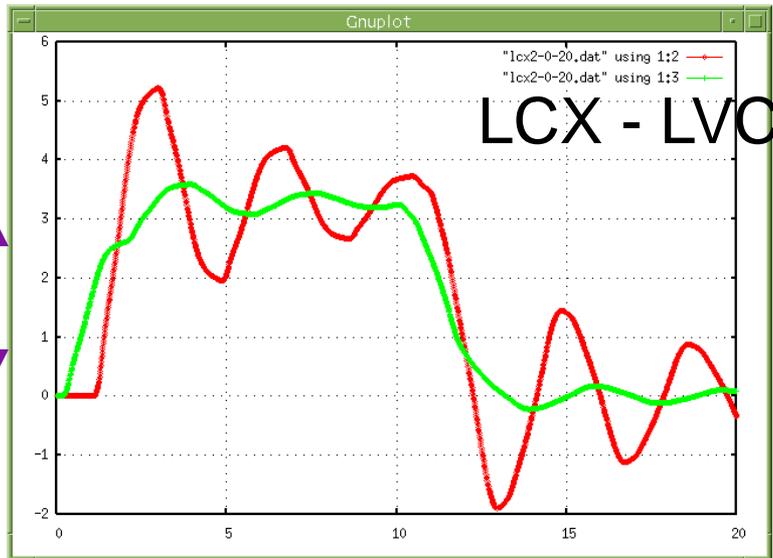
Differences  
IBIS vs  
IBIS-AMI



# Higher datarates

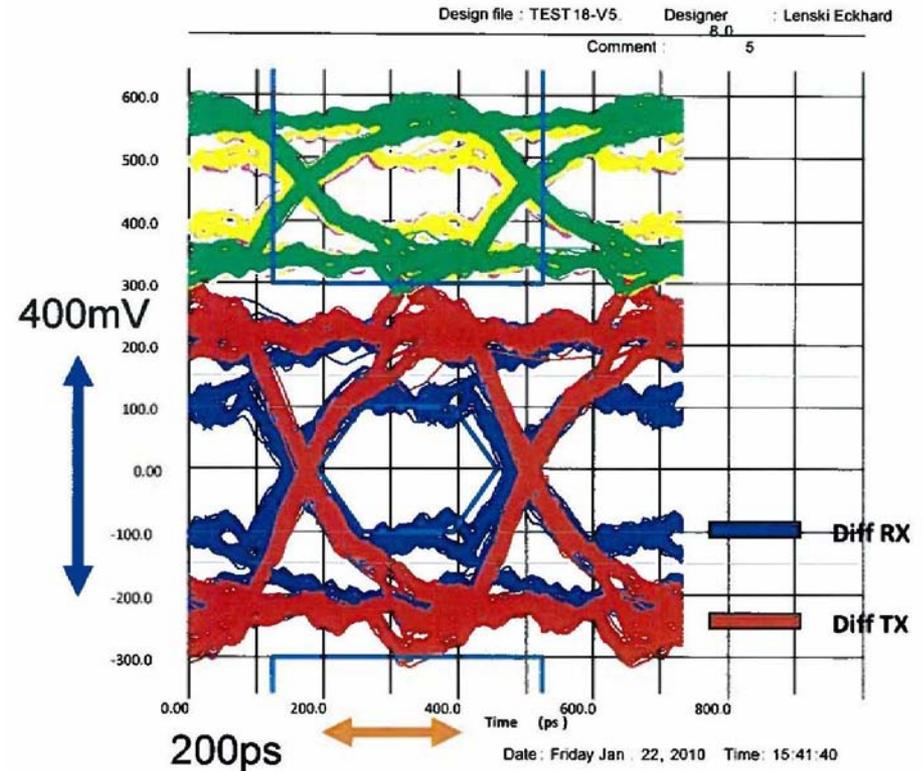
## Classic SI

- Rise fall times
- Overshoots



## BER, ISI

- UI
- Eye opening
- Jitter



# Higher datarates

## 10Gbps and more

- PCI-Express 2, 3, 4
- Serial ATA 2, 3
- USB 3
- XAUI
- CEI 11G
- Emerging 25 / 40 / 100 Gbps standards

## Above 1GHz

- ISI ( Inter Symbol Interference )
- Attenuation
- Jitter
- Influence of vias
- Influence of connectors

# Analysis time

## Estimated time for determination of BER

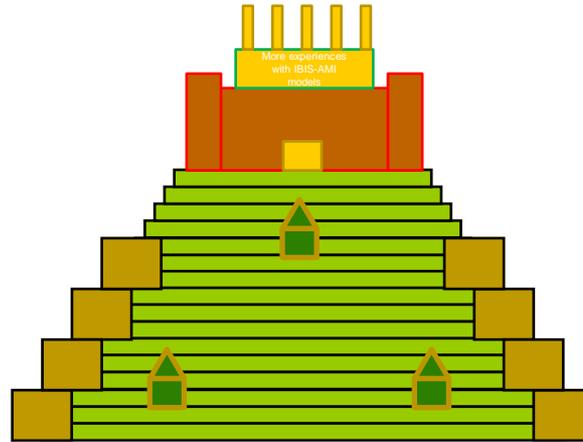
Eg 6.375Gbps

BER	Measurement	Spice	Ibis-ami
3e-12	1 minute	hour	seconds
1e-15	2 days	days	a few minutes
6e-18	1 year	more days	More minutes

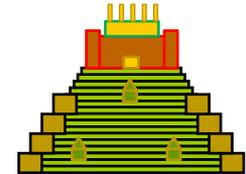
Source: measurement data from **Bogatín Altera high speed seminar 2006**  
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# More experiences with IBIS-AMI models

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Why IBIS-AMI



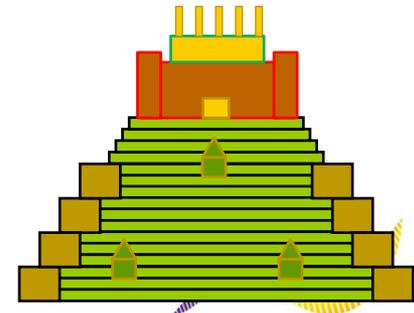
IBIS-AMI  
models



Basics IBIS-AMI



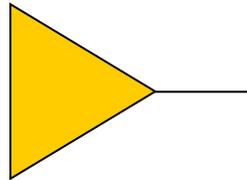
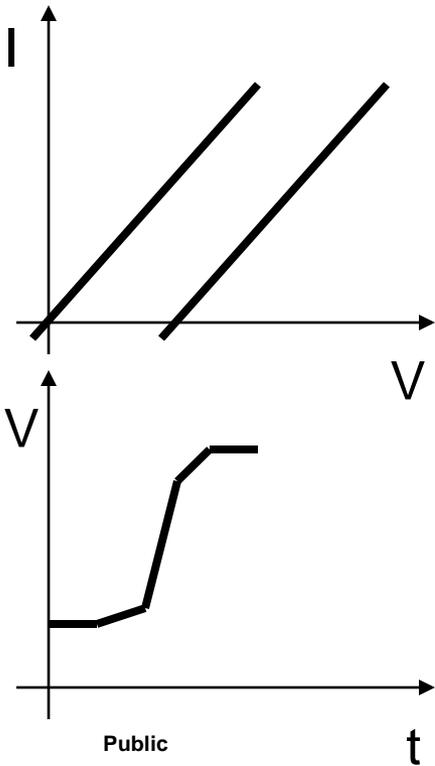
Differences  
IBIS vs  
IBIS-AMI



Public

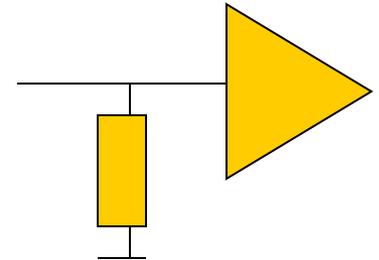
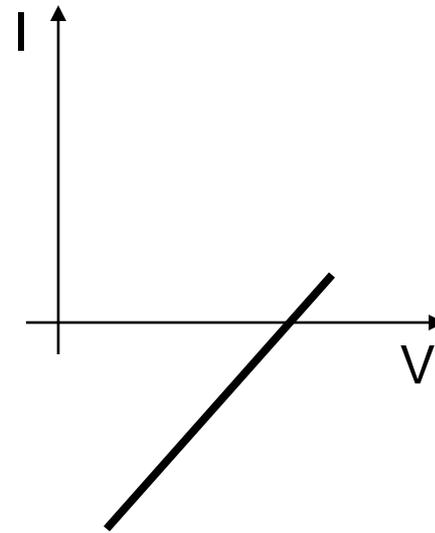
## Driver

- U-I curves
- U-t curves



## Receiver

- U-I curves
- Params ( $v_{inh}, v_{inl}$ )



# Modeling of high speed features

<b>Tx &amp; Rx with</b>		<b>In IBIS</b>	<b>IBIS-AMI</b>	Algorithmic Modeling Interface
■ equalization	■	-	X	
■ Pre-emphasis				
— 2 tap		— X	X	
— 3 tap		— -	X	
■ FFE	■	-	X	
■ DFE	■	-	X	
■ jitter	■	-	X	

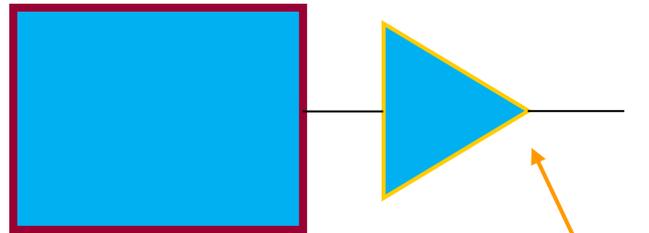
# IBIS-Ami ( Algorithmic modeling interface )

## Driver

- FFE
- Swing
- Taps / Pre-emphasis

## Receiver

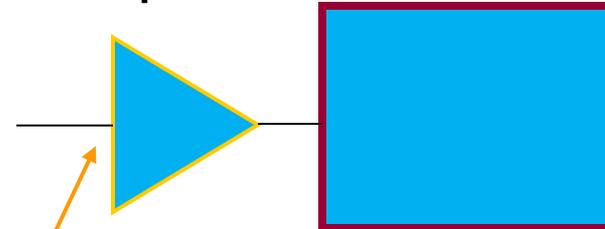
- DFE
- Taps
- Jitter
- CDR
- Equalization



**Algorithmic Tx model**

Classic ibis Tx model

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**Algorithmic Rx model**

Classic ibis Rx model

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# platform

## IBIS

- Runs on every platform
- Same model

## IBIS-AMI

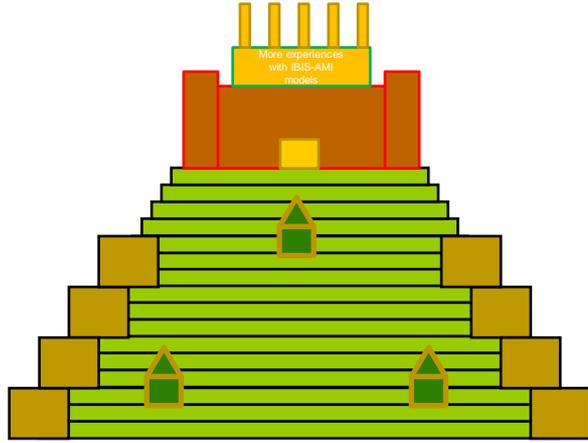
- Platform dependant
  - Linux
  - windows
- 32 / 64 bit
- different model
  - Model.so (linux)
  - Model.dll (windows)
- documentation file necessary



*summary*



*More experiences with  
IBIS-AMI models*



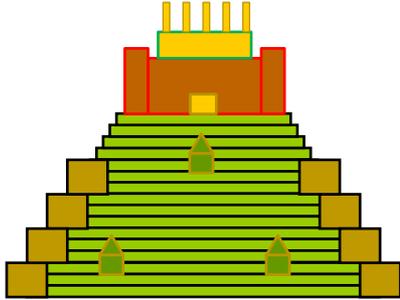
*Why IBIS-AMI*



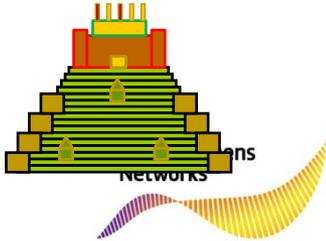
*IBIS-AMI  
models*



*Basics IBIS-AMI*



*Differences  
IBIS vs  
IBIS-AMI*



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# IBIS-AMI keyword

## [Model] Tx

- Model\_type Open\_sink
- [Voltage Range] 1.2 1.14 1.26
- C\_comp 1.00p 0.95p 1.05p
- [Pullup]
  
- [Algorithmic Model]
- Executable Windows\_VisualStudio\_32 NSNTx.dll  
NSNTx.ami
- Executable Linux\_gcc3.2.3\_32 NSNlibTx.so  
NSNlibTx.ami
- [End Algorithmic Model]

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# Executable

Windows\_VisualStudio\_32

NSNTx.dll

NSNTx.ami

■ Executable



■ Subparameter

■ Windows\_VisualStudio\_32



■ Platform\_Compiler\_Bits

■ NSNTx.dll



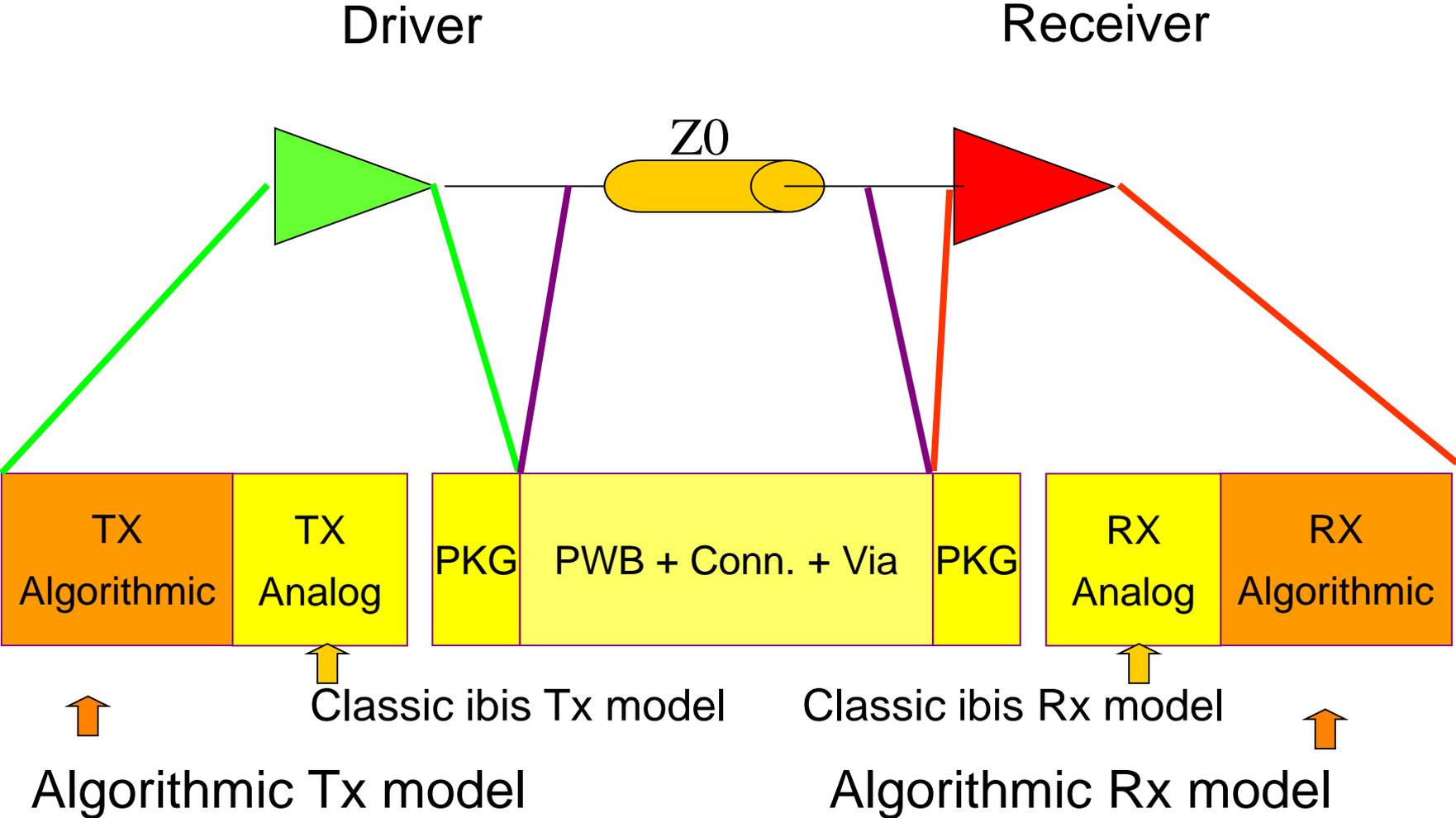
■ name of shared library file

■ NSNTx.ami



■ Description file for params

# IBIS-AMI principle

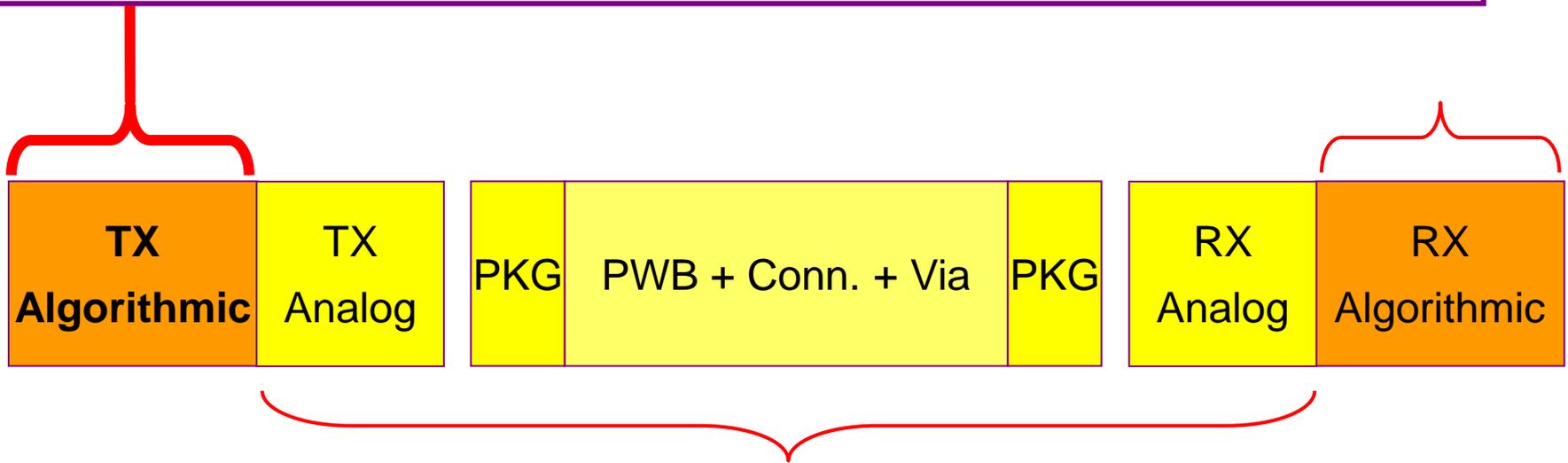


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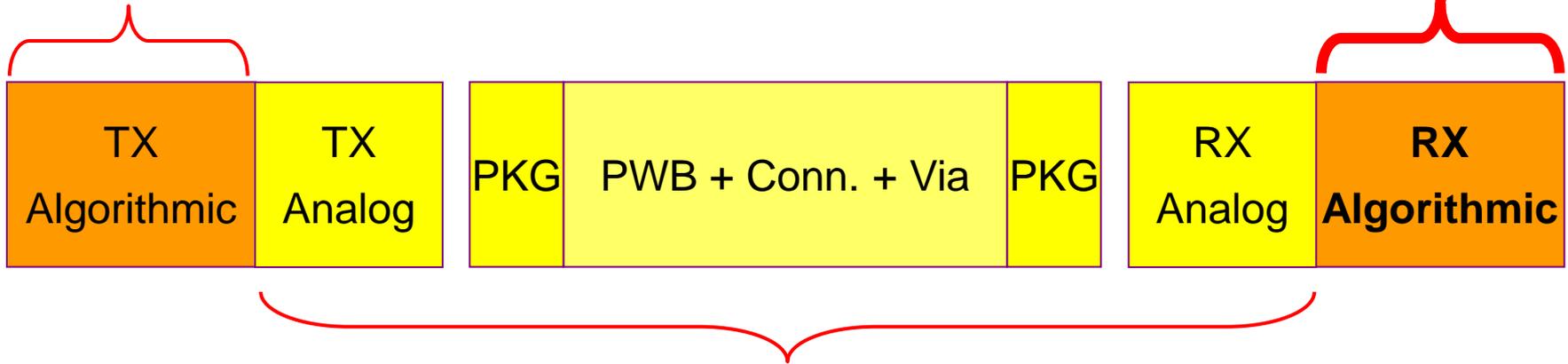
# IBIS-Ami driver

Signal processing in the driver before the final output stage  
equalization/pre-emphasis (FFE feed forward equalizer)  
encoding ( 8b/10b)

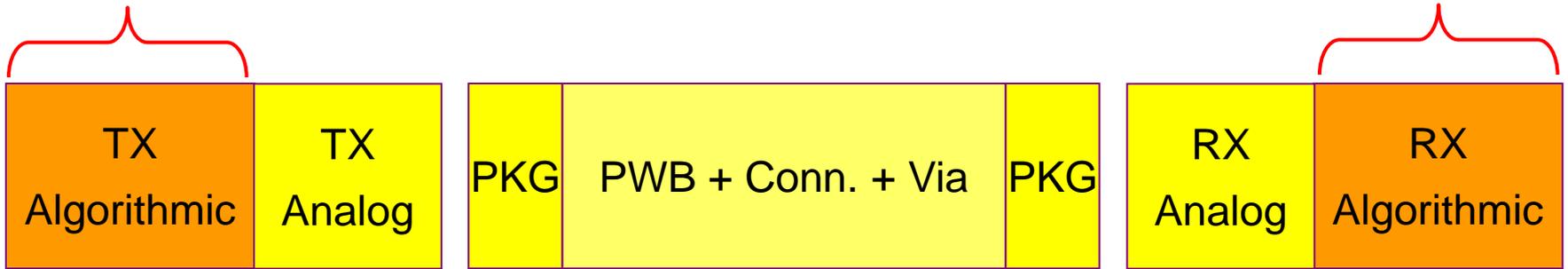


# IBIS-Ami receiver

Signal processing in the receiver after the first input stage  
equalization (e.g. DFE decision feedback equalizer)  
clock recovery

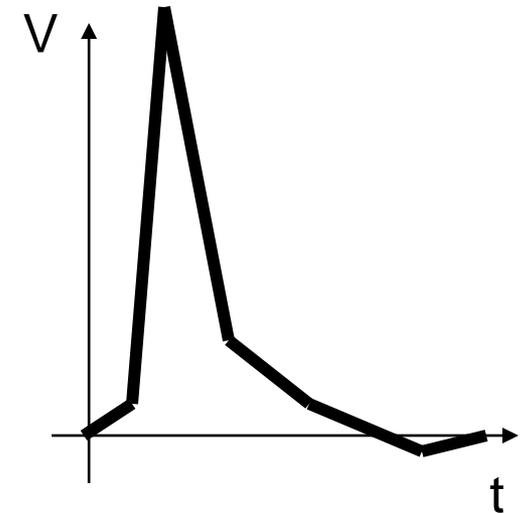


# IBIS-Ami channel



Impulse response representing the analog channel

Driver (TX) final output stage  
Channel  
Receiver (RX) input stage



Channel must be LTI ( Linear and TimeInvariant )

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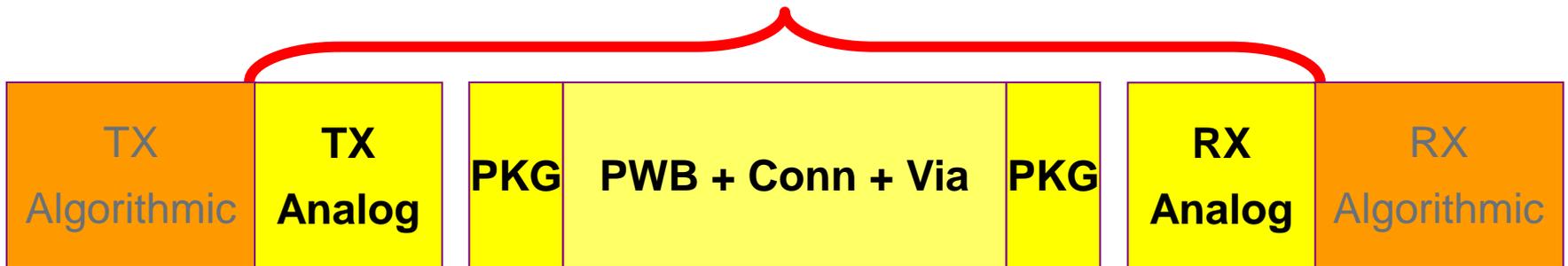
# IBIS-AMI channel elements

## Elements

- Tx analog
- Rx analog
- Pkg
- Channel, T-line
- Via
- connector

## models

- Classic ibis driver
- Classic ibis receiver
- S-params
- W-element , S-params
- S-params
- S-params



Public

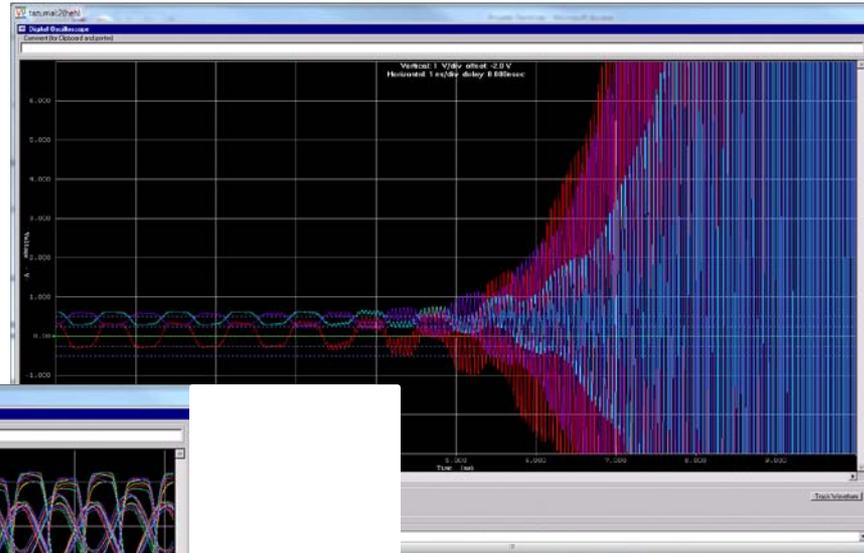
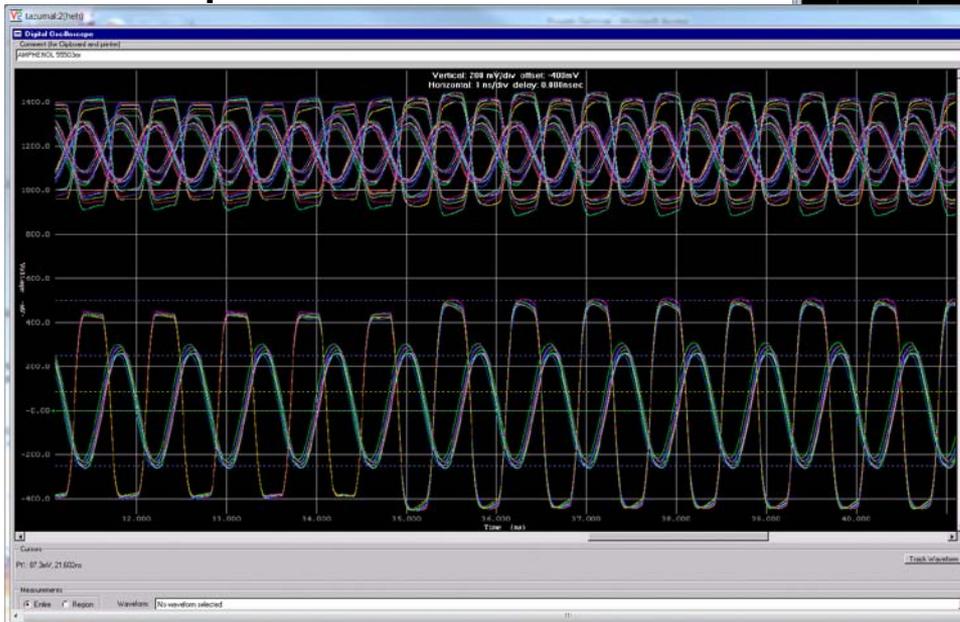
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# Modeling of high speed features

## Question

- Which simulation
  - used the good
  - used the bad
- S parameter model ?



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# IBIS-AMI driver / receiver elements

## TX

- AMI\_INIT
- AMI\_Getwave
- AMI\_close

## RX

- AMI\_INIT
- AMI\_Getwave
- AMI\_close

## TX/RX Ami

- AMI\_INIT
  - Used for statistical analysis, setting for getwave parameters, LTI
- AMI\_Getwave
  - Timing analysis, non LTI behavior
- AMI\_close
  - Clear memory, etc.

# IBIS-AMI parameters

## General parameters

- Init\_Returns\_Impulse
- Getwave\_Exists
- ( Use\_Init\_Outputs )
- Ignore\_Bits
- Max\_Init\_Aggressors
- .....

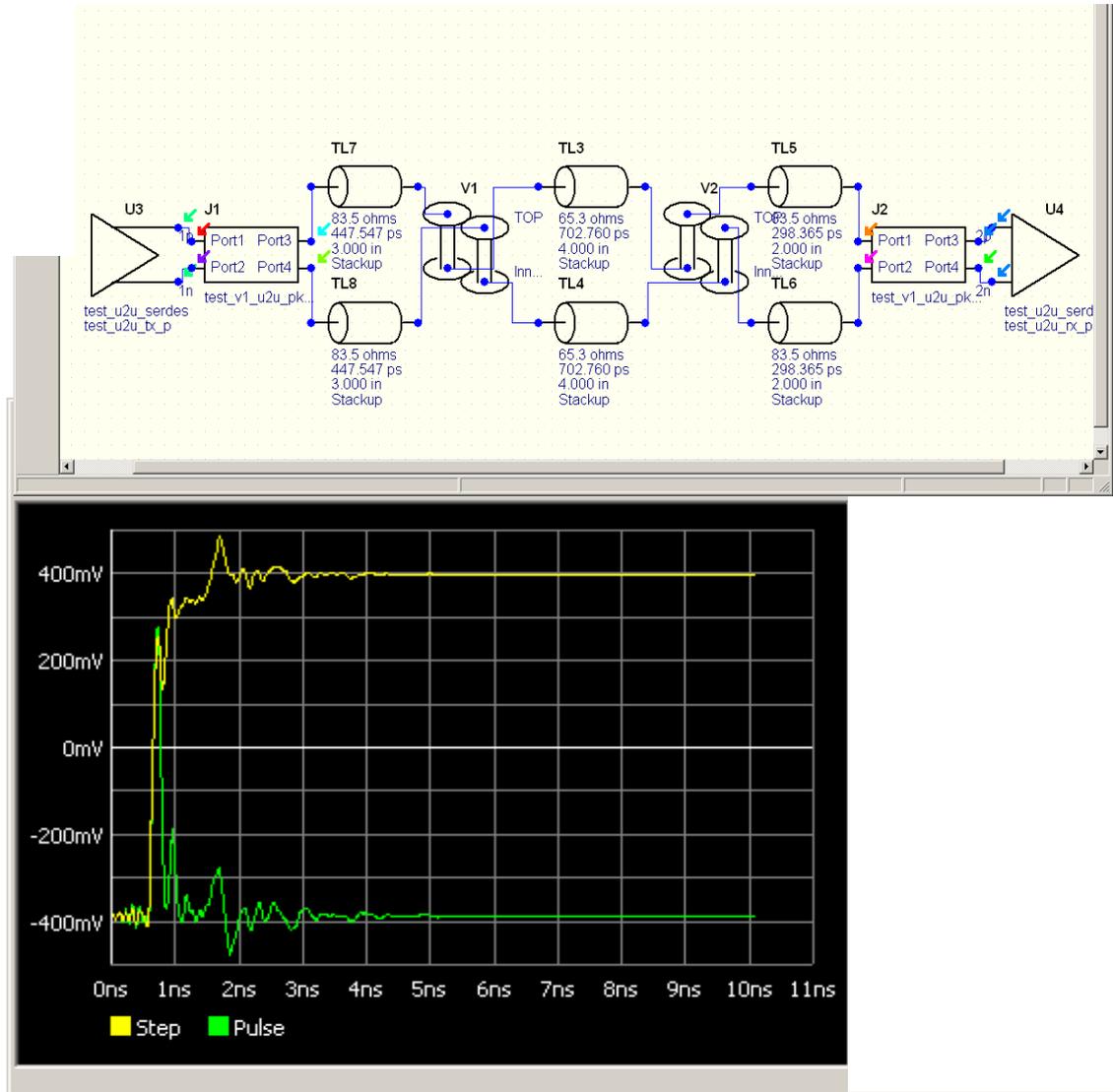
## Specific model parameters

- Tap settings
- Equalization settings
- Driver strength
- PVT conditions
- Gain
- .....

# IBIS-AMI topologie

## Impulse response

- Step response
- Impulse response
- Each topologie has its own impulse response



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# Selection of model specific parameters

## Specific model parameters

### ■ Tap 0

➤ Value 1

### ■ Tap 1

➤ Increment 0 -2 3 0.2

### ■ Equalization settings

➤ Range 0 , 0.5 , 1

### ■ Driver strength

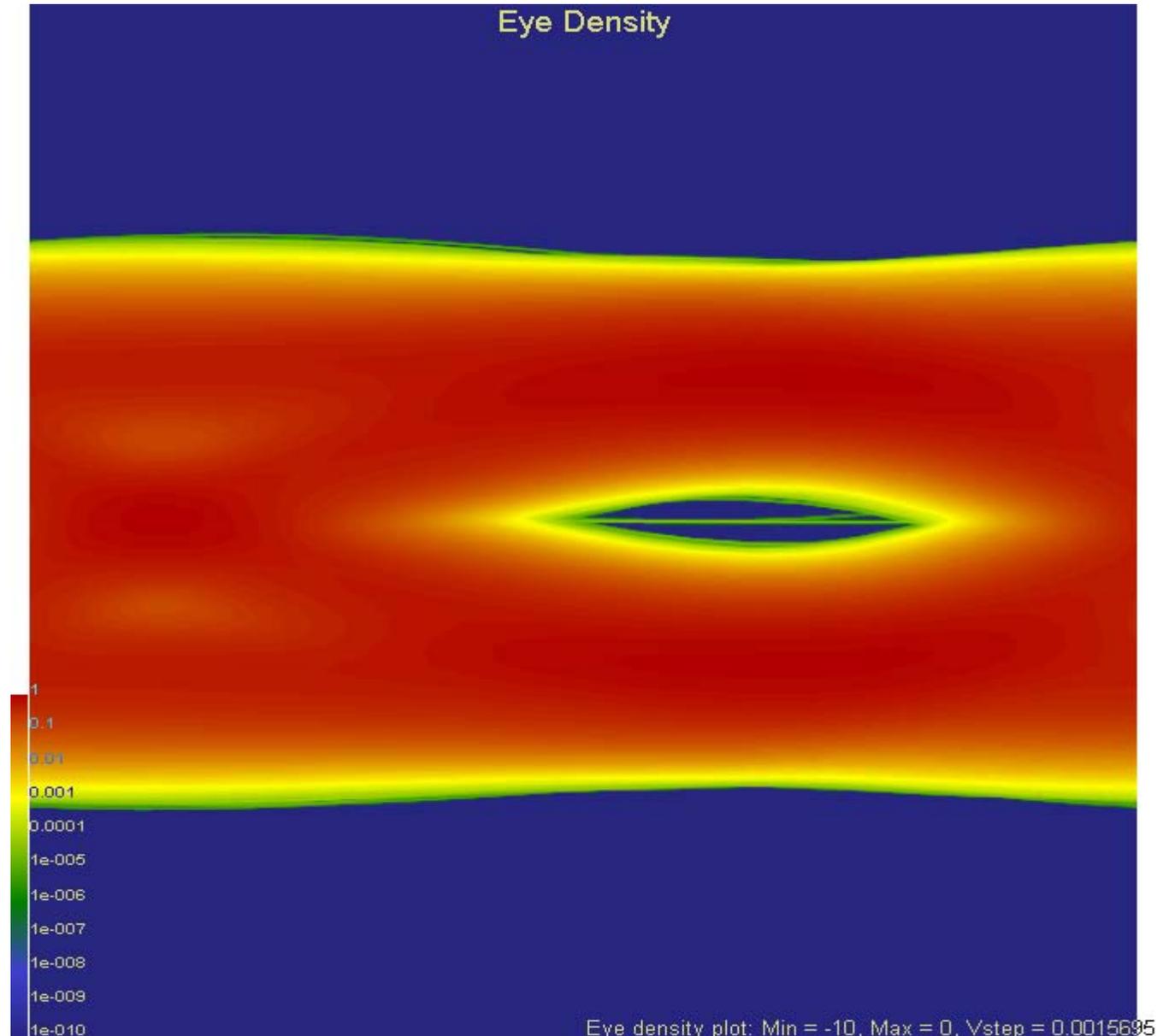
➤ list 200mv , 400mv , 600mv , 800mv

# IBIS-Ami first results

## Model settings :

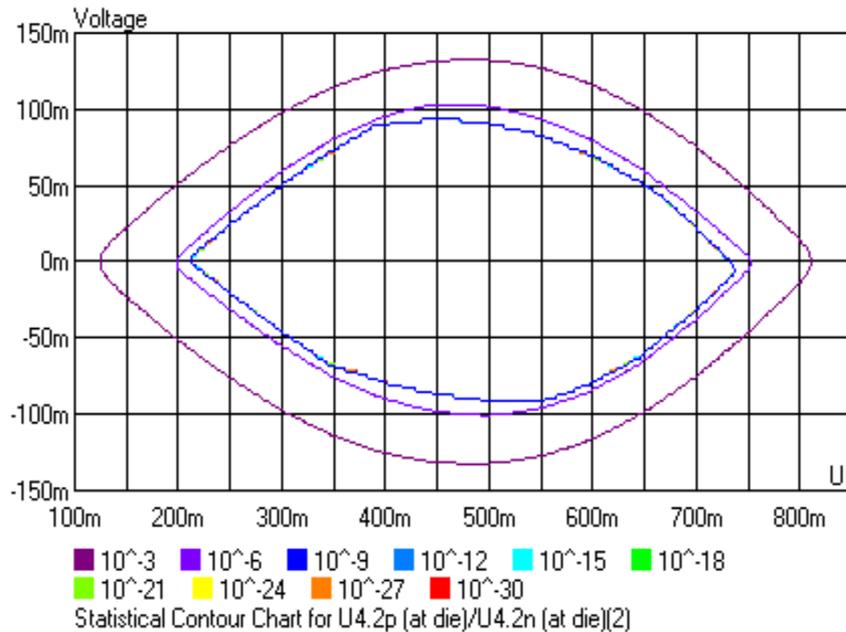
- Swing  
— 0 ... 63
- pretap  
— 0 ... 32
- posttap  
— 0 ... 63

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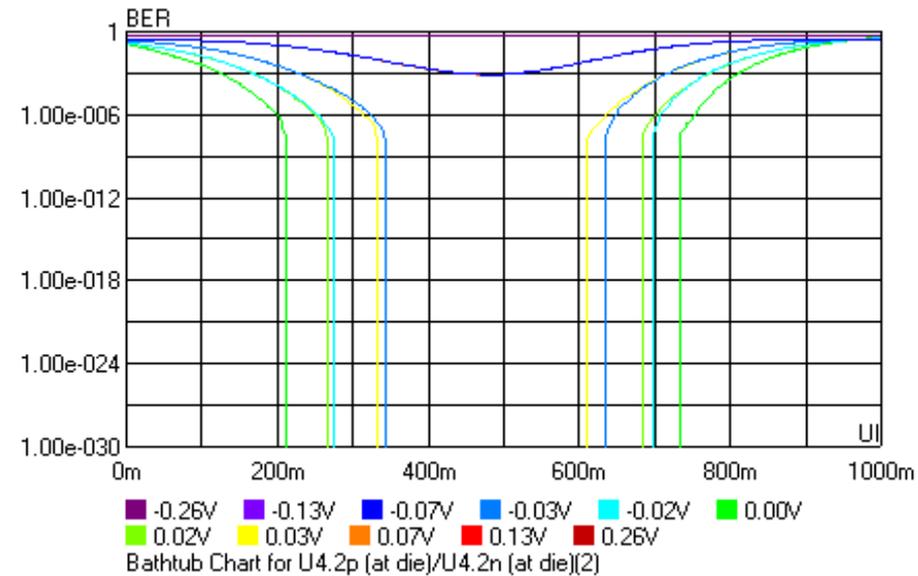


# IBIS-AMI first results

## Eye diagram



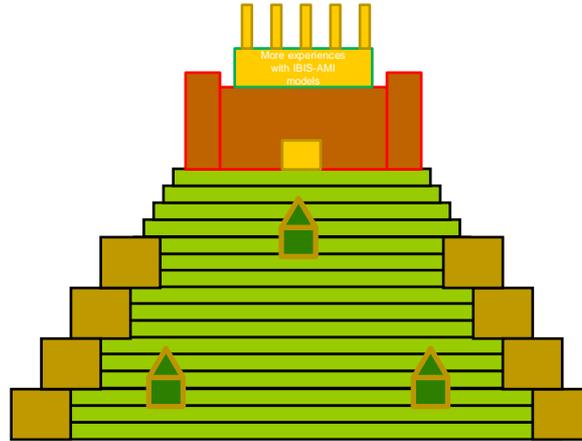
## BER



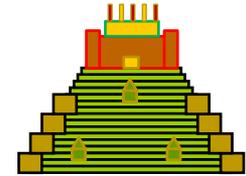
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# More experiences with IBIS-AMI models

summary



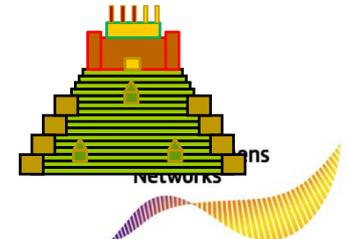
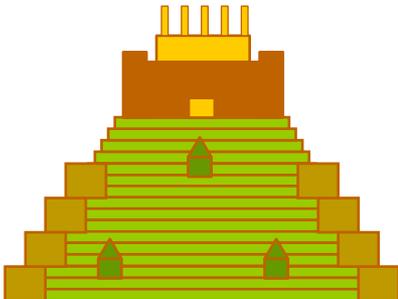
Why IBIS-AMI



IBIS-AMI  
models

Basics IBIS-AMI

Differences  
IBIS vs  
IBIS-AMI



# [Model] ALPHA

- Model\_type Open\_sink

■ **[Voltage Range]**      0.03V      0.02V      0.04V

- C\_comp 1.00p 0.95p 1.05p
- [Pullup]

- **[Algorithmic Model]**
- **Executable Windows\_VisualStudio\_32 NSNTx.dll NSNTx.am**
- **Executable Linux\_gcc3.2.3\_32 NSNlibTx.so NSNTx.am**
- **[End Algorithmic Model]**

# [Model] BETA

- Model\_type Output
- [Voltage Range] 1.8V 1.75V 1.85V
- C\_comp 0.80p 0.65p 0.95p
- [Pullup]
  
- [Algorithmic Model]
- Executable Windows\_VisualStudio\_32 NSNTx.dll NSNTx.am
- Executable Linux\_gcc3.2.3\_32 NSNlibTx.so NSNTx.am
- [End Algorithmic Model]
  
- **Model needs mathematical program ,but with specific release**
  
- **Model runs on windows XP, but not on Win7**

# [Model] GAMMA

- Model\_type Open\_sink
- [Voltage Range] 1.0V 0.92V 1.08V
- C\_comp 0.50p 0.45p 0.55p
- [Pullup]
  
- **[Algorithmic Model]**
- Executable Windows\_VisualStudio\_32 NSNTx.dll NSNTx.am
- Executable Linux\_gcc3.2.3\_32 NSNlibTx.so NSNTx.am
- **[End Algorithmic Model]**
  
- **Request for package models**
- **Answer : package model is inside dll,**
  
- **According IBIS-AMI spec this is not valid**
- **Package is a part of the channel**

# [Model] DELTA “not completed” .ami file

```
Model_type Open_sink  
[Voltage Range] 0.03V 0.02V 0.04V  
C_comp 1.00p 0.95p 1.05p  
[Pullup]
```

## [Algorithmic Model]

```
Executable Windows_VisualStudio_32 NSNTx.dll NSNTx.ami  
Executable Linux_gcc3.2.3_32 NSNiibTx.so NSNTx.ami
```

## IBIS-Ami file model10G\_file.ami

(Model\_Specific

(DRIVE (Usage In) (Type Integer)

**(Format Value 0 ) (Default 0) (Description “DRIVE”)**

## IBIS-AMI description file model10G-ami.pdf

(DRIVE) TX Swing TX Pre-emphasis

**0** **200mv** **3db**

**10** **400mv** **5db**

.....

**80** **800mv** **8db**

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# [Model] DELTA upgraded .ami file

```
Model_type Open_sink  
[Voltage Range] 0.03V 0.02V 0.04V  
C_comp 1.00p 0.95p 1.05p  
[Pullup]
```

## [Algorithmic Model]

```
Executable Windows_VisualStudio_32 NSNTx.dll NSNTx.ami  
Executable Linux_gcc3.2.3_32 NSNiibTx.so NSNTx.ami
```

## IBIS-Ami file model10G\_file.ami

(Model\_Specific

(DRIVE (Usage In) (Type Integer)

(**Format List 0 10 .....80**) (Default 0) (Description "DRIVE"))

## IBIS-AMI description file model10G-ami.pdf

(DRIVE) TX Swing TX Pre-emphasis

**0** **200mv** **3db**

**10** **400mv** **5db**

.....

**80** **800mv** **8db**

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# [Model] EPSILON & [Model] OMEGA

- **Model\_type** Open\_sink

- [Algorithmic Model]

- **Executable** Linux\_gcc3.2.3\_32  
**NSNTx.aml**

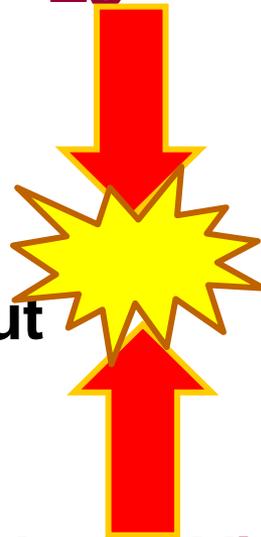
- [End Algorithmic Model]

- **Model\_type** Input

- [Algorithmic Model]

- **Executable** Windows\_VisualStudio\_32  
**NSNRx.aml**

- [End Algorithmic Model]



# [Model] ZETA

- Model\_type Open\_sink
- [Voltage Range] 0.03V 0.02V 0.04V
- C\_comp 1.00p 0.95p 1.05p

## ■ [Model Selector]

### ■ Swing10

■ .....

### ■ Swing630

ibis file  
Nsn.ibs



ibis ami file  
NsnTx.ami

## ■ [Algorithmic Model]

### ■ Executable Windows\_VisualStudio\_32 NSNTx.dll NSNTx.ami

(TX\_swing (Range 310 10 630) (Usage In)(Type Integer)(Default 310)

(Description "Transmitter swing"))

- Executable Linux\_gcc3.2.3\_32 NSNlibTx.so NSNTx.ami
- [End Algorithmic Model]

Public

# [Model] ETA

■ Executable Windows\_VisualStudio\_32 NSNTx.dll NSNTx.amf

■ (Reserved\_Parameters

(Rx\_Clock\_PDF (Usage Info) (Type Float) (Format Table

(Labels Row\_No Time\_UI Density)

(-3 -1.2 0.1)

( 3 1.17E-001 0.01)

) | End Table

) | End Rx\_Clock\_PDF

) | End Reserved\_Parameters

■ IBISCHK5 results in 2 errors :

■ E4630 - Parameter Format is mandatory for Rx\_Clock\_PDF

■ E4630 - Parameter Description is mandatory for  
Rx\_Clock\_PDF

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# [Model] ETA

Executable Windows\_VisualStudio\_32 NSNTx.dll NSNTx.ami

## ■ (Reserved\_Parameters

(Rx\_Clock\_PDF (Usage Info) (Type Float) **(Format Table**

(Labels Row\_No Time\_UI Density)

(-30 -1.17E-001 0)

( 30 1.17E-001 0)

) | End Table



**(Description "Receiver clock pdf is in table format.")**

) | End Rx\_Clock\_PDF

) | End Reserved\_Parameters

Bug in  
ibis parser

■ **E4630 - Parameter Format is mandatory for Rx\_Clock\_PDF**

■ **E4630 - Parameter Description is mandatory for Rx\_Clock\_PDF**

Error in  
ami file



# [Model] THETA no documentation supported

Model\_type Open\_sink

## IBIS-Ami file model8G\_file.ami

(Model\_Specific

(EQUAL (Usage In) (Type Integer) (Format List 0 ..... 8) (Default 0)

Labels " 0 001 0.0db "

.....  
" 8 110 9.5db : max "

(Description "EQUAL post tap settings ")



IBIS-AMI description file  
model8G-ami.pdf



(EQUAL) post  
0 ..... 001  
.....  
8 8db 110

## Data sheet model8G.txt

Equalization post-tap :

**0001** **no equalization**

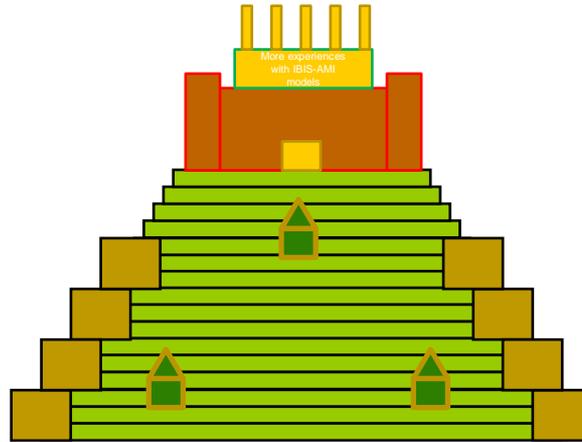
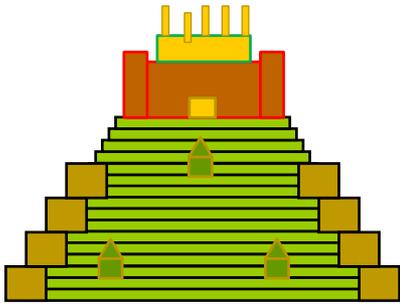
.....

**0110** **75%**

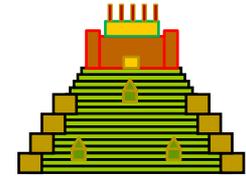
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# More experiences with IBIS-AMI models

summary



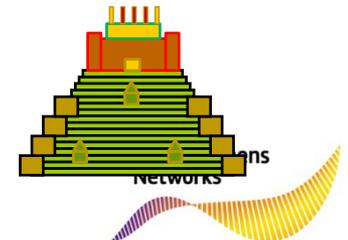
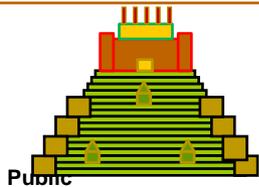
# Why IBIS-AMI



IBIS-AMI  
models

# Basics IBIS-AMI

# Differences IBIS vs IBIS-AMI



# Summary



- For  $> 5\text{Gbps}$  , IBIS,SPICE is too slow
- IBIS-AMI is a good solution for  $\text{BER} < 1\text{e-}12$
- Increasing model support from
  - IC-vendors
  - Tool vendors
- IBIS forum is working on enhancements
- IBIS-AMI is challenging

Thank you

Nokia Siemens  
Networks



Questions ?

for a  
**world**  
**in motion**<sup>TM</sup>