





- 8:30 Refreshments & Sign In
- 9:00 Welcome & Introductions Ralf Bruening, Zuken, Germany
- 9:15 Driver Schedule: Pre-/De-emphasis and Frequency/Data Rate Issues Eckhard Lenski, Nokia Siemens Networks, Germany
- 9:45 IQC IBIS Quality Checker Manfred Maurer\* and Christian Sporrer\*\*, \*Siemens AG, and \*\*Infineon AG, Germany
- 10:15 Proper IBIS Package Modeling Techniques and Usage in Ideal-PDS and SSO Simulations Sam Chitwood, Sigrity, USA (Presented by Brad Brim, Sigrity, USA)

## 10:45 COFFEE BREAK

- 11:00 Macromodels of IC Buffers Allowing for Large Power Supply Fluctuations Flavio Canavero, Ivan Maio and Igor Stievano, Politecnico di Torino, Italy (Presented by Igor Stievano, Italy
- 11:30 BIRD104.1 AMI Model New IBIS Support Manfred Maurer, Siemens AG, Germany
- 12:00 LUNCH BREAK
- 12:30 New Interconnect Models Remove Simulation Uncertainty (CANCELLED) Fangyi Rao\*, Chad Morgan\*\*, Vuk Borich\* and Sanjeev Gupta\*, \*Agilent Technologies and \*\*Tyc Electronics, USA (Presented by Saliou Dieye, Agilent, France)
- 13:15 Additional Discussion and Concluding Items
- 13:30 End of Meeting