





DATE European IBIS Summit
March the 10th, 2006



DATE 2006

 ICM Messe Munich,
 Munich, Germany




Agenda I

ZUKEN

8:30 REFRESHMENTS & SIGN IN

9:00 WELCOME AND INTRODUCTIONS
- Ralf Bruening, Zuken, Germany

9:15 SIG - SIEMENS IBIS GROUP
- Eckhard Lenski, Siemens AG, Germany

9:30 JEITA EDA - WG ACTIVITY AND STUDY OF INTERCONNECT MODEL,
Takeshi Watanabe, NEC Electronics Corporation, Atsuji Ito,
Panasonic, and Kazuhiko Kusunoki, KAW/Keihin Artwork, Japan

10:15 FIRST STEPS WITH [External Model] IN IBIS
- Katja Koller, Siemens AG, Germany

10:35 IBIS Indicator - A New IBIS Validation Tool
- Kazuyoshi Kusunoki, KAW/JEITA

11:05 BREAK (10 Minutes)

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Agenda II

11:15 ACCURACY OF IBIS MODELS WITH REACTIVE LOADS

- Arpad Muranyi, Intel Corporation, USA

11:35 IBIS MACRO MODEL LIBRARY - OVERVIEW

- Arpad Muranyi, Intel Corporation, USA

11:55 SSO SIMULATION WITH IBIS

- Manfred Maurer, Siemens AG, Germany

12:15 LUNCH/DISCUSSION (30 Minutes)

12:50 OPTIONAL: STATUS OF THE EMI MODELLING STANDARD

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Agenda III

13:00 INFLUENCE OF STIMULI PATTERNS TO MODEL BEHAVIOUR RISE AND FALL TIMES

Eckhard Lenski, Siemens AG, Germany

13:20 IC MACROMODELS FROM ON-THE-FLY TRANSIENT RESPONSES

Flavio Canavero, Ivan Maio, and Igor Stievano, Politecnico di Torino, Italy (Presented by Igor Stievano)

13:45 HDL AND IBIS 4.1 MODELS IN A FUNCTIONAL DDR MEMORY INTERFACE ANALYSIS

Simon Vines, Mentor Graphics, United Kingdom

14:15 CLOSING TOPICS/DISCUSSION

- Ralf Bruening, Zuken, Eckhard Lenski, Siemens AG, Germany

14:45 END OF MEETING

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Presentations:

⇒ <http://www.eda.org/pub/ibis/summits/mar06/>



⇒ Thank YOU for making this event – looking forward to meeting you at DATe2007

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⇒ **Room 21**



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