

Chair's Status Report



Michael Mirmak
Intel Corp.
Chair, IBIS Open Forum

IBIS Summit
at DAC 2014
San Francisco, CA
June 5, 2014

*Portions presented at the 2014
European IBIS Summit*

<http://www.eda.org/ibis/>

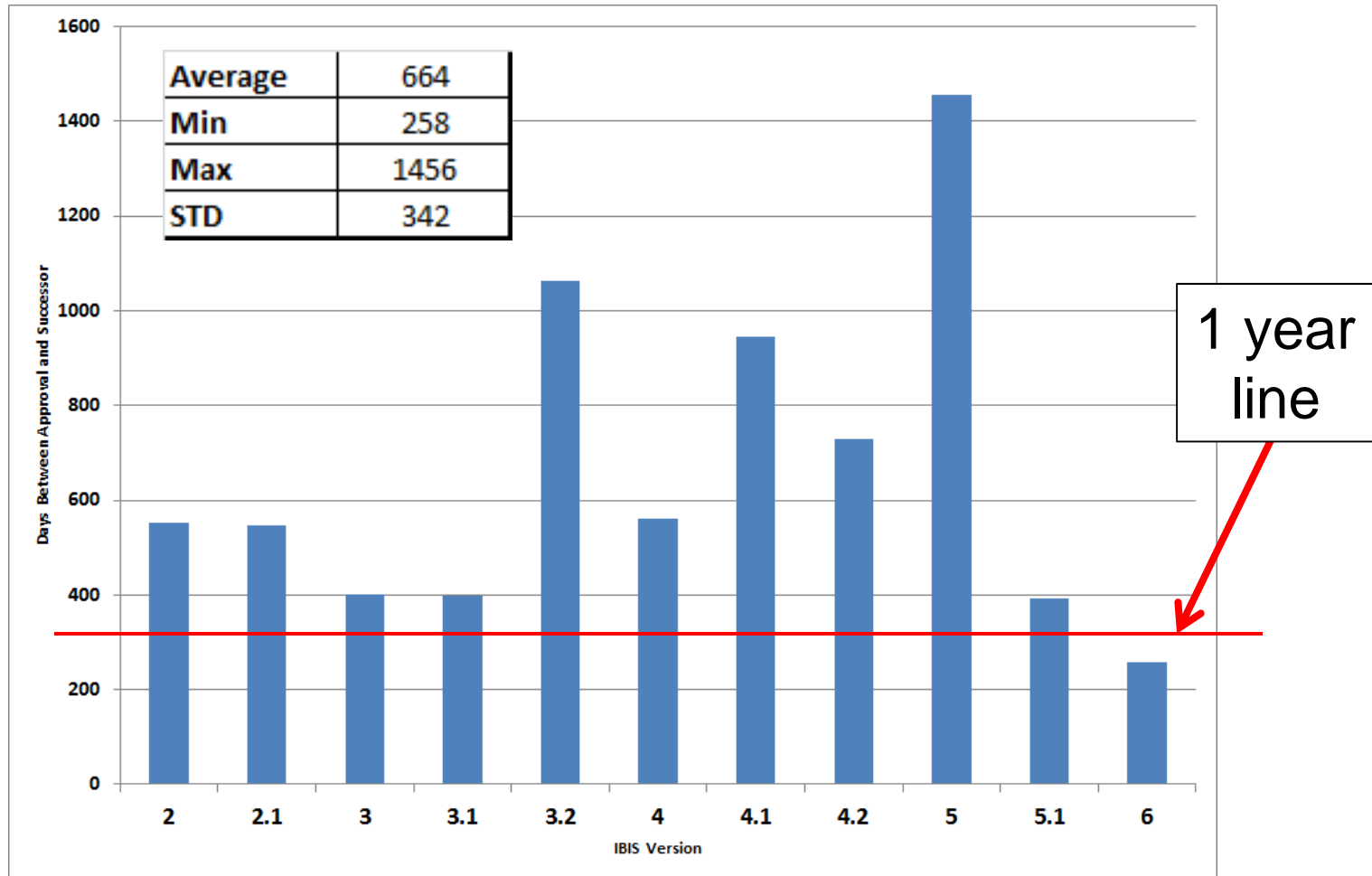
Agenda

- ❑ IBIS Development Data
 - BIRD and Version Life Cycles

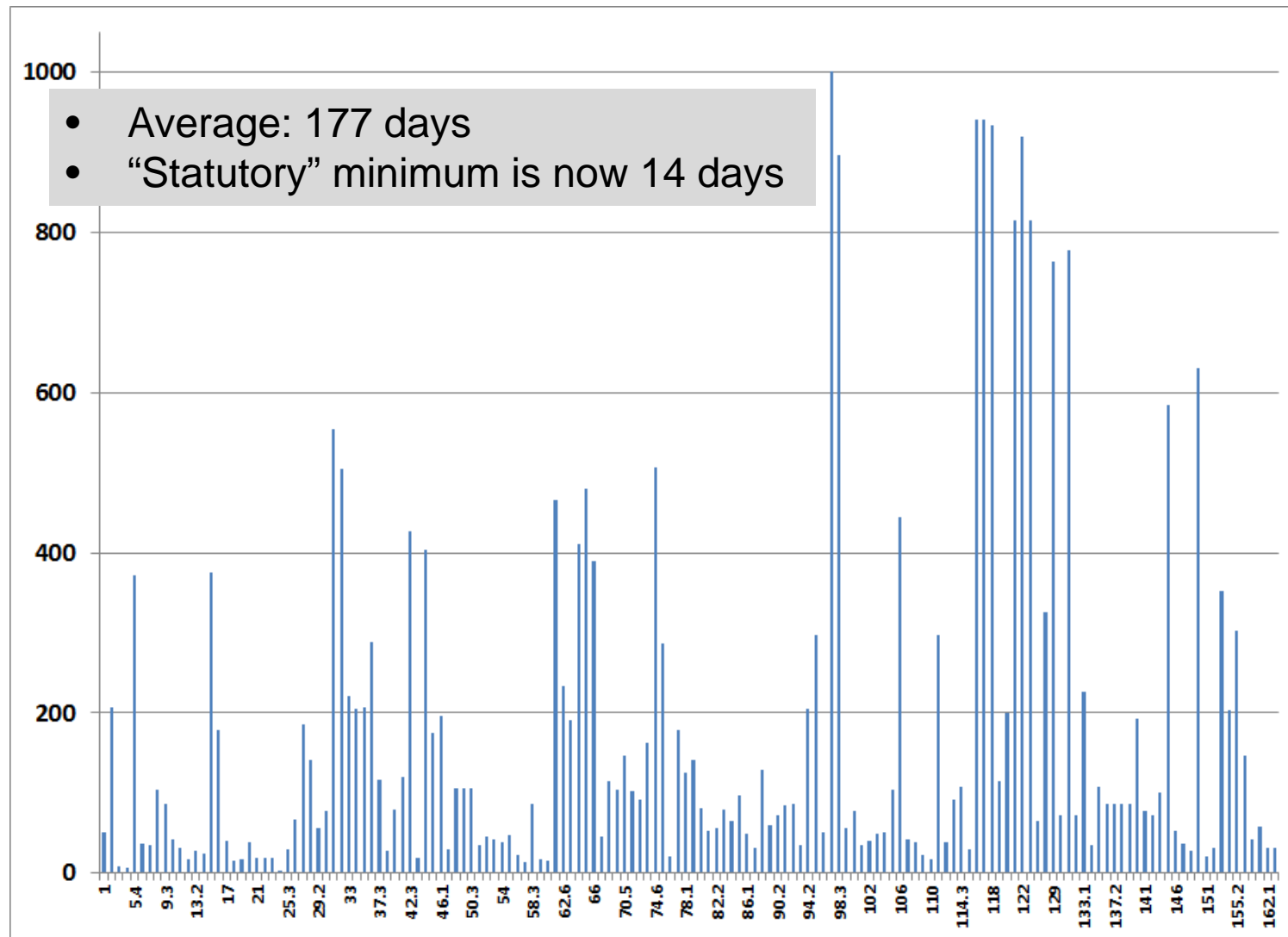
- ❑ Planning for the Future
 - Annual Development Schedule Proposal
 - Proposal for Discussion

IBIS DEVELOPMENT DATA

How Long Do Versions “Live”?



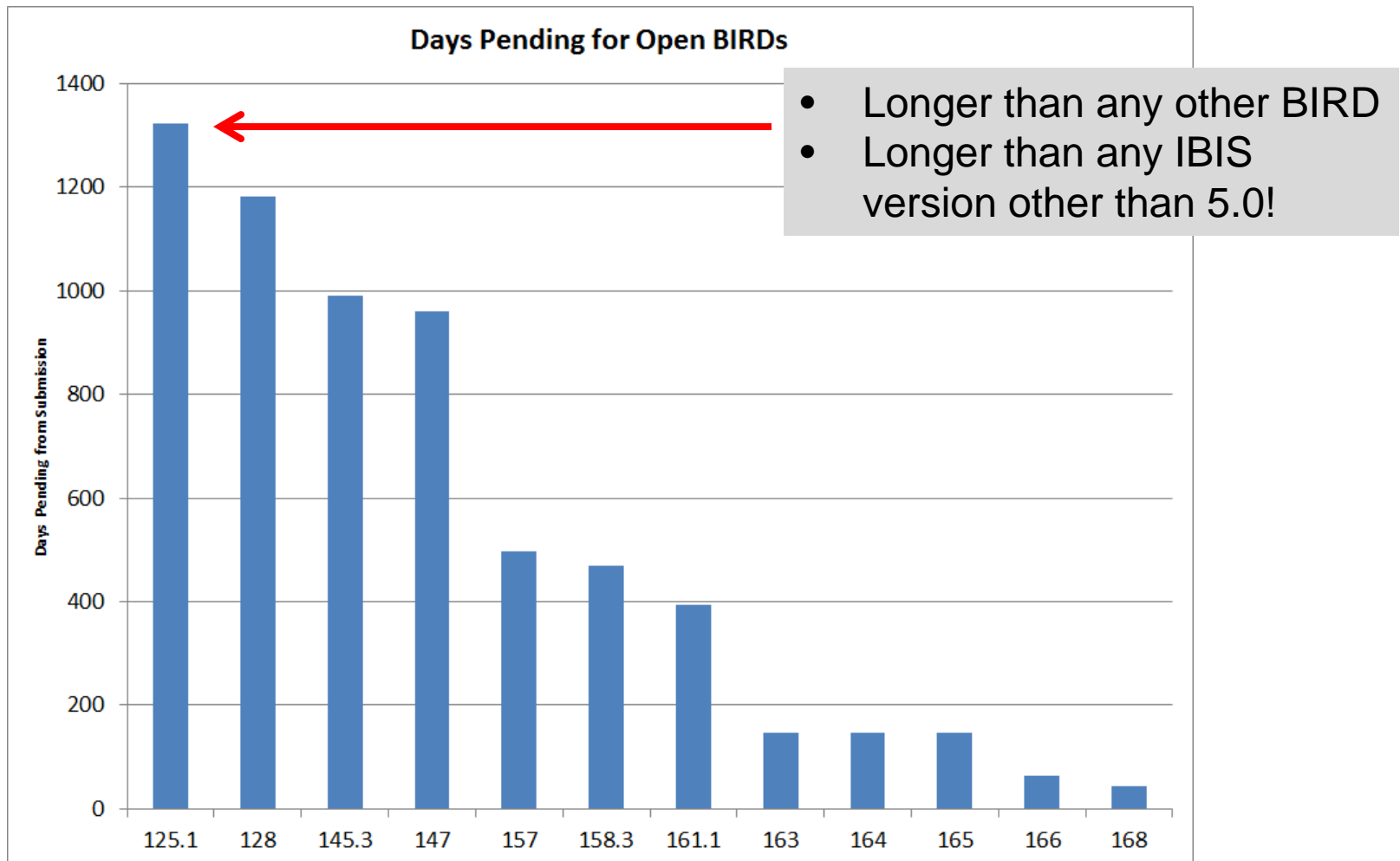
How Long are BIRDs Considered?



2014 IBIS Summit at DAC

Pending BIRDs

- 12 BIRDs are open, on packages & advanced AMI



2014 IBIS Summit at DAC

PLANNING THE FUTURE

Release Schedule for IBIS

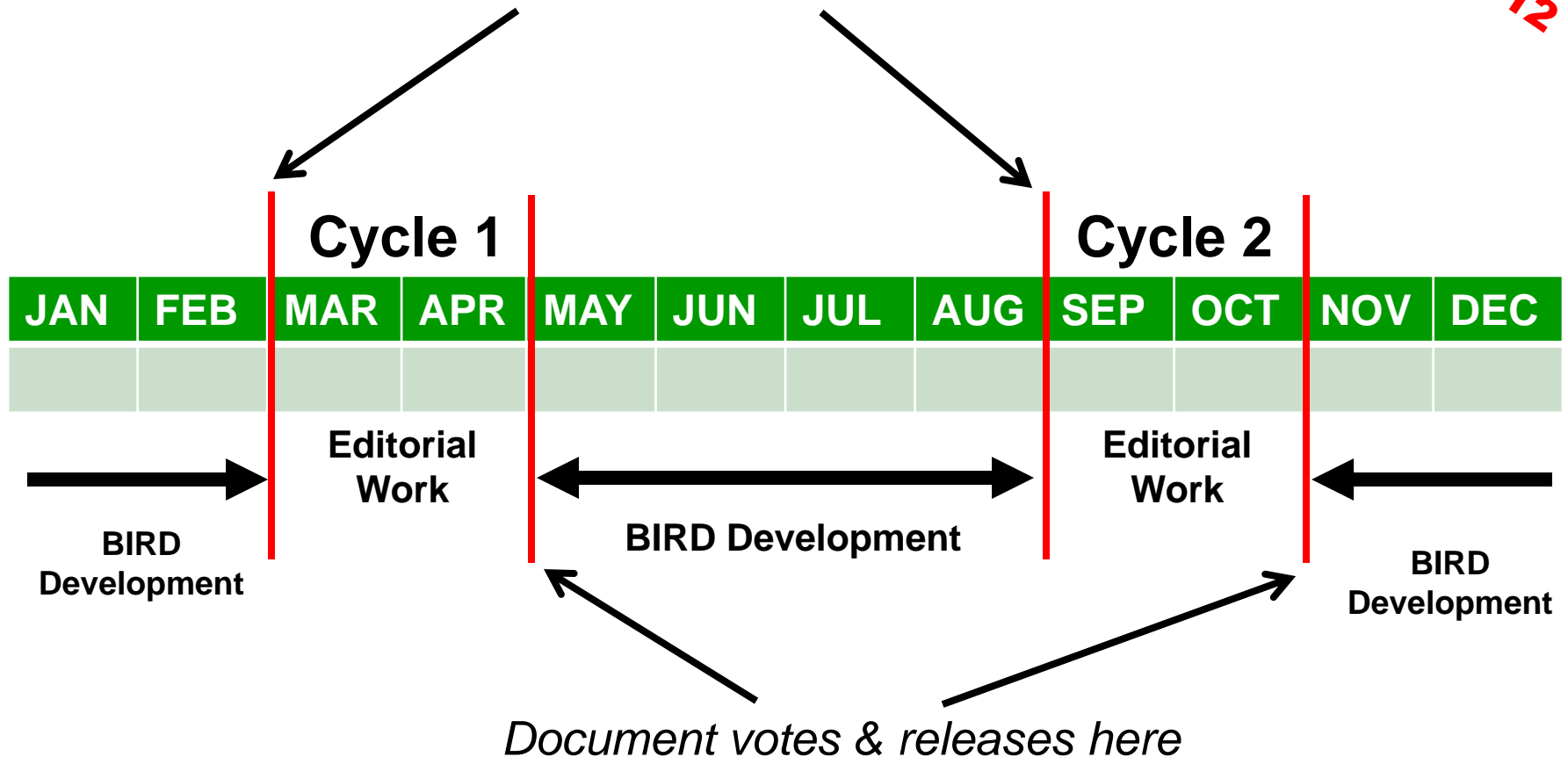
From
Feb. 2012

- ❑ Moving to a regular, software-like release plan
 - Allows better planning by “customers” (IBIS community)
 - Keeps number of changes manageable
 - Reduces & stabilizes parser development cycle time and cost
- ❑ Two “cycles”, each with two parts
 - Cycle 1
 - Nov. – Feb.: technical development
 - March 1 – close technical changes
 - May 1 – close final document (editorial fixes in-between)
 - Cycle 2
 - May – Aug.: technical development
 - Sept. 1 – close technical changes
 - Nov. 1 – close final document (editorial fixes in-between)

How Does This Work?

**From
Feb. 2012**

Parser development can start as early as this point



REVISED PROPOSAL

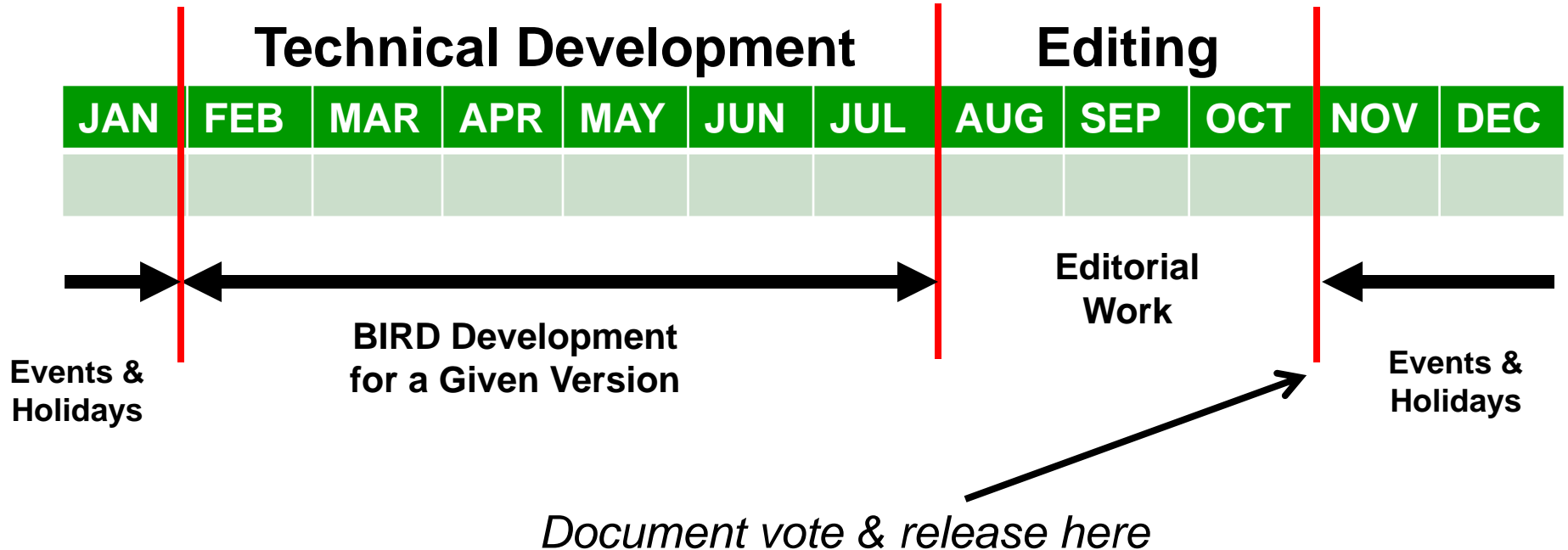
Release Schedule for IBIS

- ❑ Need to move to 9+ month cycle
 - Feedback from 2014 DesignCon IBIS Summit

- ❑ New proposal: annual cycle with three parts
 - Technical Period
 - *February - July*
 - *August 1 - close technical changes*
 - Editorial Period
 - *August - September*
 - *Nov. 1 - close & vote on final document*
 - Quiet Period (Events, Holidays)
 - *November - January*

How Does This Work?

Parser development can start as early as this point



Why This Schedule?

- ❑ Calendar can limit potential interactions
 - End-of-year holidays/shutdowns
 - Fall Asian IBIS Summits and preparation
 - New Year celebrations in Asia
 - DesignCon Summit and preparation
 - July/August vacation schedule in Europe

- ❑ Member companies are likely affected by the same calendar items
 - Peak work periods are Sept. - Oct. and March - May

IBIS Numbering Proposal

- ❑ Specification moves to date codes
 - Year and month of approval (e.g., 2014.11)
 - Multiple releases in the same month unlikely
 - Major/minor release distinction goes away
 - [IBIS Ver] and AMI_Version affected

- ❑ Parser versions track specifications
 - Parser uses specification release number, for features
 - Parser updates use additional numbers
 - *e.g., 2014.11 r01 or similar*
 - Name changes to simply “ibischk”

Implications

- ❑ If implemented immediately...
 - Next version is not 6.x or 7, but 2014.11
 - BIRDs for 2014.11 must be approved on/before Aug. 1, 2014
- ❑ Membership dues in 2015
 - Parser development fees become part of membership dues

Is this schedule realistic for the IBIS community?
Can tools and design cycles absorb annual updates?

Q/A

Pending BIRDs

ID#	Issue Title	Requester	Submitted	Days Open
125.1	Make IBIS-ISS Available for IBIS Package Modeling	Arpad Muranyi, Mentor Graphics	21-Oct-10	1323
128	Allow AMI parameters out to pass AMI parameters in data on calls to AMI GetWave	Walter Katz, SiSoft	11-Mar-11	1182
145.3	Cascading IBIS I/O buffers with [External Circuit]s using the [Model Call] keyword	Taranjit Kukal, Ambrish Varma, Cadence Design Systems, Inc. Arpad Muranyi, Mentor Graphics.	19-Sep-11	990
147	Back-channel support	Marcus Van Ierssel, Snowbush IP; Kumar Keshavan, Sigrity, Inc.; Ken Willis, Sigrity, Inc.; Walter Katz, SiSoft	18-Oct-11	961
157	Parameterize [Driver Schedule]	Arpad Muranyi, Mentor Graphics; Romi Mayder, Xilinx	24-Jan-13	497
158.3	AMI Touchstonefile (R) Analog Buffer Models	Walter Katz, Signal Integrity Software, Inc.	20-Feb-13	470
161.1	Supporting Incomplete and Buffer-only [Component] Descriptions	Michael Mirmak, Intel Corp.	8-May-13	393
163	Instantiating and Connecting [External Circuit] Package Models with [Circuit Call]	Arpad Muranyi & John Angulo, Mentor Graphics; Ambrish Varma & Brad Brim, Cadence Design Systems, Inc.	9-Jan-14	147
164	Allowing Package Models to be defined in [External Circuit]	Ambrish Varma & Brad Brim, Cadence Design Systems, Inc.; Arpad Muranyi, Mentor Graphics	9-Jan-14	147
165	Parameter Passing Improvements for [External Circuit]s	Arpad Muranyi & John Angulo, Mentor Graphics; Ambrish Varma & Brad Brim, Cadence Design Systems, Inc.	9-Jan-14	147
166	Resolving problems with Redriver Init Flow	Walter Katz, Signal Integrity Software, Inc. Darshan Shah, F5Networks, Inc.	2-Apr-14	64
168	Handling of Overclocking Caused by Delay in Waveform Tables	Radek Biernacki and Ming Yan, Agilent Technologies, Inc.; Justin Butterfield and Randy Wolff, Micron Technology, Inc.	22-Apr-14	44