IBIS SUMMIT MEETING
Thursday, June 5, 2014

Room 220
Moscone Center South, East Mezzanine

Presentations posted to http://www.eda.org/ibis/summits/

Co-sponsored by the EDA Consortium and the IBIS Open Forum

(order and times subject to change)

8:00 AM   Refreshments & Sign In

8:30 AM   Official Opening and Welcome
              - Introductions
              - Opens for Issues, Discussion Topics

8:45 AM   **Chair's Status Report**
              Michael Mirmak, Intel Corporation

9:15 AM   **Introduction of P2401 LSI-Package-Board Standard Format**
              Genichi Tanaka, Renesas Electronics

10:00 AM   BREAK

10:20 AM   **IBIS Interconnect BIRD**
              Walter Katz, Signal Integrity Software (SiSoft)

11:20 AM   **IBISCHK5/6 Specification Document Update**
              Bob Ross, Teraspeed Consulting Group
              Mike LaBonte, Signal Integrity Software (SiSoft)

11:50 AM   ELECTION OF OFFICERS

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12:05 PM   LUNCH
Pre-registration required

1:00 PM   **IBIS File Inspection Using ibisinf - A Basic Utility for the 80/20 Rule**
Michael Schaeder, Zuken
Presented by Bob Ross, Teraspeed Consulting Group

1:30 PM   **Solving Receiver Electrical Test Challenges using IBIS-AMI Modeling Techniques**
Venkatesh Avula, Avago

2:15 PM   **The Backchannel Crossroads**
Ken Willis, Ambrish Varma, Kumar Keshavan, and Brad Brim,
Cadence Design Systems
Presented by Brad Brim, Cadence Design Systems

2:35 PM   **Co-Optimization of SerDes Channels using AMI Modeling**
Walter Katz, Signal Integrity Software (SiSoft)

2:55 PM   IBIS-AMI Backchannel Discussion

3:10 PM   BREAK

3:25 PM   **IBIS Summary Documents**
Bob Ross, Teraspeed Consulting Group

3:55 PM   Open Discussion and Ad Hoc Presentations

4:45 PM   Concluding Items
- Next IBIS Open Forum Teleconference: June 20, 2014

5:00 PM   End of Summit