IBIS SUMMIT MEETING
Tuesday, June 7, 2011

Gallery 2
The Omni's Gallery Space
Omni Hotel

Presentations at http://www.eda.org/ibis/summits/

Co-sponsored by the IBIS Open Forum and Mentor Graphics

8:00 AM   Refreshments & Sign In

8:30 AM   Call to Order
- Welcome to Summit
- Introductions
- Opens for Issues, Discussion Topics

8:45 AM   **IBIS in Review**
Michael Mirmak, Intel Corporation

9:00 AM   **IBIS PDN Feature Studies**
Randy Wolff, Micron Technology
Lance Wang, IO Methodology

9:45 AM   **IBIS-ATM Task Group Report**
Arpad Muranyi, Mentor Graphics Corporation

10:00 AM   BREAK

10:15 AM   **Compliant IBIS Package Models**
Walter Katz, Signal Integrity Software (SiSoft)

10:45 AM   **Comparison of the Recent Analog Modeling BIRD Proposals**
Arpad Muranyi, Mentor Graphics Corporation

11:15 AM   **IBIS in Academia**
Bob Ross, Teraspeed Consulting Group

11:45 AM   Election of Officers

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12:00 PM   LUNCH
Pre-registration required

1:00 PM   **AMI Modeling Methodology and Measurement Correlation of a 6.25 Gb/s Link**
Ryan Coutts, NetLogic Microsystems
Antonis Orphanou, NetLogic Microsystems
Manuel Luschas, NetLogic Microsystems
Amolak Badesha, Agilent Technologies
Nilesh Kamdar, Agilent Technologies

1:45 PM   **BIRD Proposal: Extending IBIS-AMI to Support Back-Channel Communications**
Marcus Van Ierssel, Snowbush IP (Gennum)
Kumar Keshavan, Sigrity
Ken Willis, Sigrity
Walter Katz, Signal Integrity Software (SiSoft)

2:15 PM   **Compliant IBIS-AMI Models**
Walter Katz, Signal Integrity Software (SiSoft)
Todd Westerhoff, Signal Integrity Software (SiSoft)

3:15 PM   BREAK

3:30 PM   Open Discussion and Ad Hoc Presentations
- IBIS 5.1 and 5.2 Planning
- Discussion on Previous and New Material

4:45 PM   Concluding Items
- Next Meeting: June 24, 2011
- Next Summits in Asia

5:00 PM   End of Summit