AGENDA, IBIS SUMMIT MEETING
June 10, 2008

Co-sponsors:
IBIS Open Forum, Mentor Graphics

8:00 AM — Refreshments & Sign In

8:30 AM — Introductions
- Welcome to Summit
- Introductions
- Opens for Issues, Discussion Topics

8:45 AM — **IBIS Chair's Report**
Michael Mirmak, Intel Corporation

9:00 PM — **Common Issues in Models Submitted to the IBIS Model review Committee**
Lynne Green, Green Streak Programs

9:30 AM — **Case Study of Scheduled Single-ended Driver Featuring [Test Data]**
Michael Mirmak, Intel Corp.

10:15 AM — **BREAK**

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10:30 AM   **Correlating IBIS-AMI Models**
Barry Katz, Walter Katz, Mike Steinberger, Todd Westerhoff
Signal Integrity Software (SiSoft)

11:00 AM   **IBIS-AMI Modeling**
Arpad Muranyi, Mentor Graphics Corporation

11:30 AM   Election of Officers

12:00 PM   LUNCH - Pre-registration required

1:00 PM   **Power Integrity for Single Ended Systems**
Vishram Pandit and Myoung Joon Choi, Intel Corporation

1:45 PM   **An Overview of Electrical Modeling and Model
Representations for Package/Board Interconnects and
Power Delivery Networks**
Brad Brim and Sam Chitwood, Sigrity

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2:15 PM   **Touchstone Topics**
                 Bob Ross, Teraspeed Consulting Group

2:45 PM   BREAK

3:00 PM   **Next Generation IBIS Modeling**
                 Paul Franzon, North Carolina State University

3:30 PM   Opens/Discussion
                 - Miscellaneous Topics
                 - Ad Hoc Presentations

4:55 PM   Concluding Items
                 - Next Open Forum Meeting: June 27, 2008
                 - Summits in Asia

5:00 PM   End of IBIS Summit Meeting