

IBIS Chair's Report and Roadmap

Michael Mirmak

Intel Corp.

Chair, EIA IBIS Open Forum

IBIS Summit

at DAC 2007

June 5, 2007



<http://www.eigroup.org/ibis/>
<http://www.eda-stds.org/ibis/>

Agenda

- ★ Organizational Status
- ★ Task Groups
- ★ BIRD Queue
- ★ Special Thanks
- ★ Discussion
- ★ Backup

Organizational Status

- ★ Parser and Specification Development
 - IBIS 4.2 now an ANSI standard: ANSI/EIA-656-B!
 - IBISCHK4 version 4.2.1 released!
- ★ 2007 Events
 - Successful summits in San Jose, CA and Nice, France
 - Planning September events in Beijing and Tokyo
- ★ Finances and Membership
 - Closed 2006 with positive budget and membership of 34
 - 2007 renewals and income trends are strong

**IBIS continues to be organizationally healthy and
recognized world-wide**

IBIS Task Groups

IBIS Committee
(“IBIS Open Forum”)

Ad Hoc - Interconnect
Michael Mirmak, Intel

**Advanced Technology
Modeling Group**
Arpad Muranyi, Intel

Model Review
Lynne Green,
Green Streak Programs

Quality
Mike LaBonte, Cisco

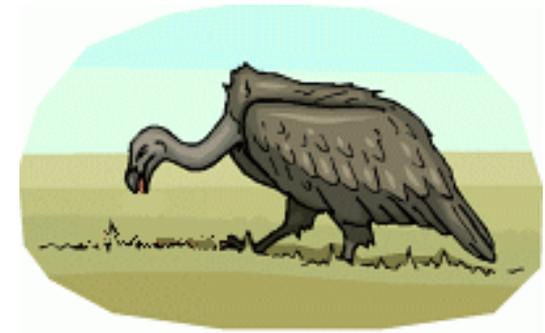
Task Groups

* Activity Updates

- You will hear about considerable ATM and Quality progress later
- Model Review is available to assist you in improving your models
- Interconnect has completed Touchstone® upgrade proposal
 - * Copies available on-line and here at the summit
 - * To be submitted to the IBIS Open Forum for June 22, 2007 meeting

Your participation is encouraged!

BIRD Queue



★ BIRDS for IBIS 5.0

- BIRD74.6 – EMI Parameters
- BIRD95.6 – Power Integrity Analysis using IBIS
- BIRD103.1 – [Model Spec] DDR2 Over/Undershoot
- BIRD97.2/98.1 – Gate Modulation Effect

Draft BIRDS

- BIRD1xx – ICM-IBIS Linking
- BIRD1xx – Algorithmic Modeling API

Proposed BIRDS (Draft Text Not Yet Available)

- BIRD1xx – [External Model] under [Circuit Call]
- BIRD1xx – User-Defined Measurements
- BIRD1xx – SPICE Parameter Passing

IBIS 5.0 will be a major advance!

Thanks to Key Players

★ Our members and parser owners

- Actel
- Agere
- Agilent
- AMD
- Ansoft
- Apache Design Solutions
- Aplac*
- Apple
- Applied Simulation Technology
- Cadence Design Systems
- Cisco
- CST*
- Ericsson
- Fluent
- Freescale
- Fujitsu*
- Green Streak Programs
- Hitachi ULSI Systems
- Huawei
- ICS
- Intel
- Legend Design*
- LSI
- Mentor Graphics
- Micron Technology
- NEC
- Panasonic (Matsushita)
- Samtec
- Siemens
- Signal Integrity Software
- Sigrity
- Silego
- Silvaco*
- STMicroelectronics
- Synopsys
- Teraspeed Consulting Group
- Texas Instruments
- Toshiba
- Xilinx
- ZTE
- Zuken

2007 IBIS Summit at DAC

* non-member parser licensee

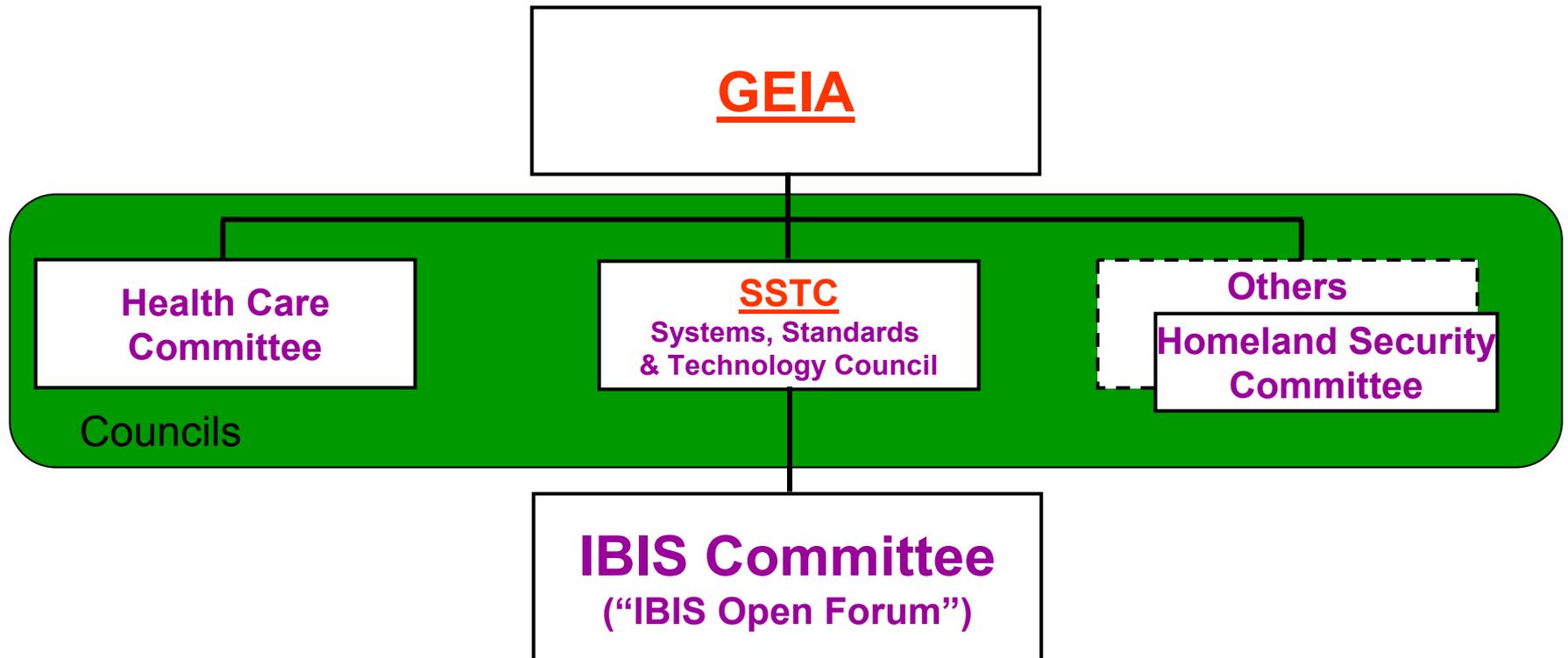
Thanks to Key Players

- * Our board staff
 - **Syed Huq – Vice-Chair**
 - * *especially for US summit logistics*
 - **Randy Wolff – Secretary**
 - * *handling official documents plus EIA/ANSI relations*
 - **Lance Wang – Model Librarian**
 - * *keeping our external IBIS model library up-to-date*
 - **Bob Ross – Postmaster/”Parliamentarian”**
 - * *maintaining reflectors and our institutional memory*

... and of course to you, the IBIS Community!
Thanks for another great year!

BACKUP

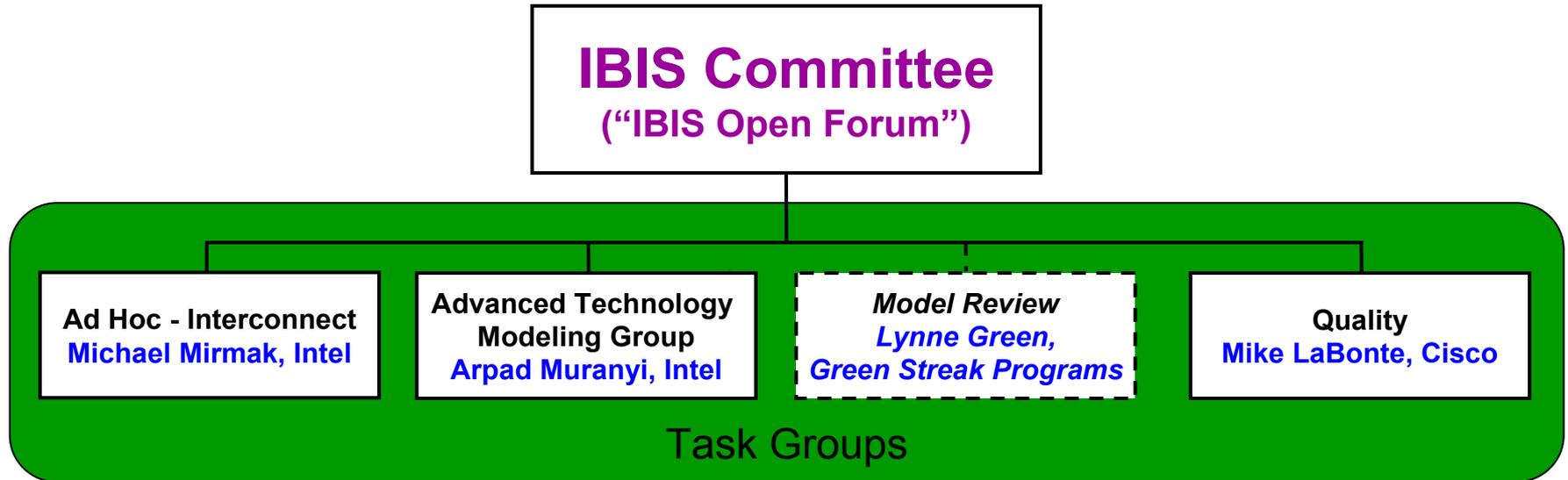
IBIS Structure



Notes

- *GEIA is an ANSI-accredited standards body*

IBIS Task Groups



Notes

- *The Cookbook Task Group has been suspended until critical mass of needed changes is achieved*
- *The Futures and Macromodeling Library groups have been combined into the Advanced Technology Modeling Group*
- *EDA vendors comprise Model Review, independently reviewing received models and providing feedback to model authors; no specification changes are recommended by this group and it does not hold meetings*
- Participation in the IBIS Task Groups is open to IBIS members and the general public alike
- Task Groups are created and Chairs appointed by the IBIS Chair
- Some issues are handled by Ad Hoc teams for BIRD development

IBIS Board

