

AGENDA, IBIS SUMMIT MEETING

June 5, 2007

Co-sponsors:

IBIS Open Forum, Mentor Graphics

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|---------|---|
| 8:00 AM | Refreshments & Sign In |
| 8:30 AM | Introductions <ul style="list-style-type: none">- Welcome to Summit- Introductions- Opens for Issues, Discussion Topics |
| 8:45 AM | IBIS Chair's Report and Roadmap
Michael Mirmak, Intel Corporation |
| 9:00 PM | IBIS Quality Report
Mike LaBonte, Cisco Systems |
| 9:15 AM | Measuring IBIS-to-SPICE Correlation Using Waveform Metrics
David Banas, Xilinx Corporation |
| 9:45 AM | IBISCHK4 Version 4.2.1 and Funny IBIS Models
Bob Ross, Teraspeed Consulting Group |

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| 10:15 AM | BREAK |
| 10:30 AM | More on Initial Time Delay Issues
Lance Wang, IO Methodology |
| 11:00 AM | Correlation of Model Simulations and Measurement
Methods of Quantifying Data Correlations
Roy Leventhal, Leventhal Designs and David Banas, Xilinx Corporation |
| 11:45 AM | Election of Officers |
| 12:00 PM | LUNCH - Pre-registration required |
| 1:00 PM | Asian IBIS Summit Review
Bob Ross, Teraspeed Consulting Group |
| 1:15 PM | Interfacing “2N” and “N+ref” Behavioral Models
Sam Chitwood, Sigrity |

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| 1:45 PM | The 3S Proposal:
A SPICE Superset Specification for Behavioral Modeling
Michael Mirmak, Intel Corporation |
| 2:15 PM | The *-AMS Experience
Arpad Muranyi, Intel Corporation |
| 2:45 PM | BREAK |
| 3:00 PM | IBIS-ATM Update: SerDes Modeling and IBIS
Todd Westerhoff, Signal Integrity Software (SiSoft) |
| 4:00 PM | Opens/Discussion |
| 4:55 PM | Concluding Items
- Next Open Forum Meeting: June 22, 2007 |
| 5:00 PM | End of IBIS Summit Meeting |