IBIS SUMMIT MEETING
July 25, 2005

Argent Hotel
50 Third Street
San Francisco, CA

The City Room

Co-sponsors:
Cisco Systems, Green Streak Programs, IBIS Open Forum, Mentor Graphics

8:00 AM   Refreshments & Sign In

8:30 AM   Introductions
               - Welcome to Summit
               - Introductions
               - Opens for Issues, Discussion Topics

8:45 AM   **IBIS Roadmap**
               Michael Mirmak, Intel Corporation

9:00 PM   **IBIS Quality Designations**
               Mike LaBonte, Cisco Systems

9:15 AM   **Macromodeling Committee Activities**
               Arpad Muranyi, Intel Corporation

9:45 AM   **A Standards-based Approach to IP Protection for HDLs**
               John Shields, Mentor Graphics

10:30 AM   BREAK

10:45 AM   **Serdes Introduction and Macro/AMS Modelling**
               Richard Ward, Texas Instruments

11:15 AM   **Algorithm Modeling Approach for Serdes Devices**
               Lance Wang, Cadence Design, and Joe Abler, IBM

11:45 AM   Election of Officers

12:00 PM   LUNCH - Pre-registration required

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1:00 PM   **Spice2Ibis - Status and Planned Improvements**
Paul Franzon, North Carolina State University

1:30 PM   **Buffers for Advanced SPICE to IBIS Testing**
Bob Ross, Teraspeed Consulting Group

2:00 PM   **IBIS in the Frequency Domain**
Michael Mirmak, Intel Corporation

2:30 PM   **Gate Modulation Effect and BIRD97/98**
Arpad Muranyi, Intel Corporation

3:00 PM   BREAK

3:15 PM   **DDR2 Electrical and Timing Measurements in VHDL-AMS**
Gary Pratt, Mentor Graphics

3:45 PM   **Integration Issues in using IBIS Multi-lingual Models**
Ian Dodd, Mentor Graphics

4:00 PM   **IBIS Summary and Evolution**
Bob Ross, Teraspeed Consulting Group

4:30 PM   Opens/Discussion

4:55 PM   Concluding Items
- Next Open Forum Meeting: Friday, August 4, 2006

5:00 PM   End of IBIS Summit Meeting