

Chair's Status Report



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IBIS Summit
at DesignCon 2014
January 31, 2014

<http://www.eda.org/ibis/>

Agenda

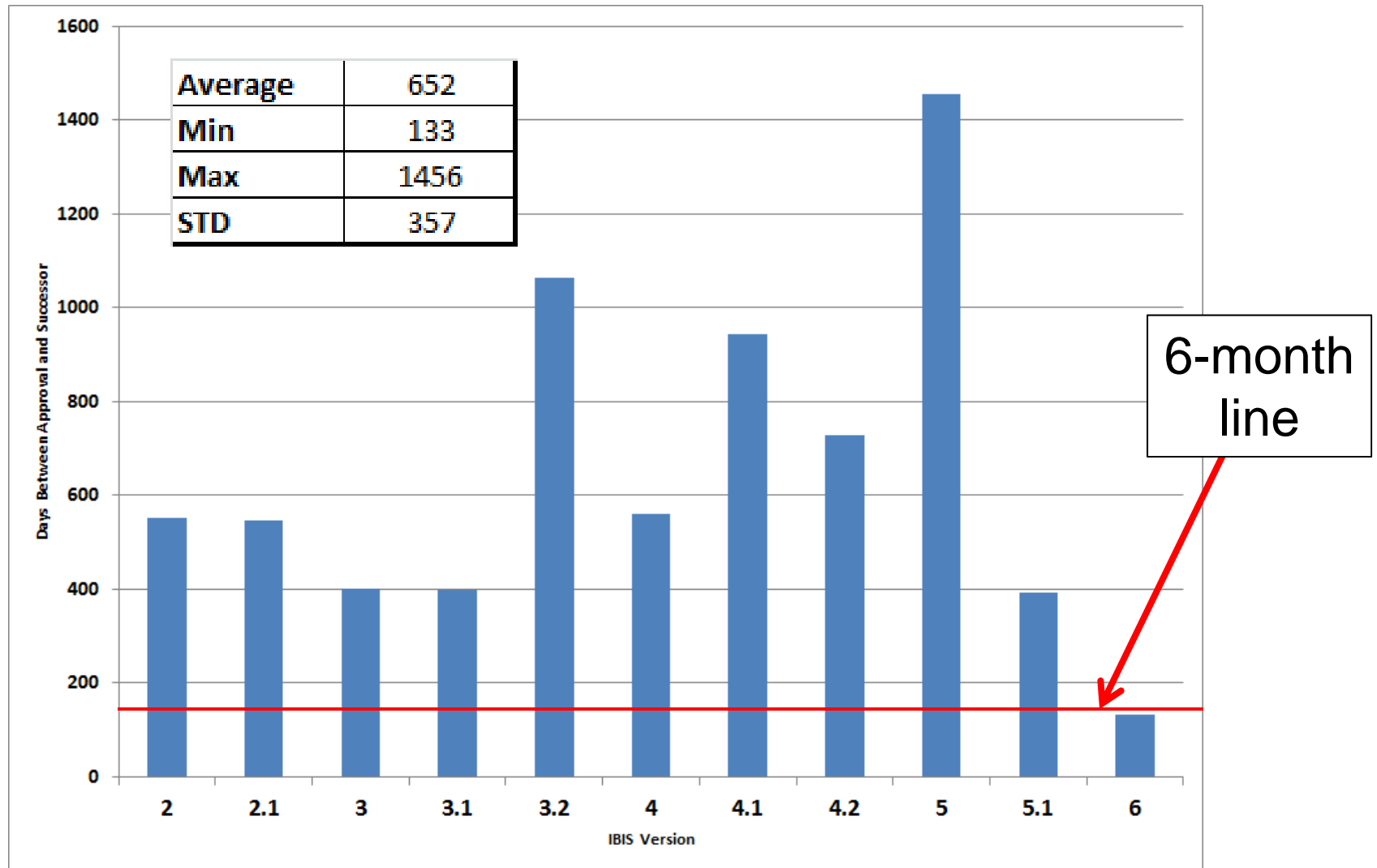
- ❑ 2013 Events and Progress
- ❑ IBIS Development Data
 - BIRD and Version Life Cycles
- ❑ Planning for the Future
 - Is a 6-month Development Schedule Feasible?
 - Proposal for Discussion
 - Focus for 1H'14

Events and Progress in 2013

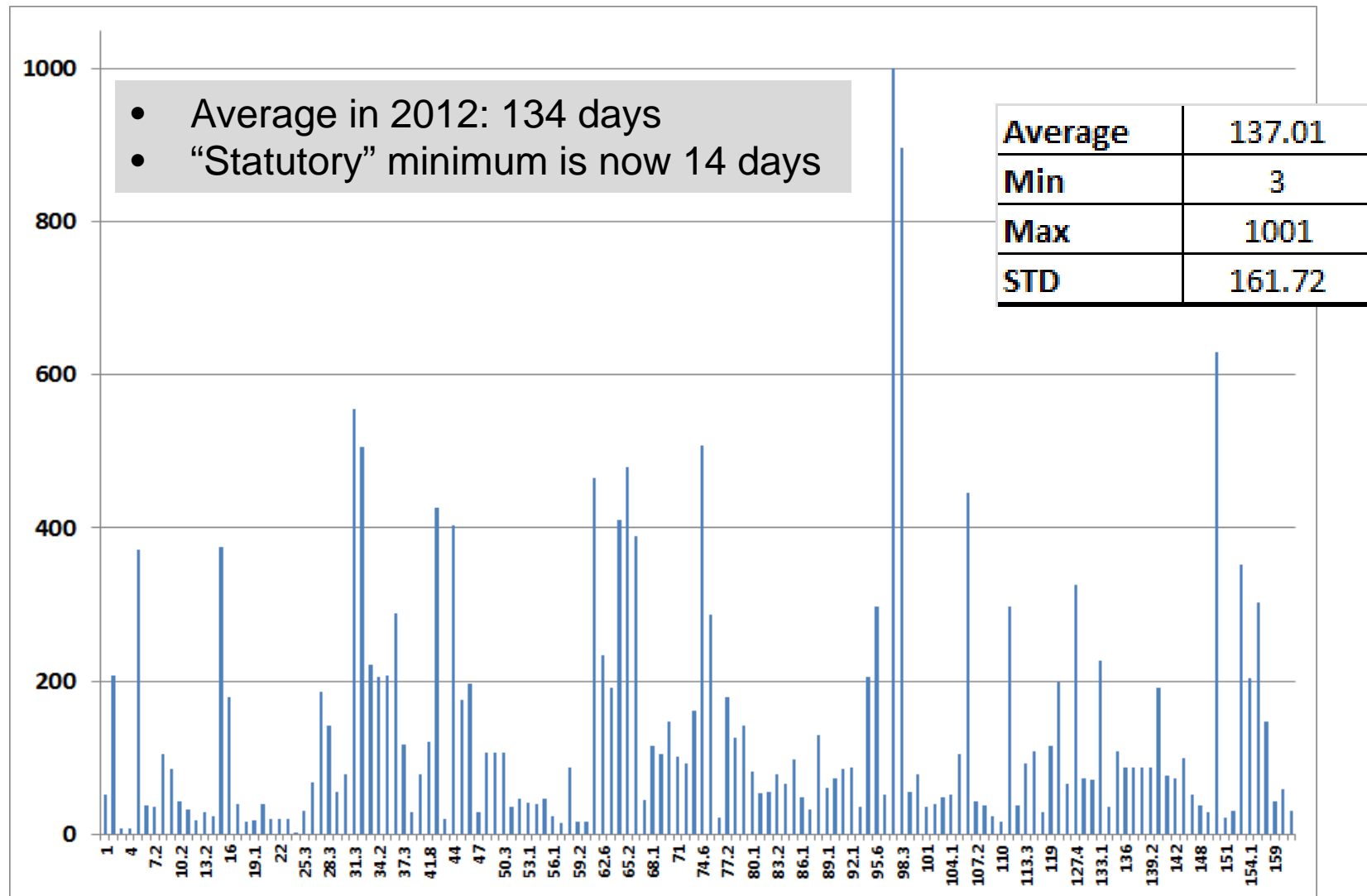
- ❑ IBIS has moved to a new “parent”: SAE, Inc.
- ❑ IBIS Ver. 6.0 approved Sept. 20, 2013
 - IBISCHK6 expected before June 2014
- ❑ 15 BIRDS or BIRD updates submitted
 - 7 approved, 10 rejected (includes BIRDS from 2010 and later)
- ❑ Asian and European Summits Continue
 - 280 attendees in Shanghai, Taipei, and Yokohama
 - IBIS in Europe continues with SPI-E 2013 in Paris

IBIS DEVELOPMENT DATA

How Long Do Versions “Live”?

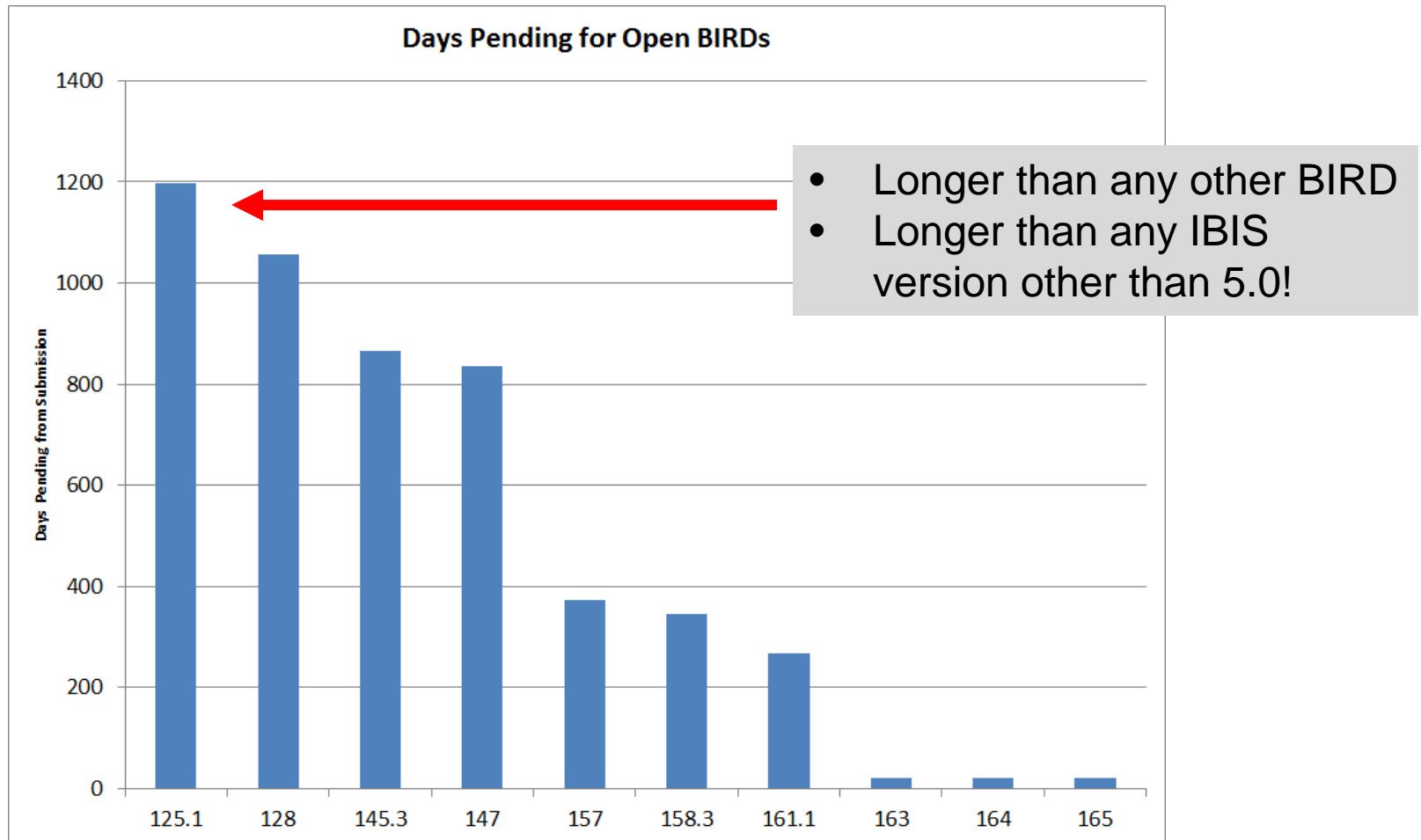


How Long are BIRDs Considered?



Pending BIRDs

- 10 BIRDs are open, mostly package-related



2014 IBIS Summit at DesignCon

PLANNING THE FUTURE

Is a 6-month Schedule Feasible?

- ❑ Recall the 6-month development schedule (see Backup)
 - 4 months of technical discussion
 - 2 months of editing
 - Approval votes in May and Nov. of each year
- ❑ To maintain this schedule, close all BIRDs by March 1
 - May 16th approval vote for next IBIS version
 - IBIS 6.0 has 50 days left on its 6-month “life”
 - Is this sensible with a ~6 month parser development schedule?
 - Is absorption time of IBIS by industry actually 1 year or more?
- ❑ *Which* version of IBIS is the next one?
 - IBIS 6.1 means new parser development would be free to licensees (expensive for Open Forum)
 - IBIS 7.0 means raising new fees from licensees
 - Date-based versioning is a proposed alternative

Proposal for Discussion

- ❑ Move to combined membership + parser contribution model in 2015
 - Membership fee would include an amount specifically for parser software development
- ❑ Move to new version numbering at same time
 - Remove distinction between major and minor releases based on features
 - Date-based version accommodates this
 - Standardization may take place at any time
- ❑ Proposal: make next version 6.1
 - Keywords/features in IBIS *per se* likely minimal
 - IBIS-ISS parsing becomes a focus

Looking Ahead



- ❑ Focus areas for 1H 2014
 - Resolve all packaging BIRDs and finalize packaging direction for IBIS as a modeling approach
- ❑ Return to IBIS-ISS parser
 - IBIS-ISS a focus under all today's package proposals
 - Call for parser bids ready but potentially stale
- ❑ Once IBIS updates are complete...
 - New charter and elections schedule under SAE

Thanks for another great year!

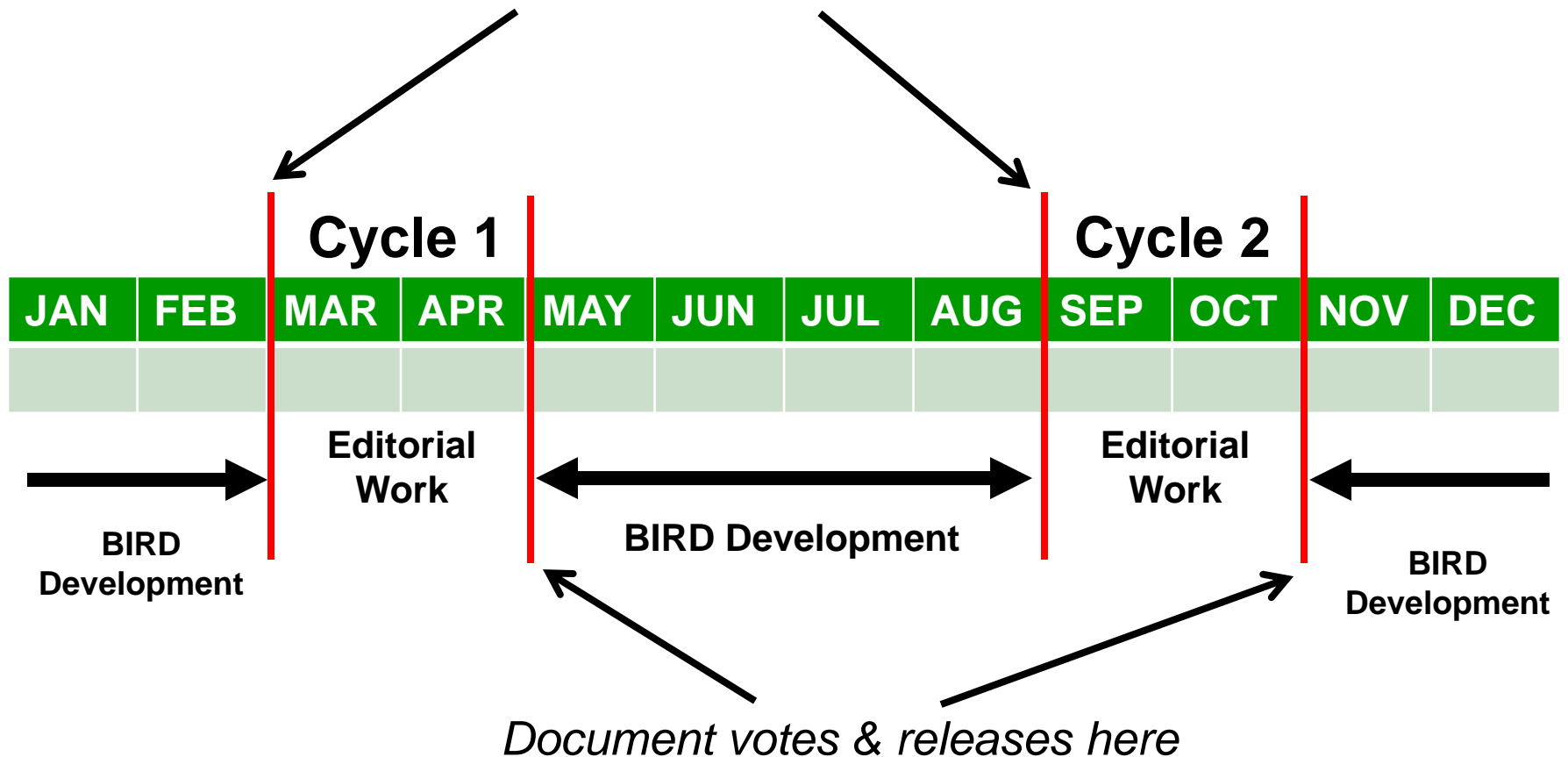
Backup

Release Schedule for IBIS

- ❑ Moving to a regular, software-like release plan
 - Allows better planning by “customers” (IBIS community)
 - Keeps number of changes manageable
 - Reduces & stabilizes parser development cycle time and cost
- ❑ Two “cycles”, each with two parts
 - Cycle 1
 - Nov. – Feb.: technical development
 - March 1 – close technical changes
 - May 1 – close final document (editorial fixes in-between)
 - Cycle 2
 - May – Aug.: technical development
 - Sept. 1 – close technical changes
 - Nov. 1 – close final document (editorial fixes in-between)

How Does This Work?

Parser development can start as early as this point



IBIS Post-6.0 BIRDs So Far

ID#	Issue Title	Requester	Submitted	Days Open
125.1	Make IBIS-ISS Available for IBIS Package Modeling	Arpad Muranyi, Mentor Graphics	21-Oct-10	1198
128	Allow AMI parameters out to pass AMI parameters in data on calls to AMI GetWave	Walter Katz, SiSoft	11-Mar-11	1057
145.3	Cascading IBIS I/O buffers with [External Circuit]s using the [Model Call] keyword	Taranjit Kukal, Ambrish Varma, Cadence Design Systems, Inc. Arpad Muranyi, Mentor Graphics.	19-Sep-11	865
147	Back-channel support	Marcus Van Ierssel, Snowbush IP; Kumar Keshavan, Sigrity, Inc.; Ken Willis, Sigrity, Inc.; Walter Katz, SiSoft	18-Oct-11	836
157	Parameterize [Driver Schedule]	Arpad Muranyi, Mentor Graphics; Romi Mayder, Xilinx	24-Jan-13	372
158.3	AMI Touchstonefile (R) Analog Buffer Models	Walter Katz, Signal Integrity Software, Inc.	20-Feb-13	345
161.1	Supporting Incomplete and Buffer-only [Component] Descriptions	Michael Mirmak, Intel Corp.	8-May-13	268
163	Instantiating and Connecting [External Circuit] Package Models with [Circuit Call]	Arpad Muranyi & John Angulo, Mentor Graphics; Ambrish Varma & Brad Brim, Cadence Design Systems, Inc.	9-Jan-14	22
164	Allowing Package Models to be defined in [External Circuit]	Ambrish Varma & Brad Brim, Cadence Design Systems, Inc.; Arpad Muranyi, Mentor Graphics	9-Jan-14	22
165	Parameter Passing Improvements for [External Circuit]s	Arpad Muranyi & John Angulo, Mentor Graphics; Ambrish Varma & Brad Brim, Cadence Design Systems, Inc	9-Jan-14	22

Q/A