**IBIS On-Site and Virtual Summit with DesignCon 2023**

February 3, 2023

**Agenda**

All times PST

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| 8:00 AM8:15 AM | **SIGN IN AND REFRESHMENTS** **Welcome**Randy Wolff (Micron Technology, USA)(Chair, IBIS Open Forum) |
| 8:20 AM | **IBIS Chair’s Report** Randy Wolff (Micron Technology, USA)(Chair, IBIS Open Forum) |
| 8:35 AM | **IBIS ATM Activity Report**  Arpad Muranyi (Siemens EDA, USA) |
| 8:45 AM | **Introducing IBIS 7.2** Michael Mirmak (Intel, USA) |
| 9:05 AM | **IBIS Quality Checklist Level 4 Additions**Weston Beal (Siemens EDA, USA) |
| 9:45 AM | **A Novel Simulation Flow for DDR5 Systems with Clocked Receivers** Matthew Leslie (Siemens EDA, USA)Justin Butterfield (Micron Technology, USA)Randy Wolff (Micron Technology, USA) [Presented by Matthew Leslie and Randy Wolff] |
| 10:25 AM | **BREAK (15 minutes)**  |
| 10:40 AM | **Rigorous Correlation Methodology for PCIe Gen5 & Gen6 DSP Based IBIS-AMI Models** Adrien Auge (Alphaware Semi, Canada) |
| 11:25 AM | **Conditional Control Proposal for IBIS and IBIS-ISS**Bob Ross (Teraspeed Labs, USA)Xuefeng Chen (Synopsys, PR China)[Presented by Bob Ross] |
| 11:45 PM | **IBIS: 30 Years and Counting, The Early History**Donald Telian (SI Guys, USA)Arpad Muranyi (Siemens EDA, USA)Will Hobbs (unaffiliated, USA)[Presented by Donald Telian, Arpad Muranyi, and Will Hobbs] |
| 12:15 PM | **IBIS 30 YEARS CELEBRATION** |
| 12:30 PM | **FREE LUNCH, NETWORKING** |
| 01:30 PM  | **Multi-Level Analog Buffer Modeling in IBIS**Arpad Muranyi (Siemens EDA, USA) |
| 01:45 PM | **IBIS-AMI modeling & simulation for PAM3 signaling in USB4 V2/Gen4 Systems**Fangyi Rao (Keysight Technologies, USA)Hongtao Zhang (AMD, USA)Geoff Zhang (AMD, USA)[Presented by Fangyi Rao] |
| 02:15 PM | **OPEN DISCUSSION** |
| 02:30 PM | **CLOSING REMARKS** - Next IBIS Open Forum Meeting Friday, February 17, 2023 |
|  | **END OF MEETING**  |
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