
IBIS SUMMIT MEETING AT DESIGNCON 2012

February 2, 2012

Great America 1

Santa Clara Convention Center

8:30 AM	Refreshments & Sign In
9:00 AM	Introductions - Welcome to Summit - Introductions - Opens for Issues, Discussion Topics
9:00 AM	Chair's Status Report Michael Mirmak, Intel Corporation
9:15 PM	IBIS-ATM Task Group Report Arpad Muranyi, Mentor Graphics Corp.
9:30 AM	A Tale of Two Parsers Bob Ross, Teraspeed Consulting Group
10:15 AM	BREAK
10:30 AM	Modeling On-Die Power Supply Decoupling Randy Wolff, Micron Technology Lance Wang, IO Methodology
11:00 AM	A System Developer's Perspective on AMI Greg Edlund, IBM
11:30 AM	Using Functional Programming Languages to IBIS AMI Modeling David Banas, Altera
12:00 PM	LUNCH Pre-registration required

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- 1:00 PM **Case Study: IBM 15 Gb IBIS-AMI Model using Dependency Tables**
Adge Hawes, IBM
Walter Katz, Signal Integrity Software
- 1:30 PM **Efficient End-to-end Simulations of 25G Optical Links**
Sanjeev Gupta, Avago
Fangyi Rao, Agilent Technologies
Jing-tao Liu, Agilent Technologies
Amolak Badesha, Avago
- 2:15 PM **How did we get here, and how should we go on?**
Arpad Muranyi, Mentor Graphics Corp.
- 2:45 PM BREAK
- 3:00 PM **Ad Hoc Discussion: BIRD116 and "Intrinsic Models"**
Walter Katz, Signal Integrity Software
- 3:45 PM Open Discussion and Ad Hoc Presentations
- 4:15 PM Concluding Items
- Next Open Forum Meeting: February 17, 2012
- 5:00 PM End of Summit