# C\_comp and Buffer Scaling Observations

Bob Ross
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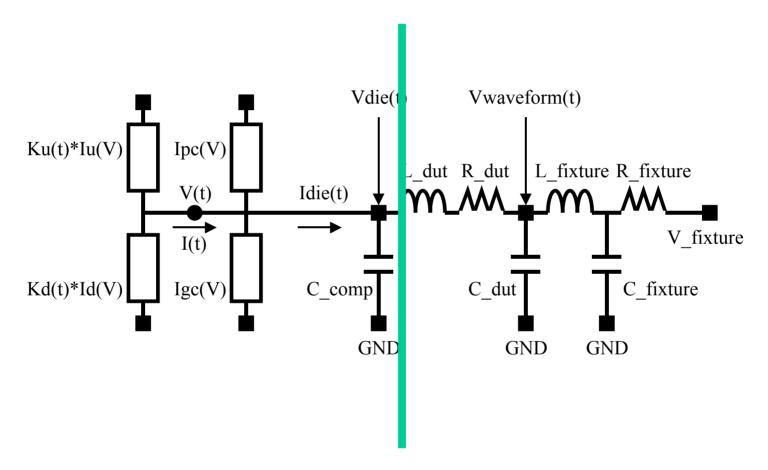


### **Agenda**

- Enhanced IBIS Version 2.1 model subset
- 2-tap pre-emphasis example and impedance interaction
  - Driver Schedule
  - Combining Buffers
- Ideal buffer test case
  - Problems
  - C\_fixture solution for peaking
- Conclusions/recommendations

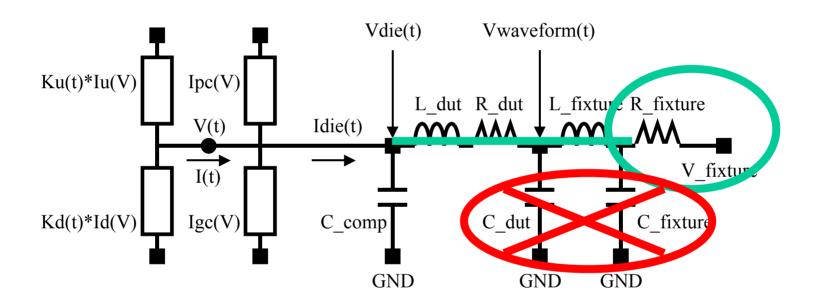


# Full IBIS Version 2.1 Buffer (C\_comp is part of buffer)





### Typical Industrial Subset of IBIS Version 2.1

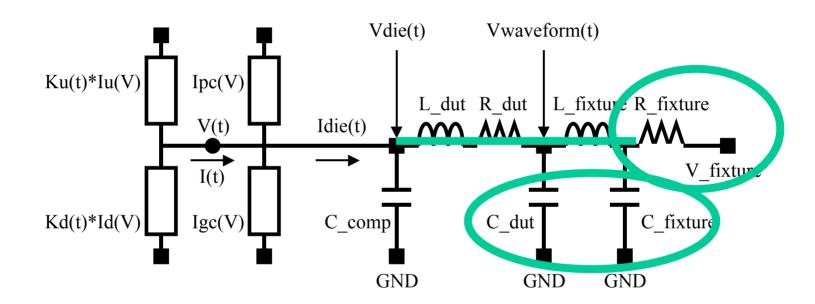


**R\_fixture**, **V\_fixture** and **C\_comp** 

Used for K-table extraction algorithm to develop internal driver model



### Proposed Enhanced Subset of IBIS Version 2.1

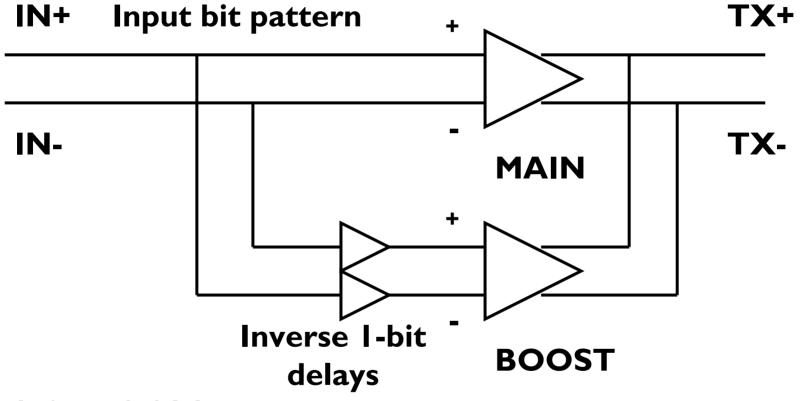


C\_comp\* = C\_comp + C\_dut + C\_fixture

K-table Extraction algorithm UNCHANGED



## 2 - Tap Pre-emphasis, Differential Control and [Driver Schedule]



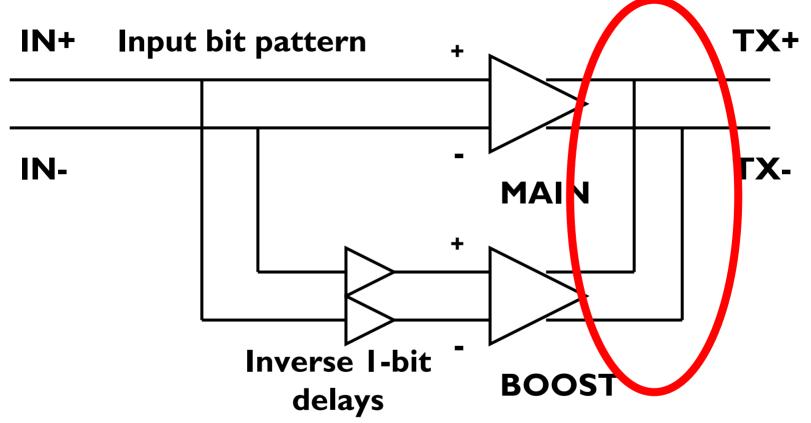
[Driver Schedule]

| Model\_name Rise\_on\_dly Rise\_off\_dly Fall\_on\_dly Fall\_off\_dly
MAIN 0 NA 0 NA

BOOST NA 0.47059ns NA 0.47059ns



Output Impedance and C\_comp Interaction??



[Driver Schedule]

| Model\_name Rise\_on\_dly Rise\_off\_dly Fall\_on\_dly Fall\_off\_dly

MAIN

0

NA

0

NA

BOOST

NA

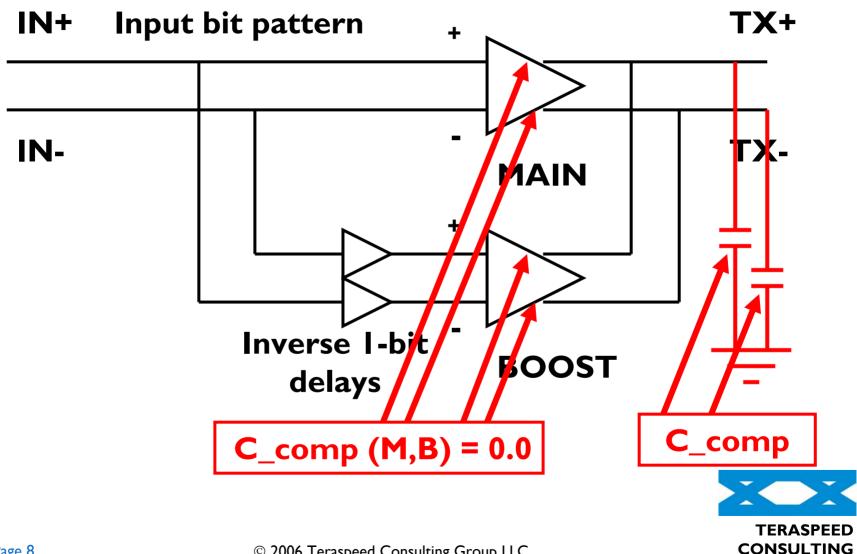
0.47059ns

NA

0.47059ns

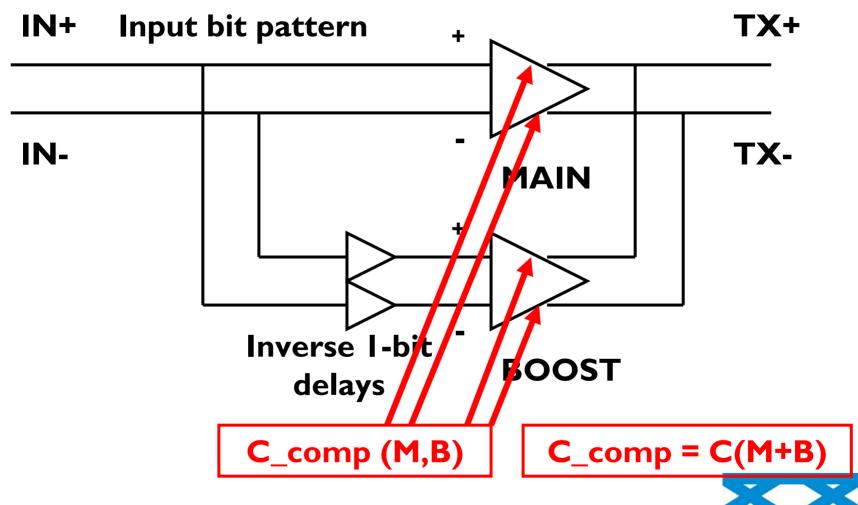


### IBIS Solution - C\_comp as Load with [Driver Schedule]



**GROUP** 

#### **Macro Model – Buffer Scaling Solution**

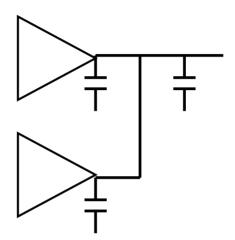


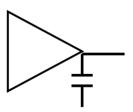
## **Shown by Ideal Buffer Test Case**

- Summation
  - 100 ps ideal ramp
  - 25  $\Omega$  ideal buffer (in connected mode)
  - $C_{comp} = 3.0 pF$
  - Vdd = 1.5 V



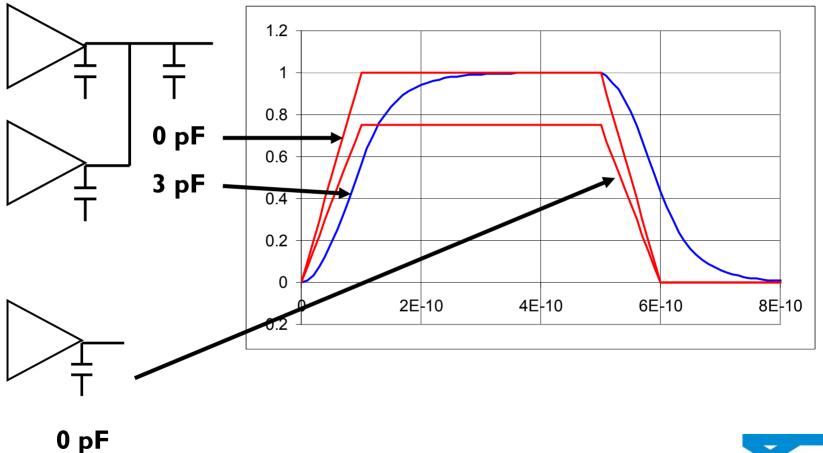
- 100 ps ideal ramp
- 50  $\Omega$  ideal buffer
- 50  $\Omega$  R fixture
- 50  $\Omega$  load





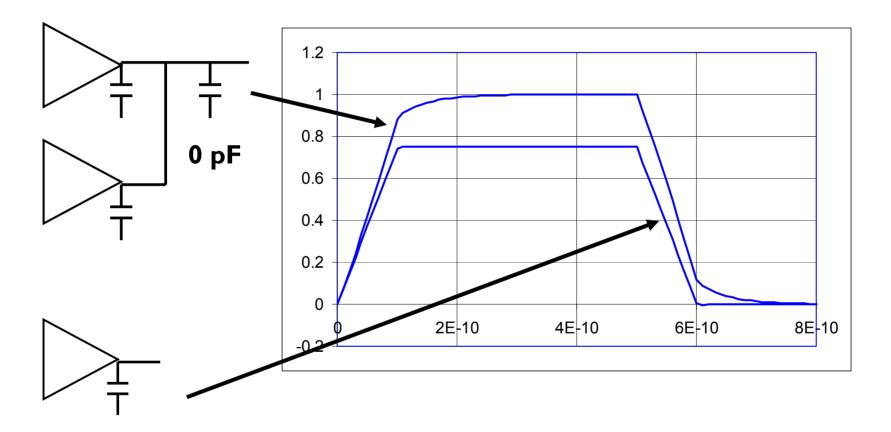


## IBIS [Driver Schedule] Setup with/without C\_comp = 3 pF





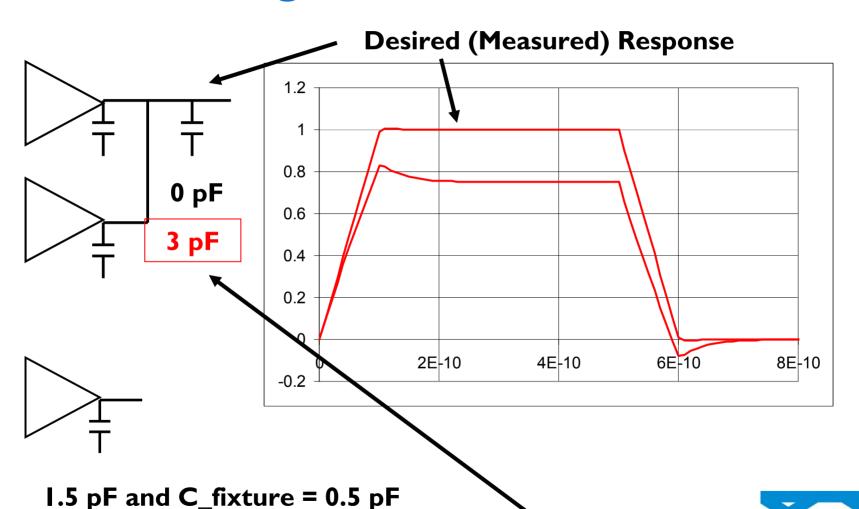
#### **Connected One-half Scaled Buffers**







### C\_fixture Peaking Works in All Cases with Original Scaled Waveforms



or 0 pF and C\_fixture = 2.0 pF for [Driver Schedule] setup



#### Conclusions/Recommendations

- Implement "enhanced" IBIS Version 2.1 subset with C\_fixture and C\_dut processing
  - No algorithm change
- Use a C\_fixture tweak for 2-tap pre-emphasis simulation ([Driver Schedule], language, macromodel, etc.) with "scaled" buffers
  - Independent tweaks for rising, falling edges possible
  - Experimentally adjust C\_fixture with real non-linear, non-ideal buffers
- Watch out for impedance interactions!!

