

DATE European IBIS Summit

April the 23th, 2009



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DATE 2009

Nice, France



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Agenda



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- 8:30 Refreshments & Sign In
 - 9:00 Welcome & Introductions
Ralf Bruening, Zuken, Germany
 - 9:10 Size Matters - Observations on Size and Complexity Development of IBIS Device Descriptions
Ralf BRUENING and Michael SCHAEDEER, Zuken, German
 - 9:40 Decoding IBISCHK: Static vs. Dynamic Waveforms
Eckhard LENSKE, Nokia Siemens Network GmbH, Germany
 - 10:00 BREAK (20 Minutes)
 - 10:20 The Touchstone Format For Interconnect Modeling
Manfred MAURER, Siemens AG, Germany
 - 11:00 Enhanced Mpilog Model for Power Integrity Analysis
Antonio GIRARDI*, Igor STIEVANO**, Roberto IZZI*, T. LESSIO*, Flavio CANAVERO**, Ivan MAIO**, and Luca RIGAZIO** - *Numonyx and **Politecnico di Torino, Italy
 - 11:55 The Use of Optimization in Signal Integrity Performance Centric High Speed Digital Design Flows
Saliou DIEYE, Agilent Technologies, France
 - 12:25 Experiences with ICEM Models for EMC/PI Simulations
Ralf BRUENING, Zuken, Germany
 - 12:45 Closing Remarks, Future of European IBIS meeting
Ralf BRUENING, Zuken, Germany
 - 13:00 END OF MEETING