

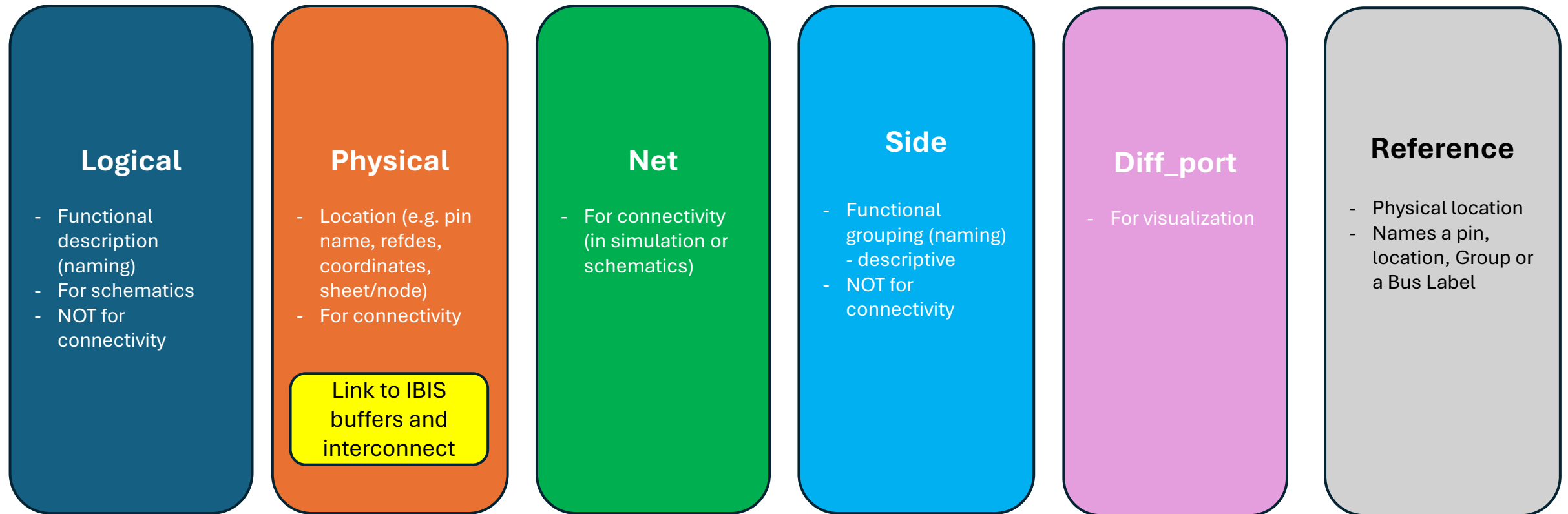
Port Mapping Summary

Michael Mirmak, Intel Corp.

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Public

Port Descriptors



These are not mutually exclusive

Examples

* Reference not used

Group used on 3a, 3b, 10, 11

Physical & Net

Logical

2: Transistor*

Physical

9: C_comp Model

1: Cable*

3a: Waveguide (pin)

3b: Waveguide (xyz)

7: EMD*

8: Ts4file*

10: EMD, Per-Interface Referencing

11: EMD, Group Wildcard Referencing

12: EMD, Per-Port Referencing

13: EMD, Pin-grouped Referencing

14: EMD, Rai Signal name referencing

15: EMD, A_gnd Referencing

16: EMD, A_gnd+ Referencing

17: IBIS Package (pad to pin)

18: IBIS on-die Interconnect

Physical, Net, Logical

4: IBIS Package
(pin to pad)

5: IBIS Package*
(buffer to pin)

6: PCB Trace*

Side, Type, and other optional parameters not shown

Additional Options and Parameters

Data_Usage Parameter – Entire Port Map	Meaning
IBIS_Interconnect	IBIS [Interconnect Model]
EMD	IBIS [EMD Model]
C_comp_model	IBIS [C Comp Model]
Ts4file	IBIS-AMI Touchstone file

- Parameters Available per Port Map
 - Source
 - Swathing
 - Group
 - Units
 - Symbol_*
 - UD
- Parameters Available Per Port Line
 - Type