

Power and Ground changes gives different Quiet-line simulation results

- Preliminary

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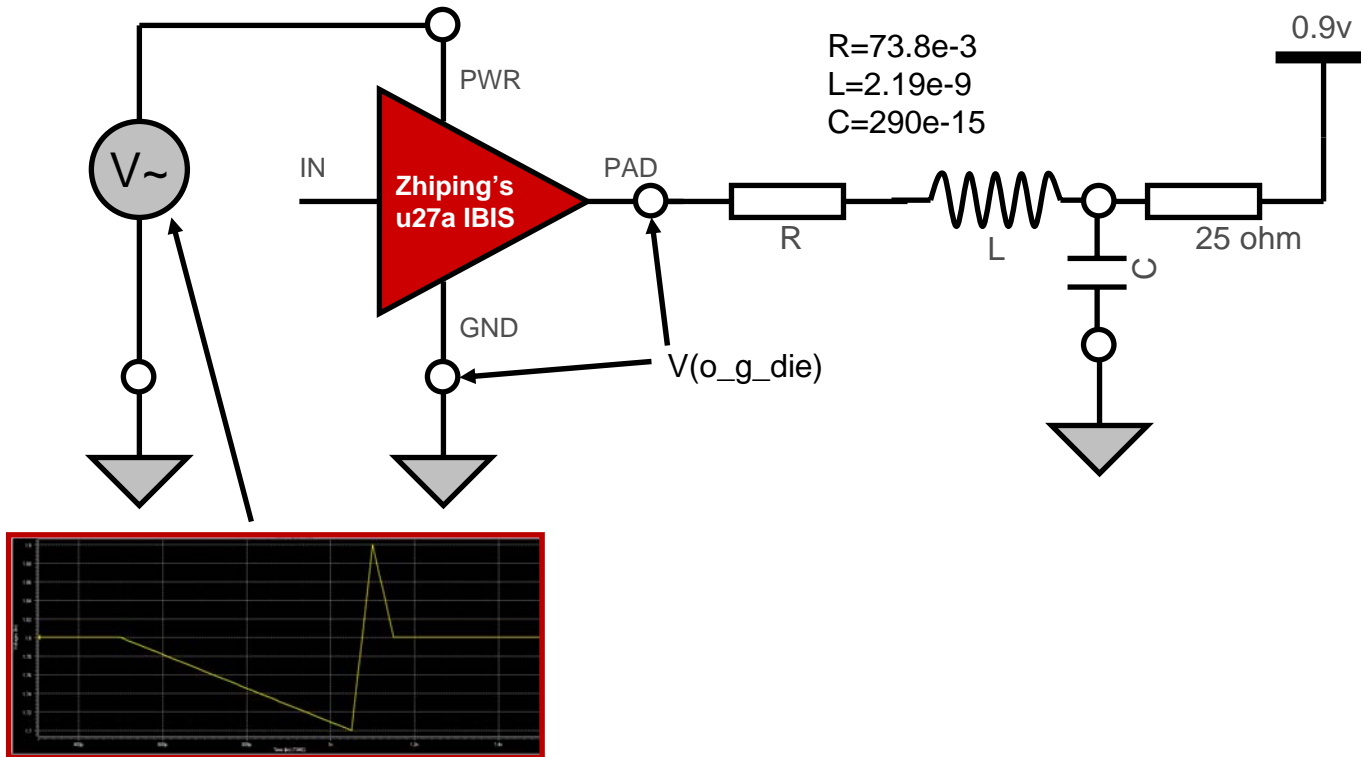
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Purpose and Setting

- Model Only Power changes and Only Ground changes, but Keep Power Supply ($V_{\text{pwr}} - V_{\text{gnd}}$) as the same in both cases.
- To get whether the results (voltage between buffer output and buffer ground) are the same.

Test Results

- Test Schematic – Power changes

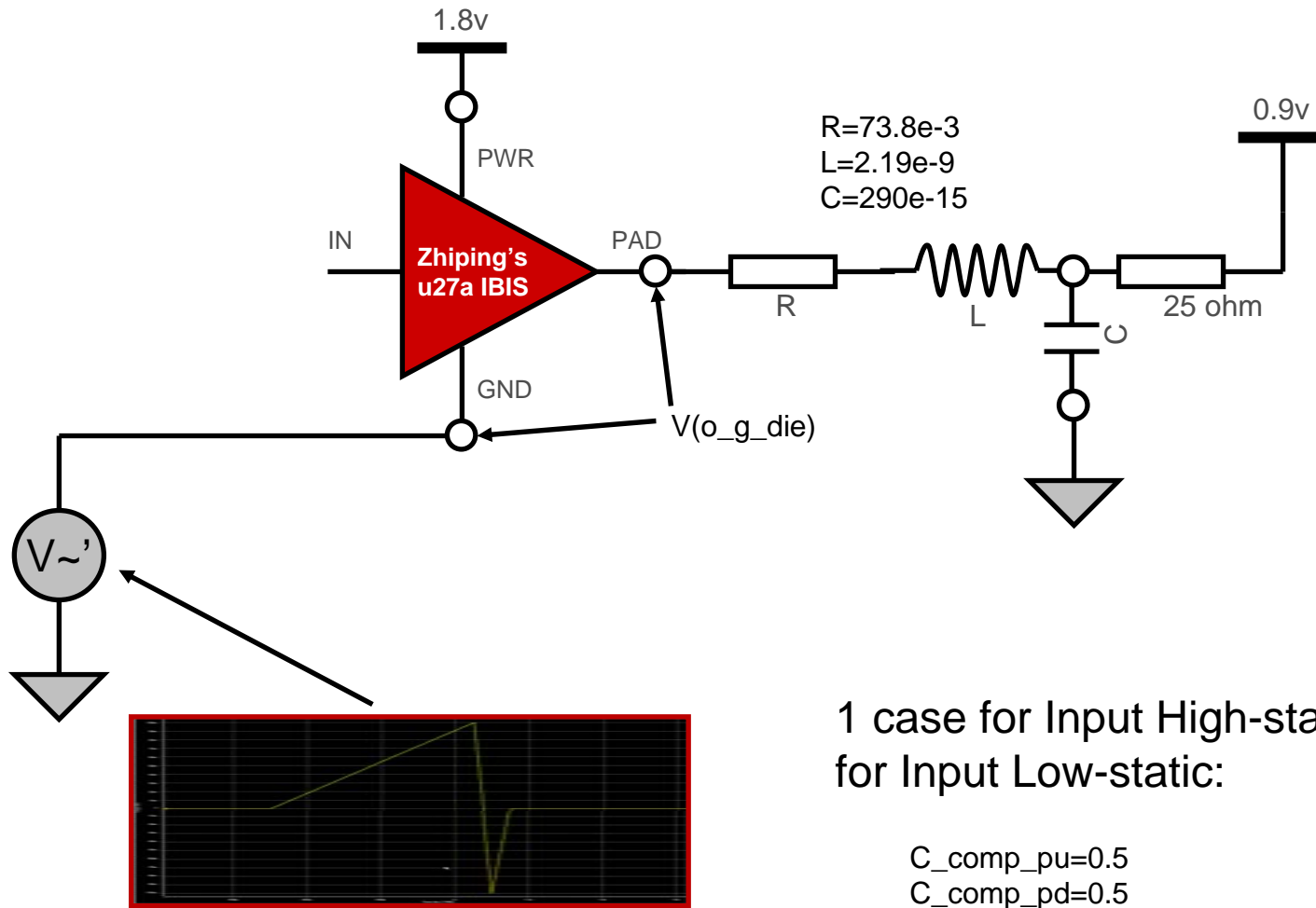


1 case for Input High-static and 1 case for Input Low-static:

$C_{comp_pu}=0.5$
 $C_{comp_pd}=0.5$

Test Results

- Test Schematic – Ground changes

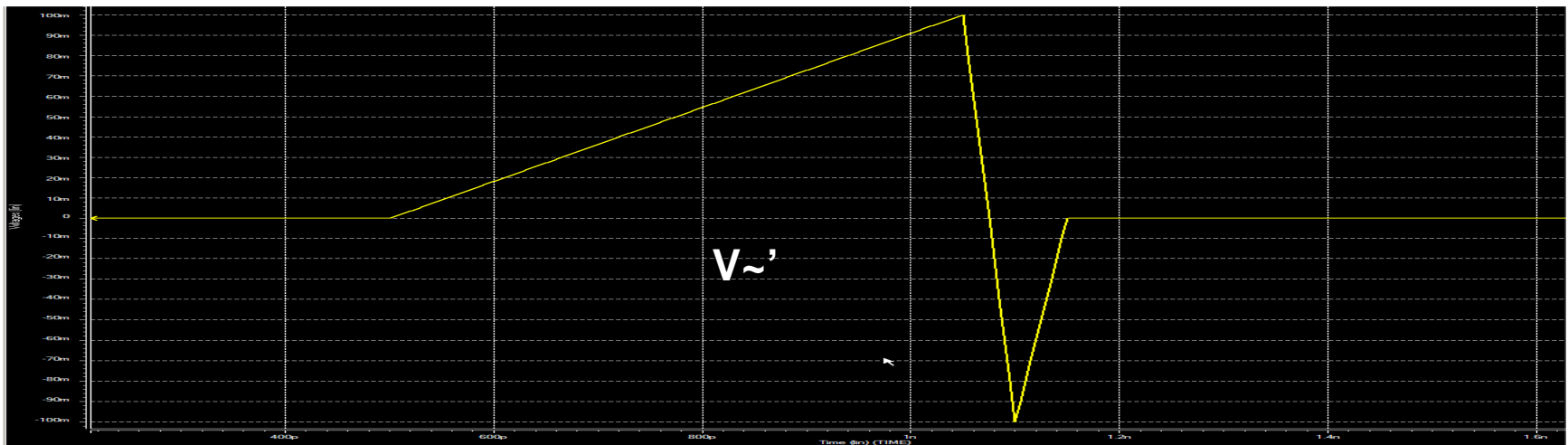
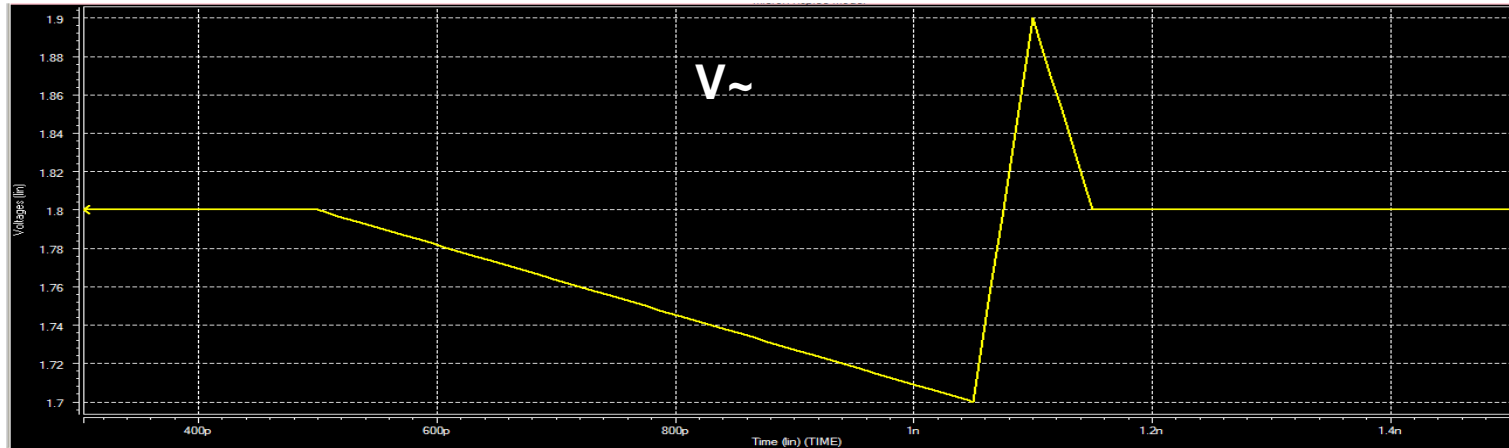


1 case for Input High-static and 1 case for Input Low-static:

$C_comp_pu=0.5$
 $C_comp_pd=0.5$

Test results

- V_{\sim} and V_{\sim}' Setting



Test Results

- Input High-Static



Test Results

- Input Low-Static

Come soon!!!