IBIS 6.2 BIRD Candidates (rev1)

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| **ID#** | **Issue Title** | **Requester** | **Date  Submitted** | **Date  Accepted** | **Supporting  Version** |
| A | Reserved name rule scoping clarifications (IBIS 6.1 Page 10) |  |  |  |  |
| B | I/V table reference terminal clarifications |  |  |  |  |
| C | Ground symbol clarifications (signal GND & local GND) |  |  |  |  |
| D | Require one model\_name per signal\_name in [Pin] (Bob Ross: fold this into BIRD 180) |  |  |  |  |
| E | Specify POWER/GND [Pin Mapping] by [Pin] signal\_name reference ([Bus Label]) |  |  |  |  |
| F | Specify buffer reference terminals for pins ([Pin Reference]) |  |  |  |  |
| G | Simulation (aka DIA) vs. DUT |  |  |  |  |
| H | “GND” clarifications (signal name vs. pin name, etc.) |  |  |  |  |
| I | C\_comp reference in simulation clarifications |  |  |  |  |
| J | AMI\_Version and [IBIS Ver] do not have to match |  |  |  |  |
| K | Known issues wording (Page 72, suggestions in [Bus Label] BIRD) | Bob Ross |  |  |  |
| L | Document “allowed arguments” of [Resistance Matrix] (They are not keywords or subparameters) |  |  |  |  |
| M | “Component attached to” fix (Page 79) |  |  |  |  |
| 180 | [Require Unique Pin Names in [Pin]](http://ibis.org/birds/bird180.docx) | Mike LaBonte, Signal Integrity Software | February 17, 2016 |  |  |
| 179 | [New IBIS-AMI Reserved Parameter Special\_Param\_Names](http://ibis.org/birds/bird179.docx) | Arpad Muranyi, Mentor Graphics | October 13, 2015 | December 18, 2015 |  |