**BUFFER ISSUE RESOLUTION DOCUMENT (BIRD)**

**BIRD NUMBER:** 225

**ISSUE TITLE:** Clarification for bus\_label rules

**REQUESTOR:**  Arpad Muranyi, Weston Beal, Randy Wolff, Siemens EDA

**DATE SUBMITTED:** July 11, 2023

**DATE REVISED:**

**DATE ACCEPTED:** September 15, 2023

**DEFINITION OF THE ISSUE:**

The error message E5810 issued by the IBIS parser states that: “IBIS 7.0 compatibility is required for IBIS files referenced by EMD”, but the IBIS specification doesn’t spell out such a requirement. Since the reason and intent of this error message is correct, we need to add this requirement to the IBIS specification.

**SOLUTION REQUIREMENTS:**

The IBIS specification must meet these requirements:

Table 1: Solution Requirements

|  |  |
| --- | --- |
| Requirement | Notes |
| 1. Define the missing requirement
 |  |

**SUMMARY OF PROPOSED CHANGES:**

For review purposes, the proposed changes are summarized as follows: Add a definition for the missing requirement.

**PROPOSED CHANGES:**

**Change the following paragraph on pg. 378 in IBIS 7.2 from:**

The fourth column (bus\_label) is optional for rail pins (signal\_type POWER or GND). The bus\_label entry is a name assigned to a subset of the pins with a rail signal\_name. As its name implies, bus\_label entries are not required to be unique for each row. However, all pins that have the same bus\_label must have the same signal\_name. If the bus\_label column is not specified for signal\_type POWER or GND, then the bus\_label shall be assumed to be the signal\_name.

**to:**

The fourth column (bus\_label) is optional for rail pins (signal\_type POWER or GND). The bus\_label entry is a name assigned to a subset of the pins with a rail signal\_name. As its name implies, bus\_label entries are not required to be unique for each row. However, all pins that have the same bus\_label must have the same signal\_name within EMD files as well as within any IBIS [Component] referenced in [EMD Parts]. If the bus\_label column is not specified for signal\_type POWER or GND, then the bus\_label shall be assumed to be the signal\_name.

**BACKGROUND INFORMATION/HISTORY:**

TBD