**BUFFER ISSUE RESOLUTION DOCUMENT (BIRD)**

**BIRD NUMBER:** 170

**ISSUE TITLE:** Delete Extra Paragraph for Ports under [External Circuit]

**REQUESTOR:**  Bob Ross, Teraspeed Consulting Group

**DATE SUBMITTED:** June 25, 2014

**DATE REVISED:**

**DATE ACCEPTED BY IBIS OPEN FORUM: August 1, 2014**

**STATEMENT OF THE ISSUE:**

In the IBIS Version 6.0 Specification, under “Ports” for [External Circuit], two paragraphs repeat the same sentences.

**ANALYSIS PATH/DATA THAT LED TO SPECIFICATION:**

The same verbage appears under Ports for [External Model] on page 100, but without the extra paragraph.

**ANY OTHER BACKGROUND INFORMATION:**

The second paragraph under Ports for [External Circuit] on page 121 repeats sentences within the previous paragraph. The second paragraph should be deleted:

Ports are interfaces to the [External Circuit] which are available to the user and tool at the IBIS level. They are used to connect the [External Circuit] to die pads. The Ports parameter is used to identify the ports of the [External Circuit] to the simulation tool. The port assignment is by position and the port names do not have to match exactly the names inside the external file. The list of port names may span several lines if the word Ports is used at the start of each line.

The Ports parameter is used to identify the ports of the [External Circuit] to the simulation tool. The port assignment is by position and the port names do not have to match exactly the port names in the external file. The list of port names may span several lines if the word Ports is used at the start of each line.