



Figure 9: Example \*-AMS implementation

- \* This signal is automatically created, by inverting and delaying D\_drive based on the information in [Diff Pin] (digital output will be based on evaluation of signals %% and %% also using [Diff Pin]).
- \*\* D\_receive for pseudo-differential buffers is determined by the state of A\_signal (Inverting) and A\_signal (Non-inverting) according to the [Diff Pin] declaration.
- \*\*\* D\_enable is shared between the separate buffers. This sharing is handled by the EDA tool.