### **GND BIRD**

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### Intent of This Presentation

I suspect that there will be an IBIS 6.2 which will be strictly limited to cleaning up Ground in IBIS. I put the following together as an introduction to this "BIRD". I do not know how this would be parsed into the IBIS document, but I wanted to first get an agreement as to the intent of the change, before going through IBIS to actually wordsmith the changes. I propose that we get agreement on this within IBIS-ATM, and then charter an editorial committee to go through IBIS 6.1, make all of the changes, and then submit the edited document to IBIS for approval as IBIS 6.2.



# IBIS Makes Implicit Assumptions About "Ground"

- GND, Ground, Reference Node, Node 0, A\_gnd and Absolute Ground need careful review and documentation.
- When IBIS was originally written "Ground" was often interpreted to be truly global, have a value of 0.0 Volts and represented as Node 0 in SPICE simulators. The name GND is actually used in several different contexts in this document.



### "must not be used" Needs Clarification

#### **3 GENERAL SYNTAX RULES AND GUIDELINES**

This section contains general syntax rules and guidelines for ASCII .ibs files:

1. The content of the files is case sensitive, except for reserved words and keywords.

2. The following words are reserved words and **must not be used** for any other purposes in the document: POWER - reserved model name, used with power supply pins

POWER GND NC

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- reserved model name, used with ground pins
- reserved model name, used with no-connect pins

NA

- used where data not available,

CIRCUITCALL - used for circuit call references in Section 6.3



#### GND is Often Used in the Context of Signal\_name GND in this Case is the Data Book Name

[Pin]	signal_na	me model_	model_name		_pin C_pin
1	RAS0#	Buffer1	200.0	m 5.0nH	2.0pF
2	RAS1#	Buffer2	209.0	m NA	2.5pF
3	EN1#	Input1	NA	6.3nH N	Ą
4	A0	3-state			
5	D0	I/O1			
6	RD#	Input2	310.0m	3.0nH 2	2.0pF
7	WR#	Input2			
8	A1	I/O2			
9	D1	I/O2			
10	GND	GND	297.0	m 6.7nH	3.4pF
11	RDY#	Input2			
12	GND	GND	270.0	m 5.3nH	4.0pF
$\left  \cdot \right $					
1.					
1.					
18	Vcc3	POWER			
19	NC	NC			
20	Vcc5	POWER		.0m NA	1.0pF
21	BAD1	Series_switch1   Illegal assignment			
22	BAD2	Series_se	lector1	Illegal a	assignment

We Are Signal Integrity

# IV Tables Reference Rail Voltages in Simulation

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- IV tables define the current contribution to the A\_signal terminal of an I/O buffer model. The voltage used to control these tables is the voltage between the A\_signal node of an I/O buffer and the A\_puref, A\_pcref, A\_pdref, and A\_gcref buffer model terminals.
- This should not be confused with the derivation method used to create the data in the IV tables which refer to GND, Ground, Absolute Ground, or static voltages reference to Test Fixture Ground.
- The use of the node name GND, the ground symbol ---, or names such as GND\_Clamp\_Reference, Power\_Clamp\_Reference, Pullup\_Reference, Pulldown\_Reference are voltages relative to the Test Fixture Ground.



### **Simulation Netlists**

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- SPICE (including AMS language) signal integrity simulations are preformed using netlists of connected interconnect models, power delivery models, I/O buffer models, and simulator dependent control elements.
- The IBIS organization defines standards for distributing interconnect models and I/O buffer models.
- Ultimately these models are instantiated in simulation netlists as instances of SPICE or AMS elements that have terminals.
- Terminals that have the same name are "connected" and have the same voltage potential, and are called a node.
- The operation of any interconnect model or I/O buffer models is a function of the voltage potential difference between the nodes of the terminals of that model (and the current flowing into the terminal.



### **Simulator Reference Node**

 A simulator may (and usually does) have a concept of a reference node (often referred to as Node 0, Absolute Ground, or GND), the I/O buffer or interconnect elements should not use this node and certainly should not supply current to or draw current from this internal simulator reference node. This node 0 should not be confused with the use of the name GND in this IBIS document.



# C\_comp is Connected to ?

- IBIS 1.0 says nothing about how to connect C\_comp.
- In many locations in IBIS 6.0 C\_comp is connected to GND, GND Symbol, Test Fixture GND, GND\_Clamp\_Reference, Pulldown\_Reference.
- There has been an implication that therefore simulators should connect C\_comp to the simulator reference ground (e.g. Node 0)
- This implication is incorrect, it is an interpretation based on the text used to derive the IV curves.
- IBIS 6.2 should explicitly say that C\_comp should be connected to the buffer local ground rail.

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