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**ASIAN IBIS SUMMIT (TAIPEI) AGENDA**

Time/Date: 8:00 - 16:30, Friday, November 13, 2015

**Sponsors: IO Methodology**

**Keysight Technologies**

**Synopsys**

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**8:15 SIGN IN**

- Vendor Tables Open at 8:30

**9:00 WELCOME**

- Mike LaBonte (Chair, IBIS Open Forum)

(Signal Integrity Software (SiSoft), USA)

**9:10 IBIS Chair's Report**

Mike LaBonte (Signal Integrity Software (SiSoft), USA)

**9:35 Introducing IBIS Version 6.1**

Michael Mirmak (Intel Corporation, USA)

[Presented by Mike LaBonte

(Signal Integrity Software (SiSoft), USA)

**9:55 Enabling Full Power-aware Bus Simulation with Non-IBIS**

**Device Model - A Kit Using IBIS [External Model]**

Skipper Liang (Cadence Design Systems, ROC)

**10:40 BREAK** (Refreshments and Vendor Tables)

**11:00 A Practical DOE Application in Statistical SI Analysis Using**

**IBIS & How Can We Make IBIS Work Beyond Best Case/Worst Case?**

Feng Shi\*, Anders Ekholm\*\*, Zilwan Mahmod\*\*, and

David Zhang\* (Ericsson, \*PRC, \*\*Sweden)

[Presented by Feng Shi (Ericsson, PRC)]

**11:30 IBIS Interconnect BIRD Update**

Katz, Walter (Signal Integrity Software (SiSoft), USA)

[Presented by Mike LaBonte

(Signal Integrity Software (SiSoft), USA)

**12:00 FREE BUFFET LUNCH** (Hosted by Sponsors)

- Vendor Tables

**13:30 PAM4 System Simulation Using AMI Models**

Rao, FangYi (Keysight Technologies, USA)

[Presented by Ming-Chih Lin (Keysight Technologies, ROC)]

**14:00 Some Results for General K-table Extraction Proposal**

**Using SPICE**

Bob Ross\* and XueFeng Chen\*\*

(\*Teraspeed Labs, USA, \*\*Synopsys, PRC)

[Presented by Lance Wang (IO Methodology, USA)]

**14:30 IBIS Simulation Case Study: Unexpected Glitch and Using**

**C\_fixture**

Lance Wang (IO Methodology, USA)

**15:00 BREAK**

**15:20 Laplace Transform Time Response Utility**

Bob Ross (Teraspeed Labs, USA)

[Presented by Anders Ekholm (Ericsson, Sweden)]

**15:50 DISCUSSION**

**16:20 CONCLUDING ITEMS**

**16:30 END OF IBIS SUMMIT MEETING**

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