**Virtual IBIS Summit**

**IEEE SPI 2022 Workshop**

**May 26, 2022**

**Agenda**

All times CEST

|  |  |
| --- | --- |
| 16:0016:10 | **SIGN IN****Welcome**Randy Wolff (Micron Technology, USA)(Chair, IBIS Open Forum) |
| 16:15 | [**IBIS Chair’s Report**](https://ibis.org/summits/may22/wolff.pdf) Randy Wolff (Micron Technology, USA)(Chair, IBIS Open Forum) |
| 16:30 | [**Circuit Synthesis of Multiport Networks from Passive Poles and Residues**](https://ibis.org/summits/may22/chou.pdf)Chiu-Chih Chou\*, Jose Schutt-Aine\*\*(National Central University\*, ROC; University of Illinois\*\*, USA)[Presented by Jose Schutt-Aine\*\*, Chiu-Chih Chou\* (National Central University\*, ROC; University of Illinois\*\*, USA)] |
| 17:00 | [**Fast Simulation of Analog Circuit Blocks under Nonstationary Operating Conditions via Reduced Order Equivalent Circuits**](https://ibis.org/summits/may22/bradde.pdf)Tommaso Bradde, Alessandro Zanco, Stefano Grivet-Talocia(Politecnico di Torino, Italy)[Presented by Tommaso Bradde (Politecnico di Torino, Italy)] |
| 17:30 | [**Low-Frequency Modal Extrapolation and Regularization for Full-Bandwidth Macromodeling of Electromagnetic Structures**](https://ibis.org/summits/may22/de_stefano.pdf)Marco De Stefano\*, Stefano Grivet-Talocia\*, T. Wendt\*\*, C. Yang\*, Christian Schuster\*\*(Politecnico di Torino\*, Italy; Hamburg University of Technology (TUHH)\*\*, Germany)[Presented by Marco De Stefano (Politecnico di Torino, Italy)] |
| 17:50 | [**K.T. Wang (Wang Algebra) - Expanded History**](https://ibis.org/summits/may22/ross.pdf)Bob Ross (Teraspeed Labs, USA) |
| 18:05 | **BREAK (10 minutes)**  |
| 18:15 | [**Bathtub Extrapolation of IBIS-AMI Timing Jitter**](https://ibis.org/summits/may22/bai.pdf)Longfei Bai (Dassault Systemes, Germany) |
| 18:40 | [**IBIS Power Current Prediction with Overclocking**](https://ibis.org/summits/may22/viscardi.pdf)Aniello Viscardi\*, Xuefeng Chen\*\*(Micron Technology\*, Italy; Synopsys\*, PRC)[Presented by Aniello Viscardi (Micron Technology, Italy)] |
| 19:10 | [**Pole-Residue in Touchstone**](https://ibis.org/summits/may22/muranyi.pdf)Arpad Muranyi\*, Bob Ross\*\* (Siemens EDA\*, USA, Teraspeed Labs\*, USA)[Presented by Arpad Muranyi (Siemens EDA, USA)] |
| 19:30 | **Discussion** |
| 19:55 | **CLOSING REMARKS** - Next IBIS Open Forum Meeting Friday, June 3, 2022 |
| 20:00 | **END OF SUMMIT**  |

