**AGENDA, EUROPEAN IBIS SUMMIT MEETING**

 Wednesday, May 15, 2013

 Hyatt Regency Paris Etoile

 (NOTE: formerly Hotel Concorde La Fayette)

 Room: Pissaro-Cezanne (Check at Entrance)

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**13:00 SIGN IN**

**13:15 WELCOME AND INTRODUCTIONS**

 Lance WANG, IO Methodology, IBIS Vice Chair, USA

**13:30 IBIS Modeling for Load Dependent Current Mode**

 **Differential Drivers**

 Lance WANG, IO Methodology, USA

**13:50 X2IBIS: Using X-Parameters to Generate IBIS Models**

 Tom COMBERIATE and Jose SCHUTT-AINE,

 University of Illinois, USA

 (Presented by Thomas COMBERIATE, USA)

**14:30 Thevenin's Theorem Revisited. A New Approach to IC Buffer**

 **Modeling and Its Relation to IBIS**

 Igor STIEVANO\*, Cherif DIOUF\*\*, Mihai TELESCU\*\*,

 N. TANGUY\*\*, and Flavio CANAVERO\*,

 \*Politecnico di Torino, Italy;

 \*\*Université Européenne de Bretagne, Université de Brest,

 France

 (Presented by Mihai TELESCU, France)

**15:00 A Novel Two-Port Behavioral Model for I/O Buffer**

 **Overclocking Simulation**

 Wael DGHAIS,

 Universidade de Aveiro, Instituto de Telecommcações,

 Portugal

**15:30 BREAK (15 Minutes)**

**AGENDA, EUROPEAN IBIS SUMMIT MEETING (CONTINUED)**

**15:45 Novel Extraction of Table-Based I-Q Behavioral Model for**

 **High-Speed Digital Buffers/Drivers**

 Wael DGHAIS,

 Universidade de Aveiro, Instituto de Telecommcações,

 Portugal

**16:15 Interconnect Modeling Update - EMD Specification**

 Randy WOLFF, Micron Technology, USA

**16:45 Ibischk5 Specification and Parser**

 Bob ROSS\* and Mike LABONTE\*\*,

 \*Teraspeed Consulting Group and

 \*\*Signal Integrity Software (SiSoft), USA

 (Presented by Anders EKHOLM, Ericsson, Sweden)

**17:30 DISCUSSION AND CLOSING REMARKS**

 Lance WANG, IO Methodology, USA

**18:00 END OF MEETING**

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